

Scalable Multiparty Garbling

Gabrielle Beck

Aarushi Goel

Aditya Hegde

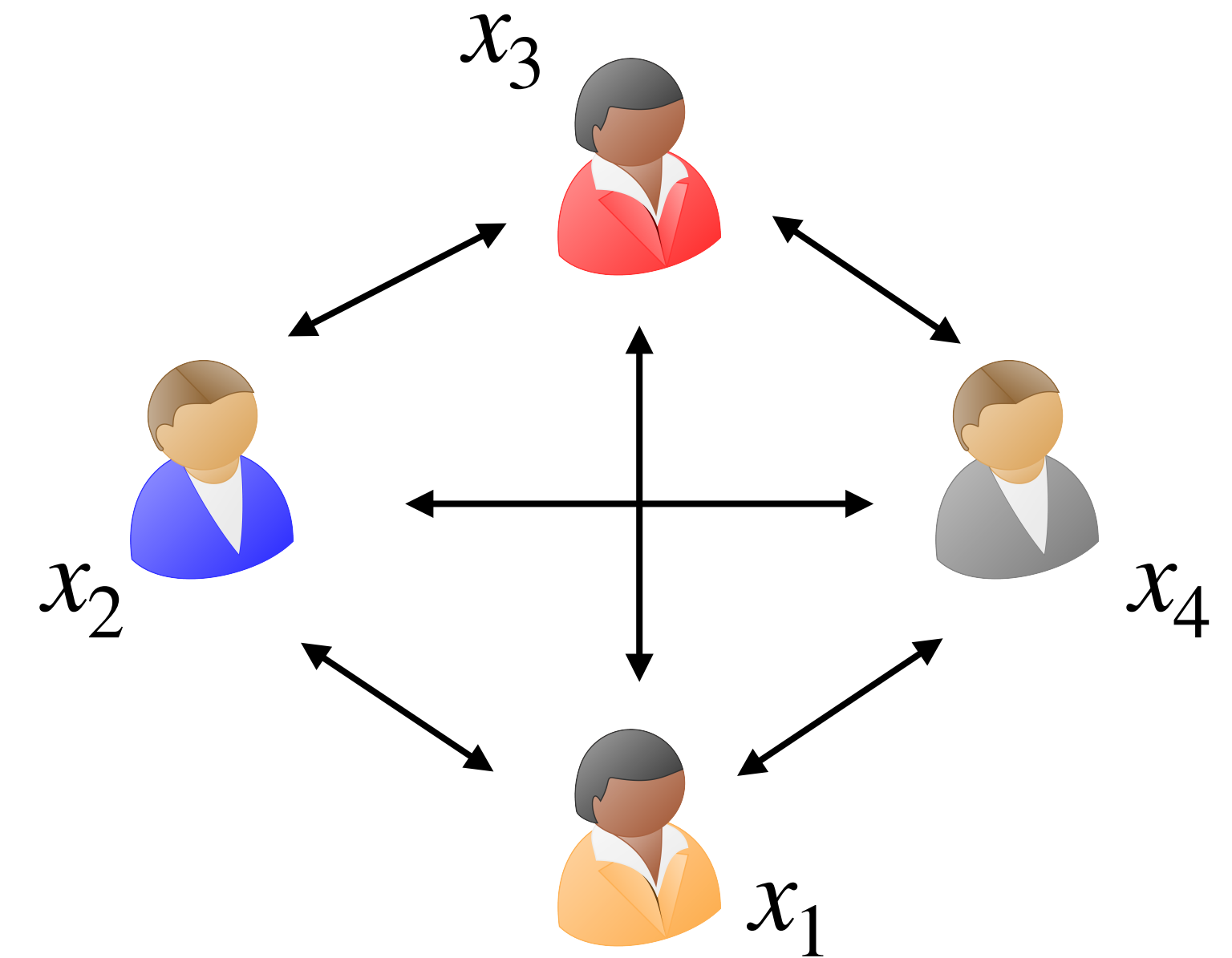
Abhishek Jain

Zhengzhong Jin

Gabriel Kaptchuk

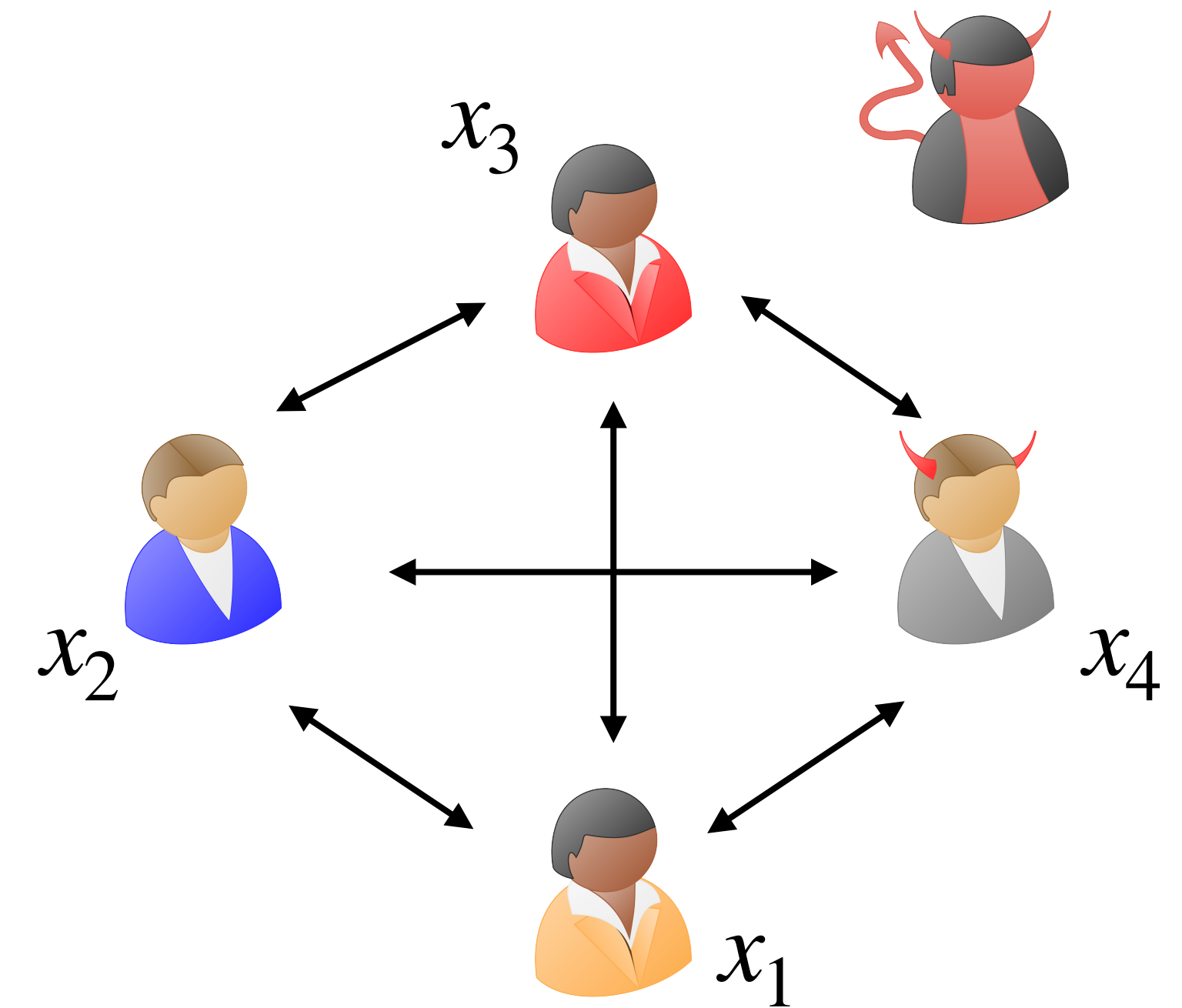


Secure Multiparty Computation



$$y = f(x_1, x_2, x_3, x_4)$$

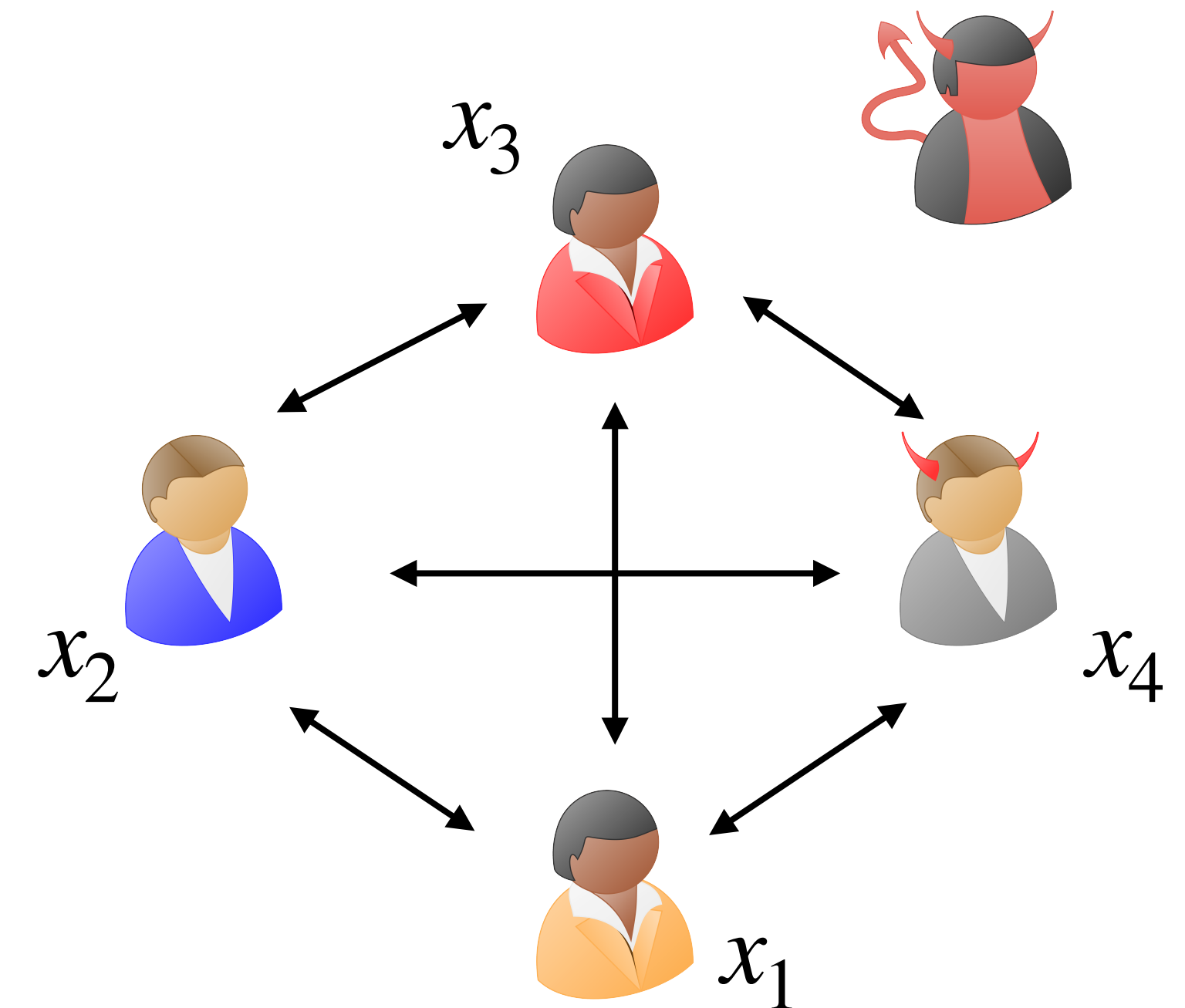
Secure Multiparty Computation



$$y = f(x_1, x_2, x_3, x_4)$$

Secure Multiparty Computation

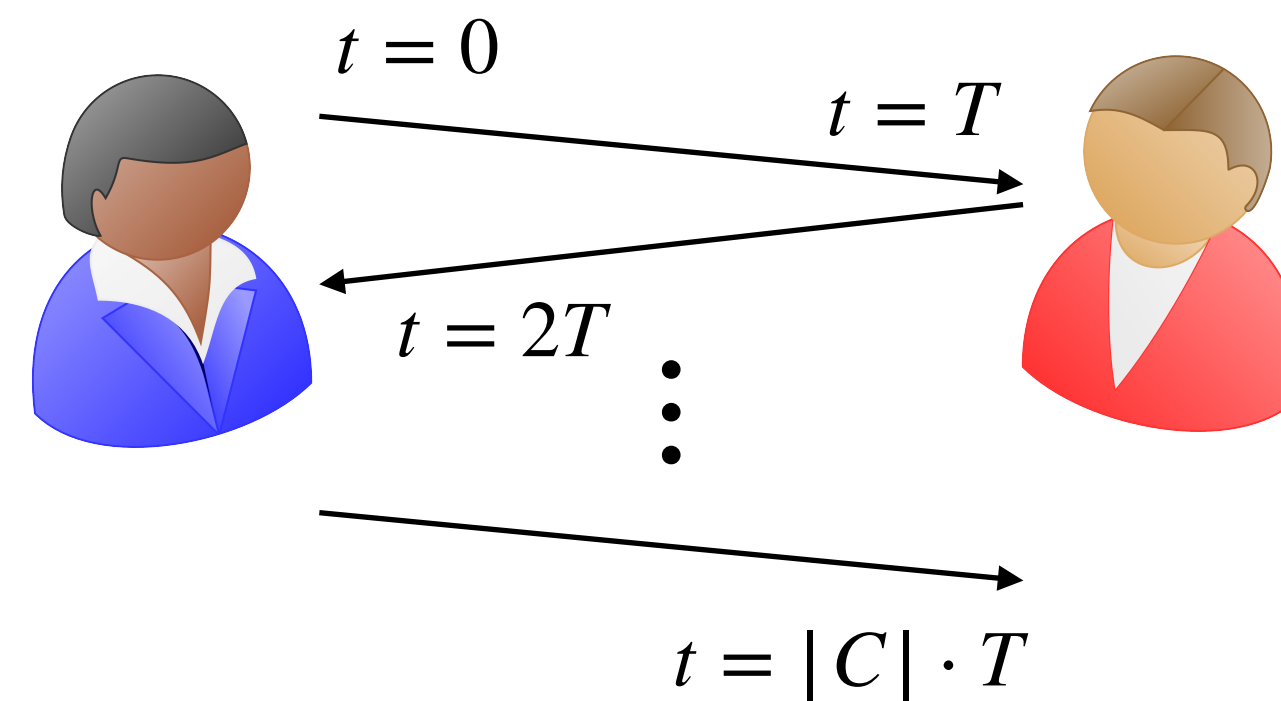
- Applications
 - Machine learning on distributed datasets.
 - Data as a service: secure access to shared and private data.
 - Securing digital assets and key management.



$$y = f(x_1, x_2, x_3, x_4)$$

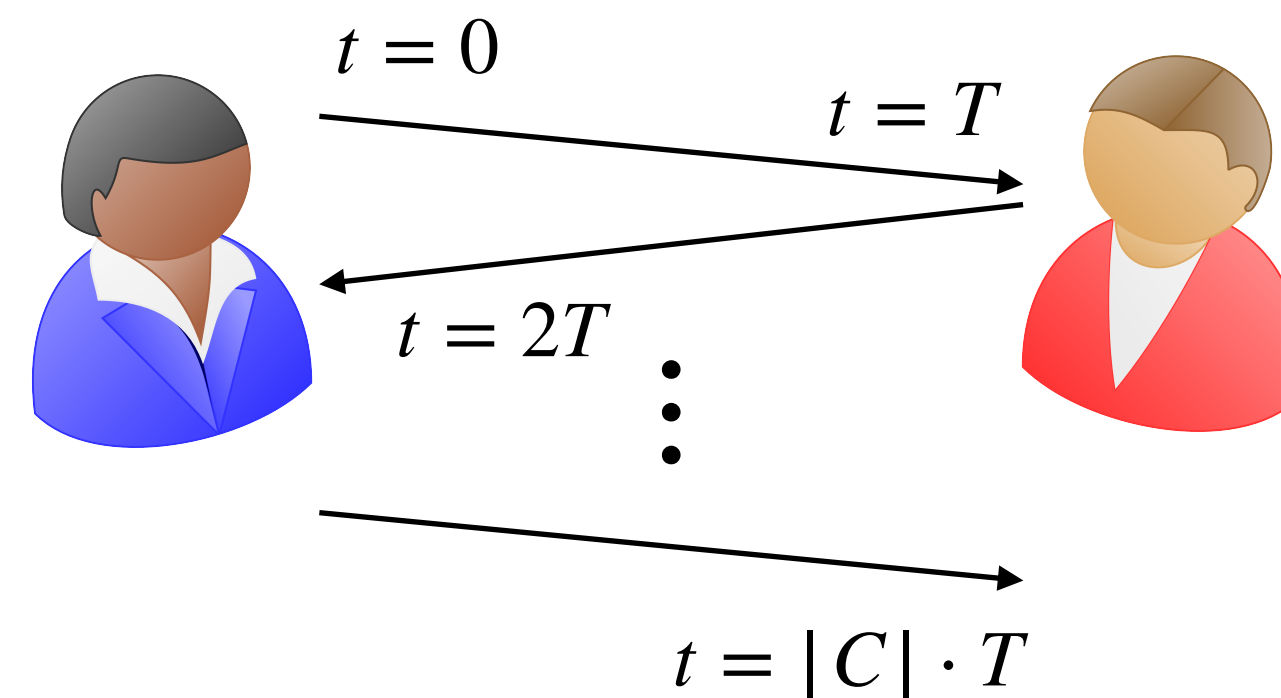
MPC Over The Internet

- Over the internet, **network latency** limits protocol runtime [WRK17b].



MPC Over The Internet

- Over the internet, **network latency** limits protocol runtime [WRK17b].
- **Constant round MPC**: Parties interact constant number of times.
 - Supports evaluating **large-depth circuits** over the internet.
 - 2 Approaches: FHE and **Multiparty Garbling**.



Constant Round MPC

- Best known constant round MPC has a total **communication cost** of

$$O(n^2 \cdot |C|)$$

[HSS17, WRK17b, BCOOSS21].

Number of parties

Size of circuit

- In the honest majority setting, **non-constant round** MPC with a total communication cost of

$$O(|C|)$$

[BGJK21, GSY21, GPS21, GPS22].

Problem: Bringing The Same Efficiency To Multiparty Garbling

Does there exist a multiparty garbling protocol with $O(|C|)$ communication in the honest majority setting?

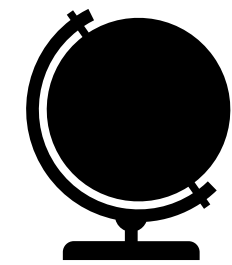
MPC Protocols With $O(|C|)$ Communication

 Per party communication **decreases** as the number of parties **increases**.

MPC Protocols With $O(|C|)$ Communication



Per party communication **decreases** as the number of parties **increases**.

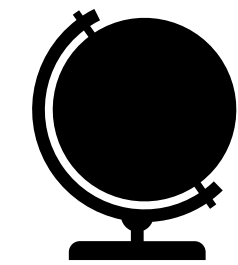


MPC that **scales** to hundreds and thousands of parties.

MPC Protocols With $O(|C|)$ Communication



Per party communication **decreases** as the number of parties **increases**.



MPC that **scales** to hundreds and thousands of parties.



Honest majority is a more **plausible** assumption.

Our Contributions

- Scalable **multiparty garbling**
- $O(|C|)$ communication complexity
- **Malicious** security with abort
- $t < n \left(\frac{1}{2} - \epsilon \right)$
- Based on the **LPN** assumption over large fields
- Practical: **~700 MB** for garbling AES-128 with **250 parties**.

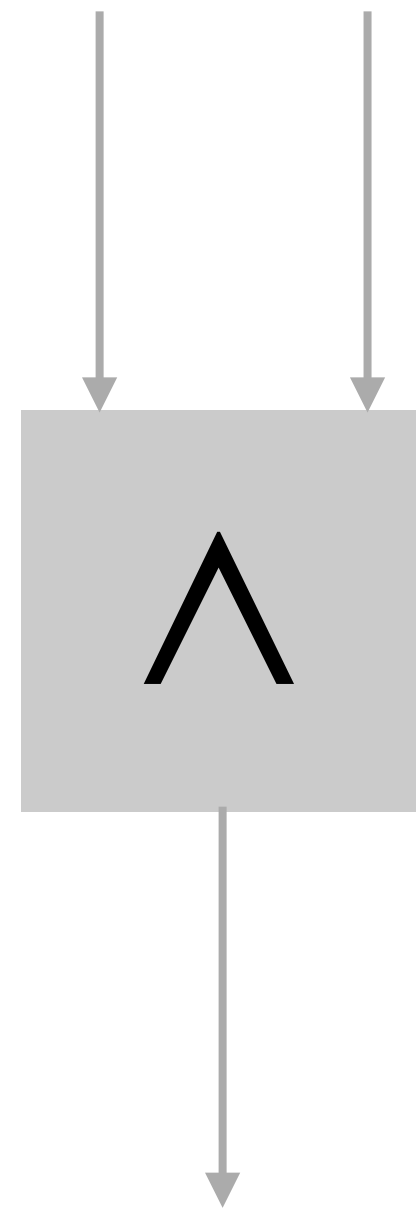
Agenda

- Structure Of Garbled Circuit
- Multiparty Garbling Protocol
- Efficiency Analysis

Agenda

- Structure Of Garbled Circuit
- Multiparty Garbling Protocol
- Efficiency Analysis

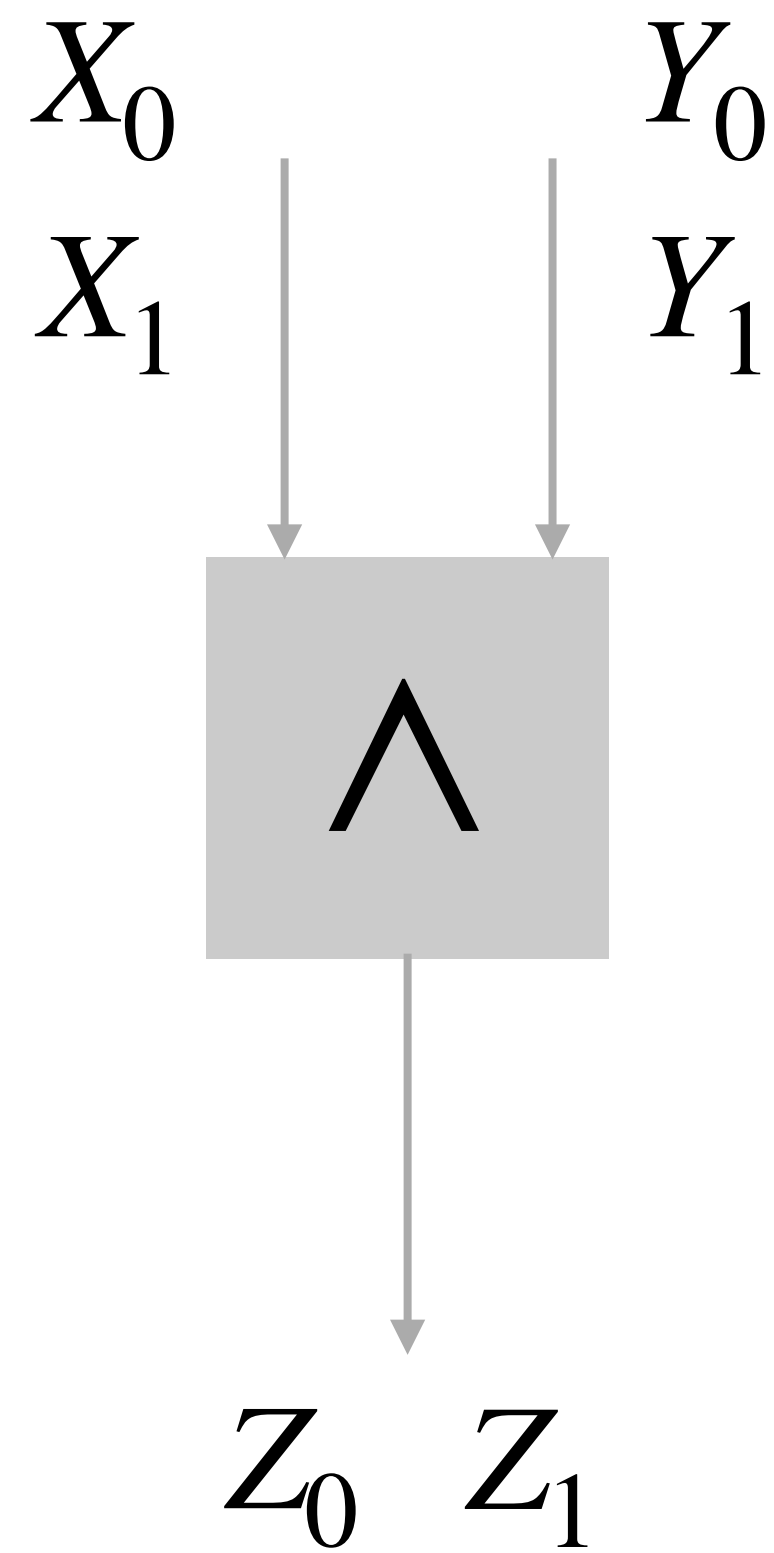
Two Party Garbling: Garbler



Truth Table

0	0	0
0	1	0
1	0	0
1	1	1

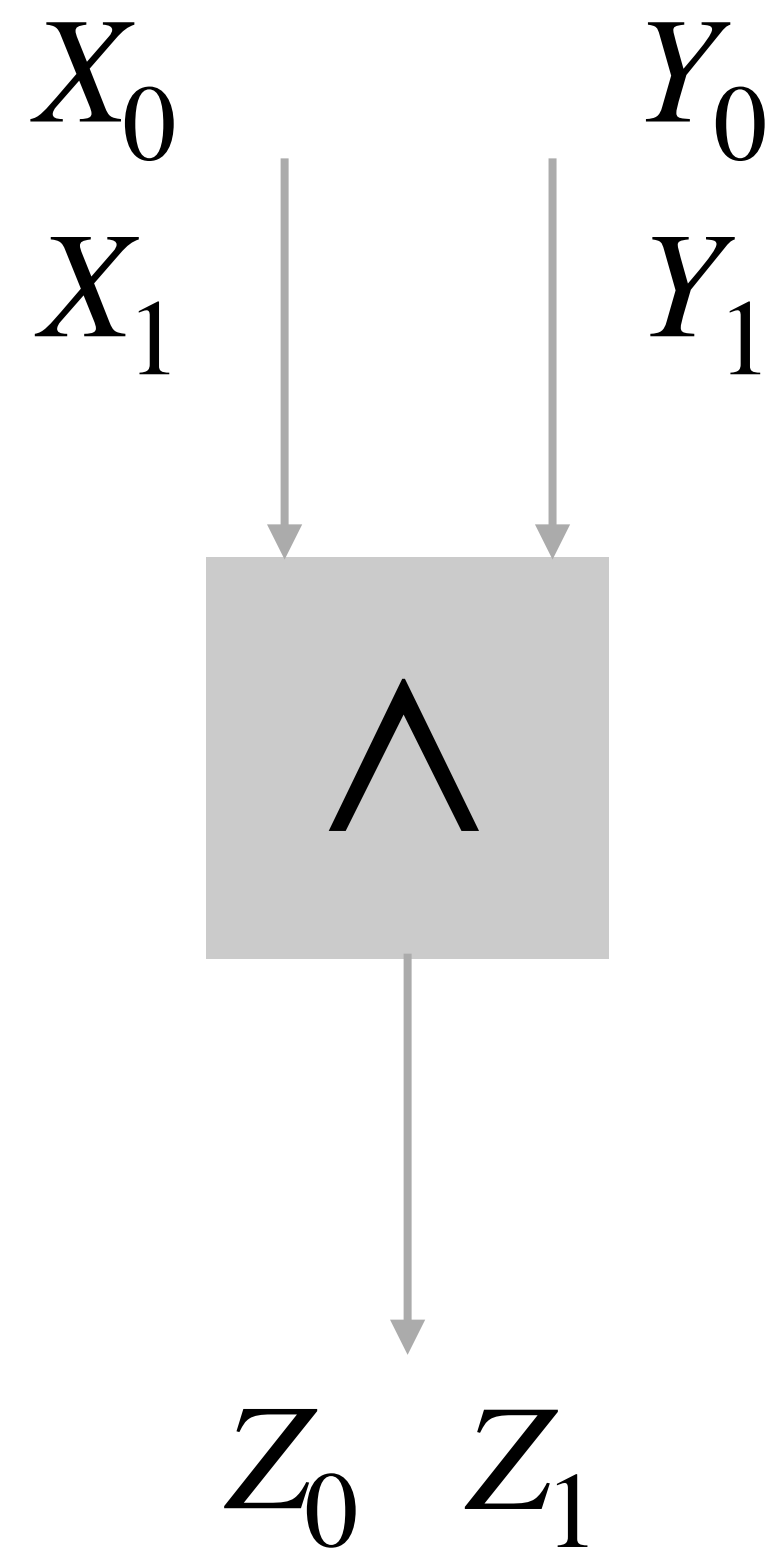
Two Party Garbling: Garbler



Truth Table

0	0	0
0	1	0
1	0	0
1	1	1

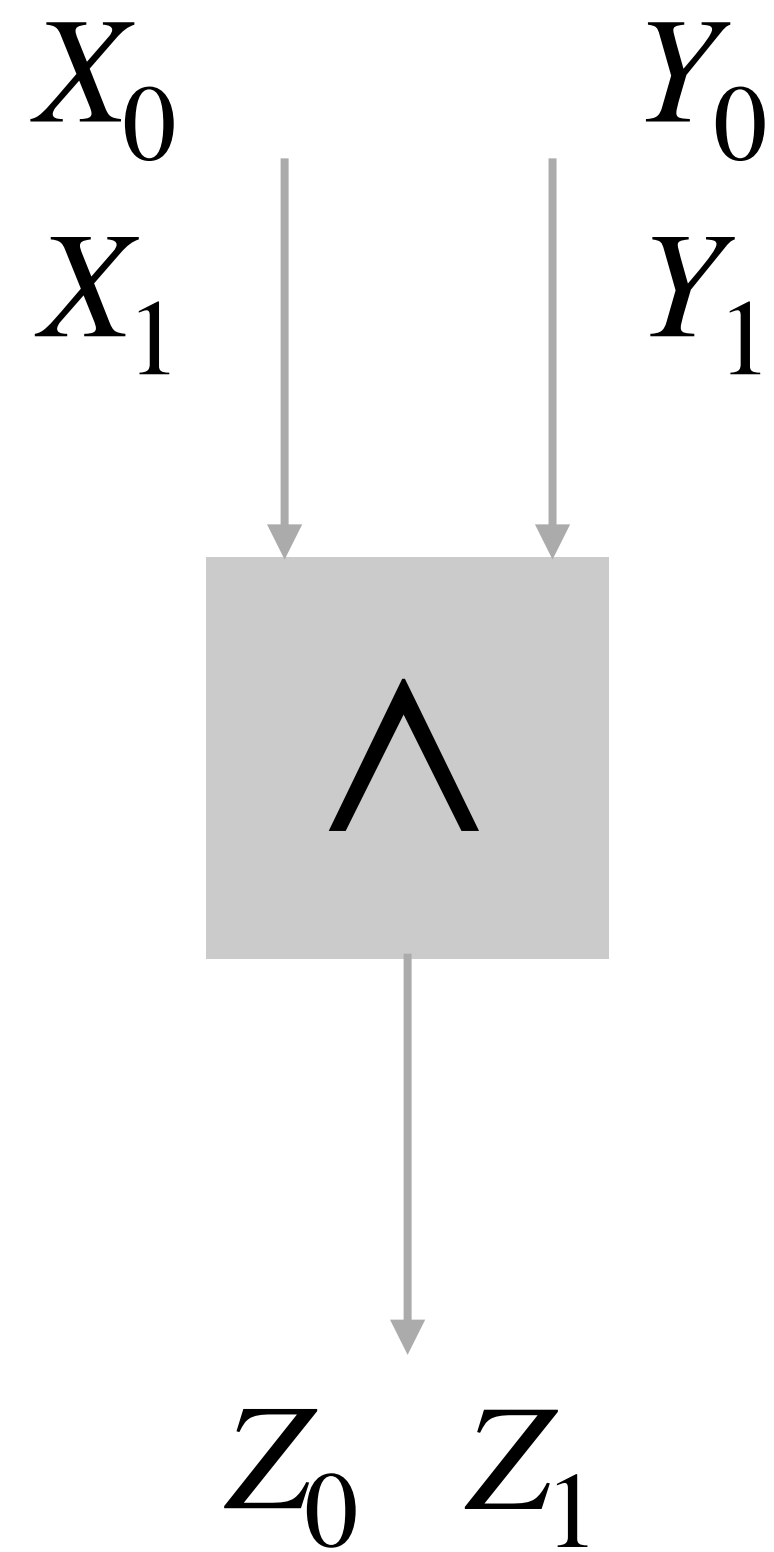
Two Party Garbling: Garbler



Encrypting The Truth Table

X_0	Y_0	Z_0
X_0	Y_1	Z_0
X_1	Y_0	Z_0
X_1	Y_1	Z_1

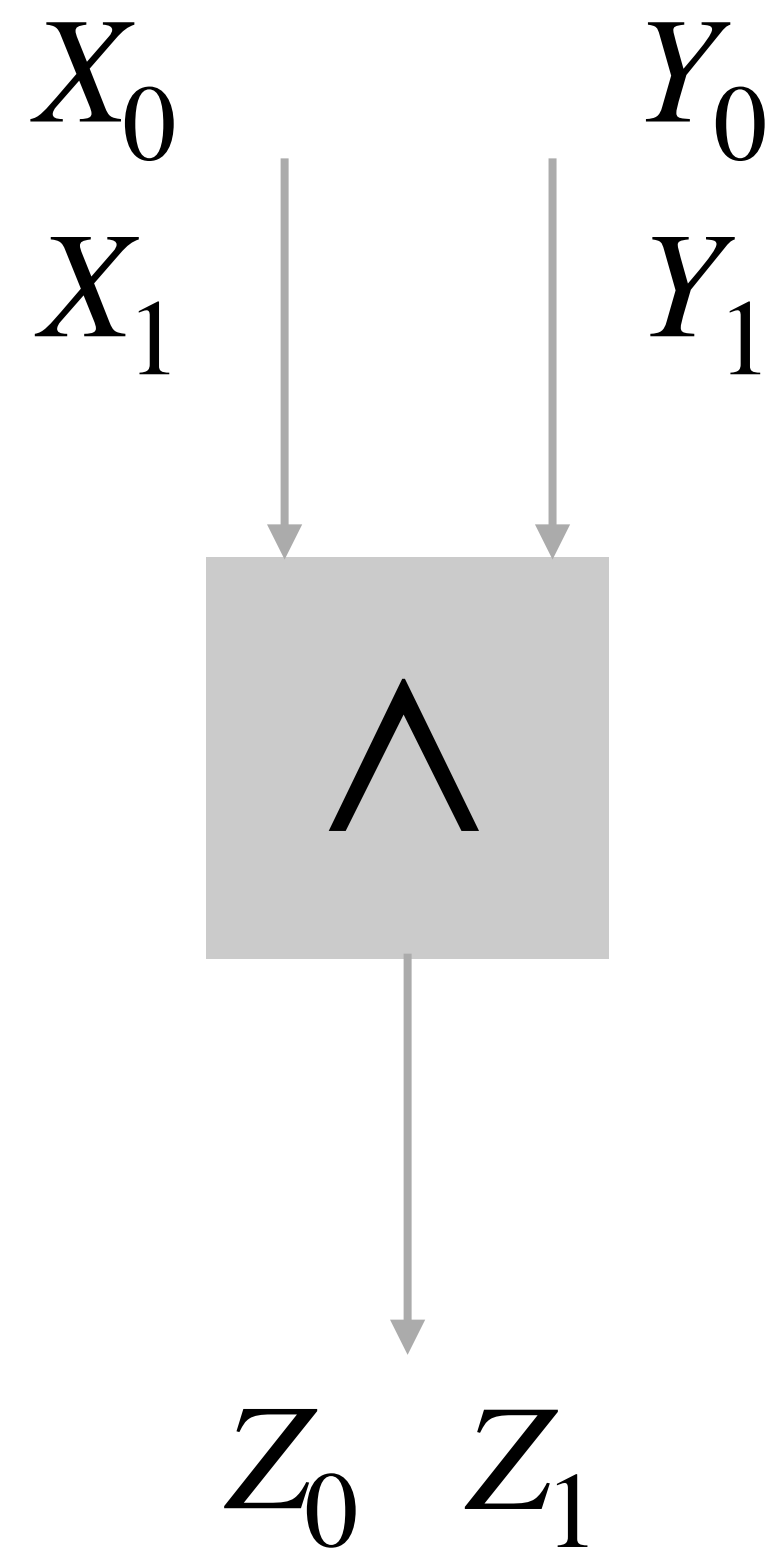
Two Party Garbling: Garbler



Encrypting The Truth Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	
X_1	Y_0	Z_0	
X_1	Y_1	Z_1	

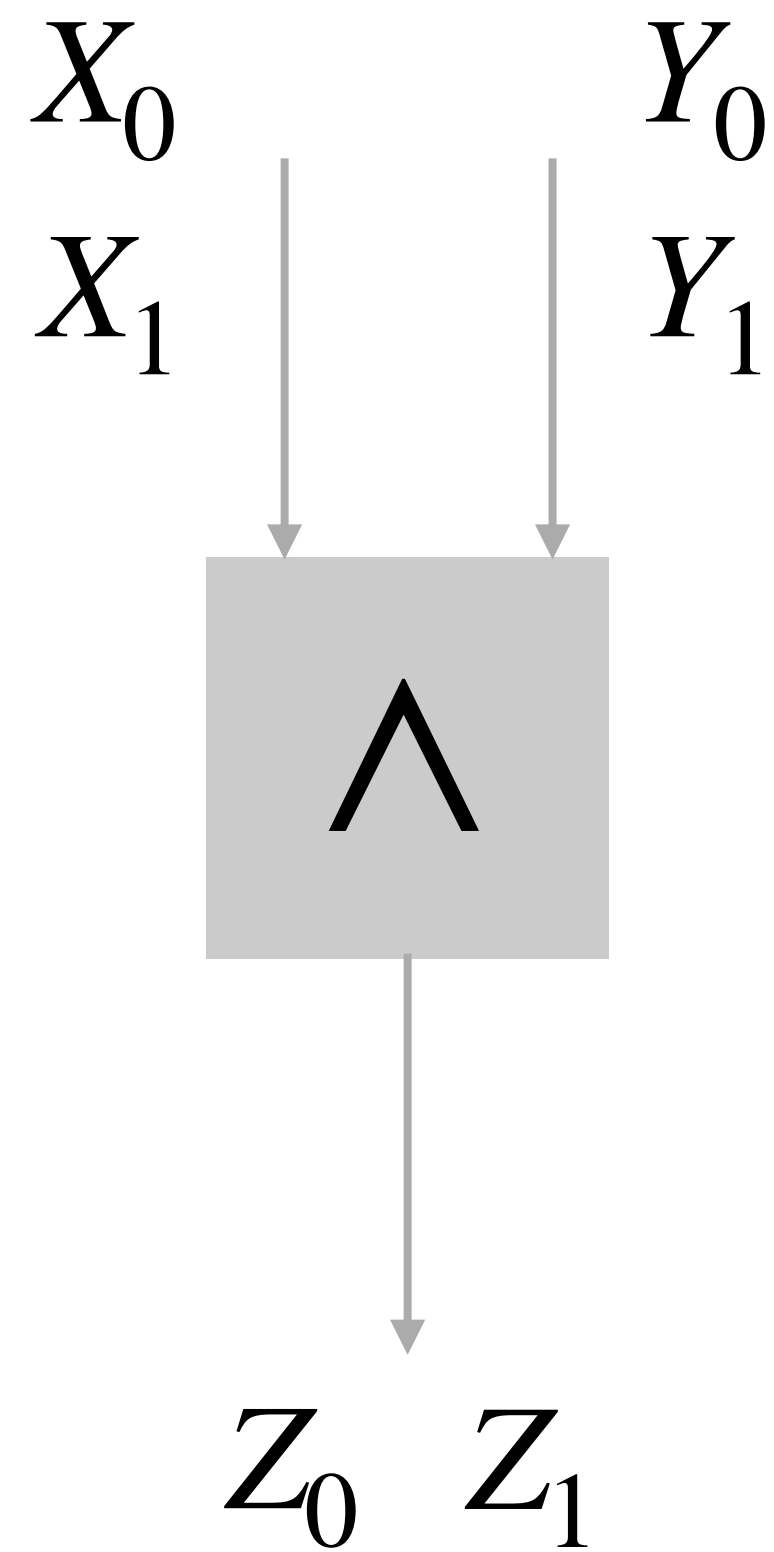
Two Party Garbling: Garbler



Encrypting The Truth Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

Two Party Garbling: Garbler



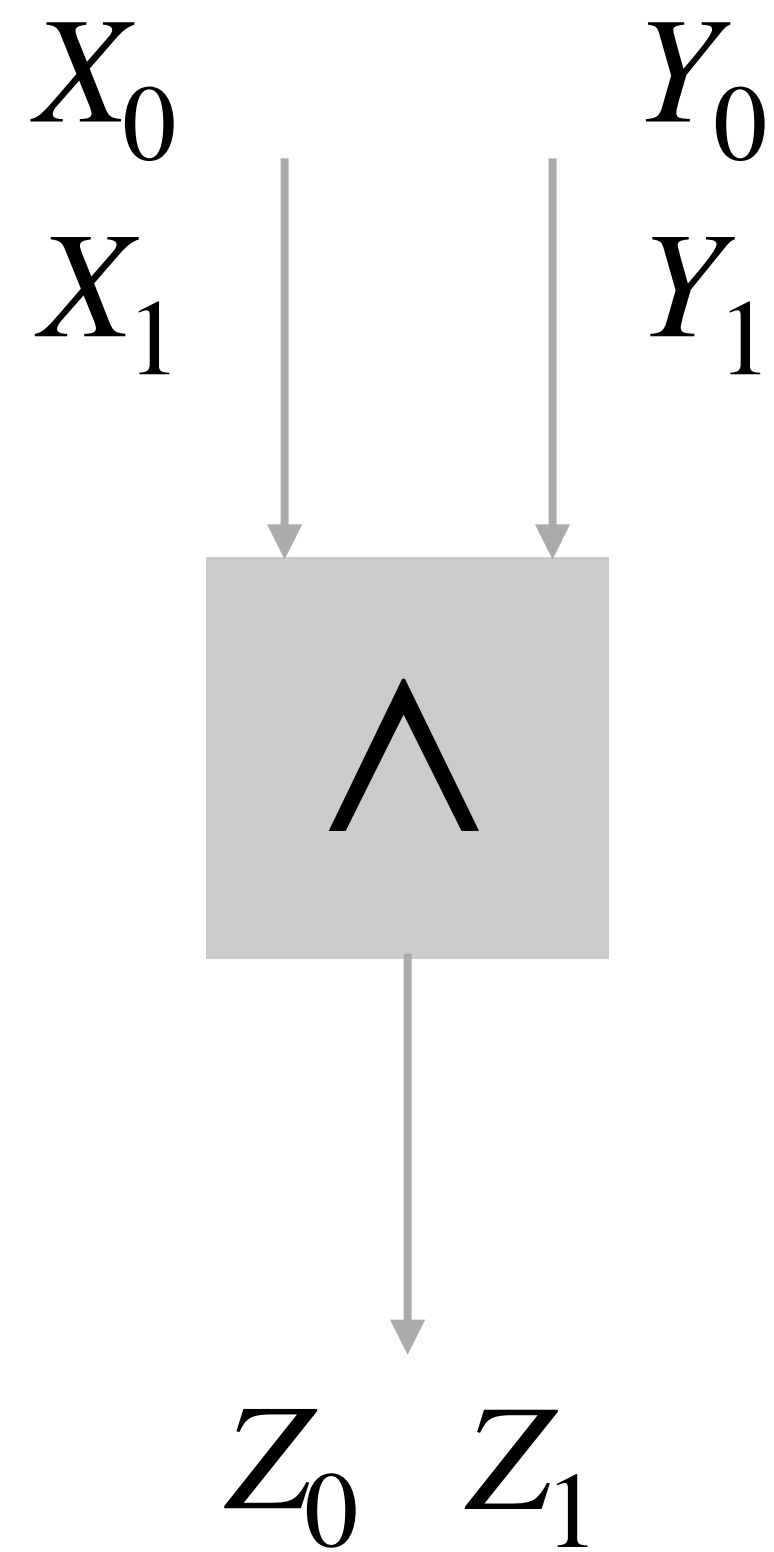
Encrypting The Truth Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

Shuffled

$\mathcal{G}$

Two Party Garbling: Garbler



Encrypting The Truth Table

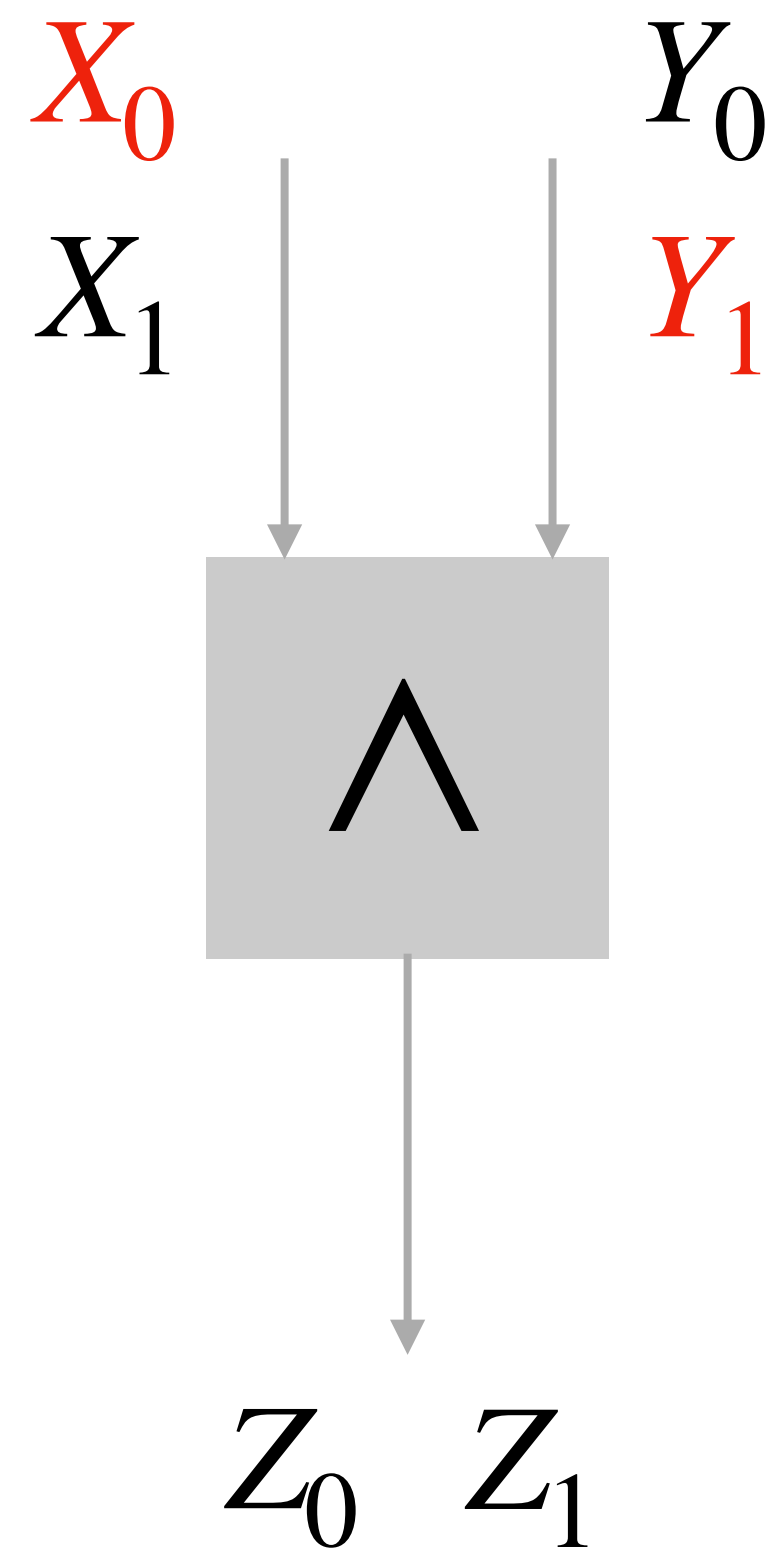
X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

Shuffled

Computed and sent to evaluator.

$\mathcal{G}$

Two Party Garbling: Evaluator



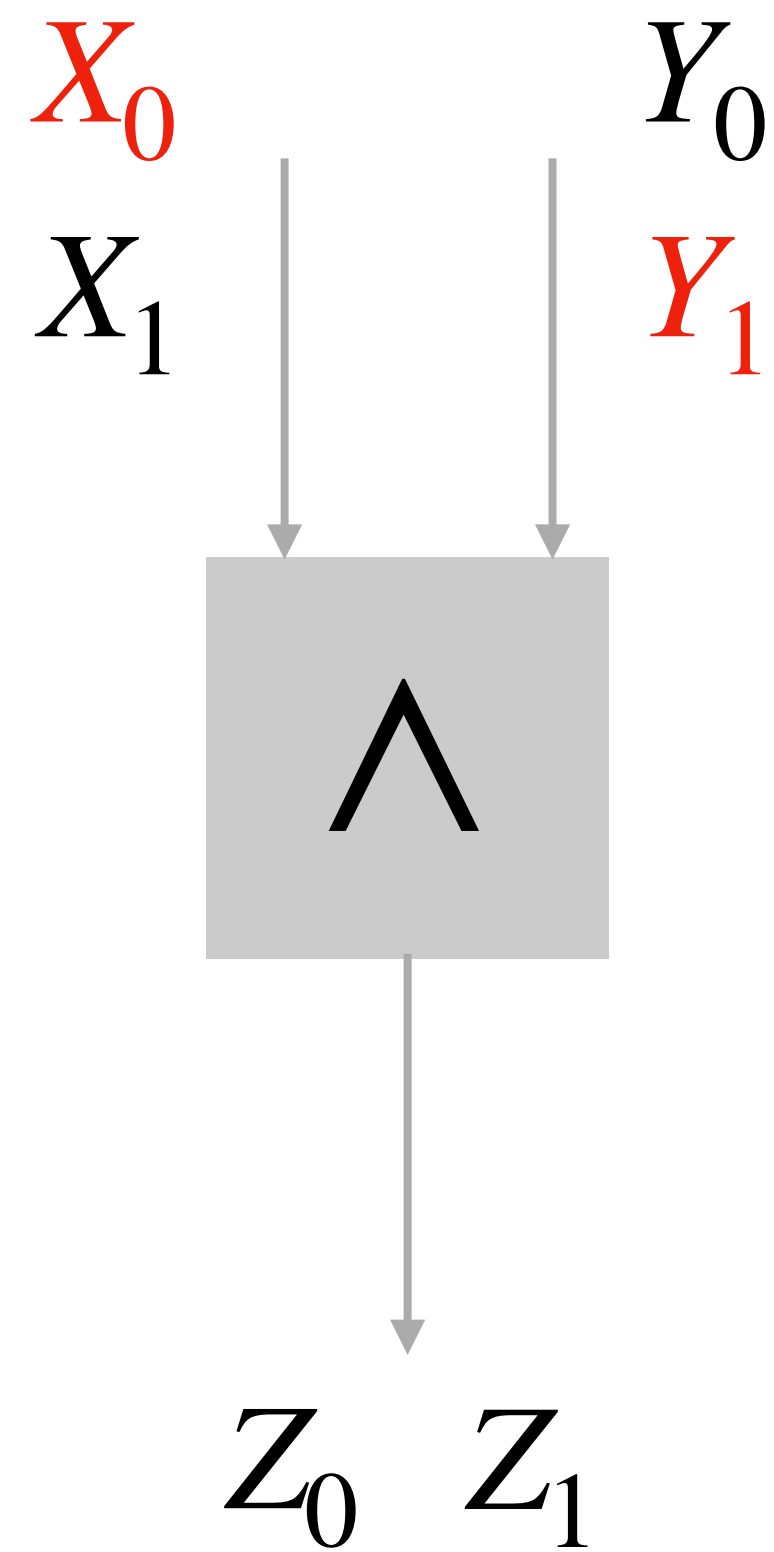
Garbled Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

Shuffled

$\mathcal{G}$

Two Party Garbling: Evaluator



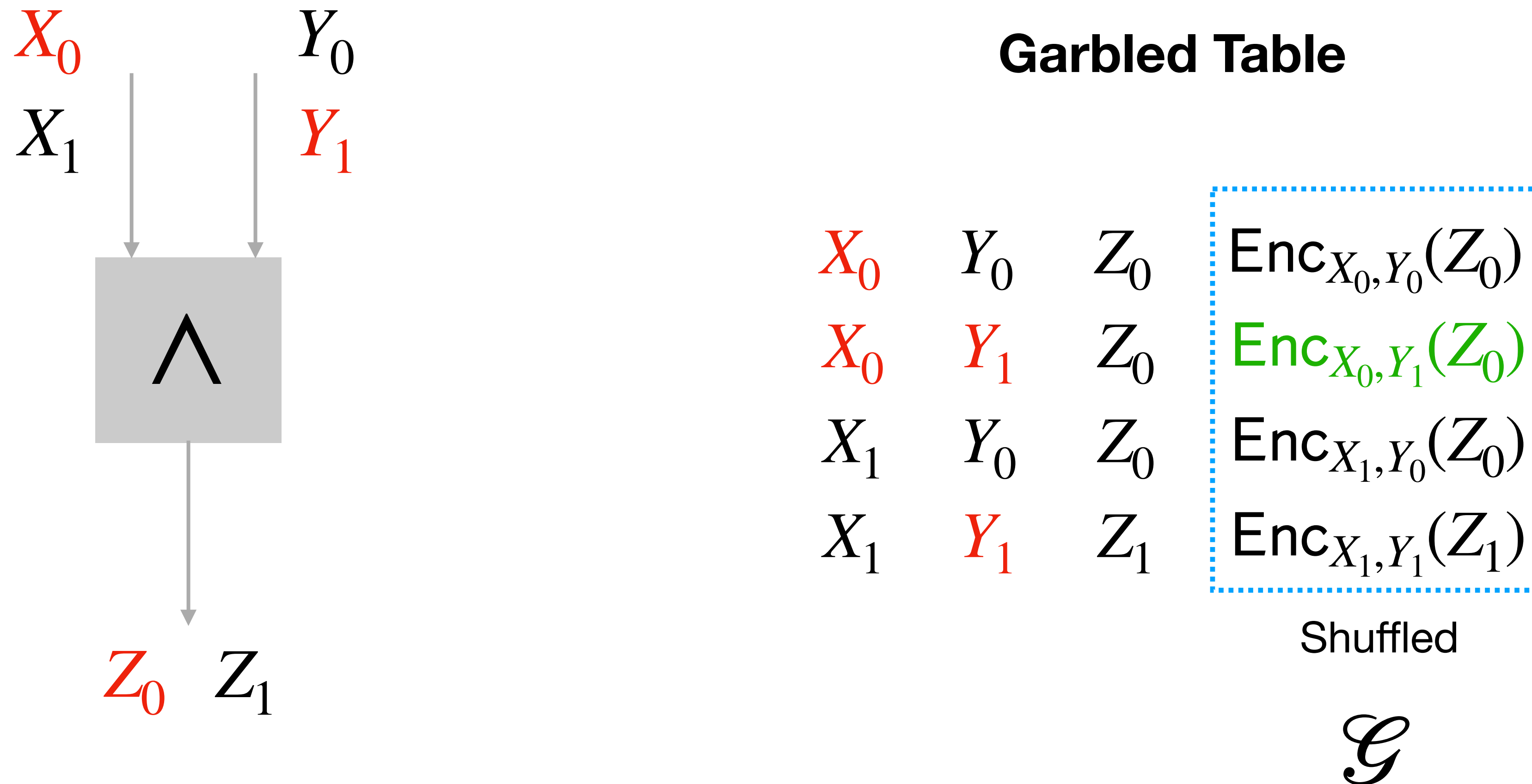
Garbled Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

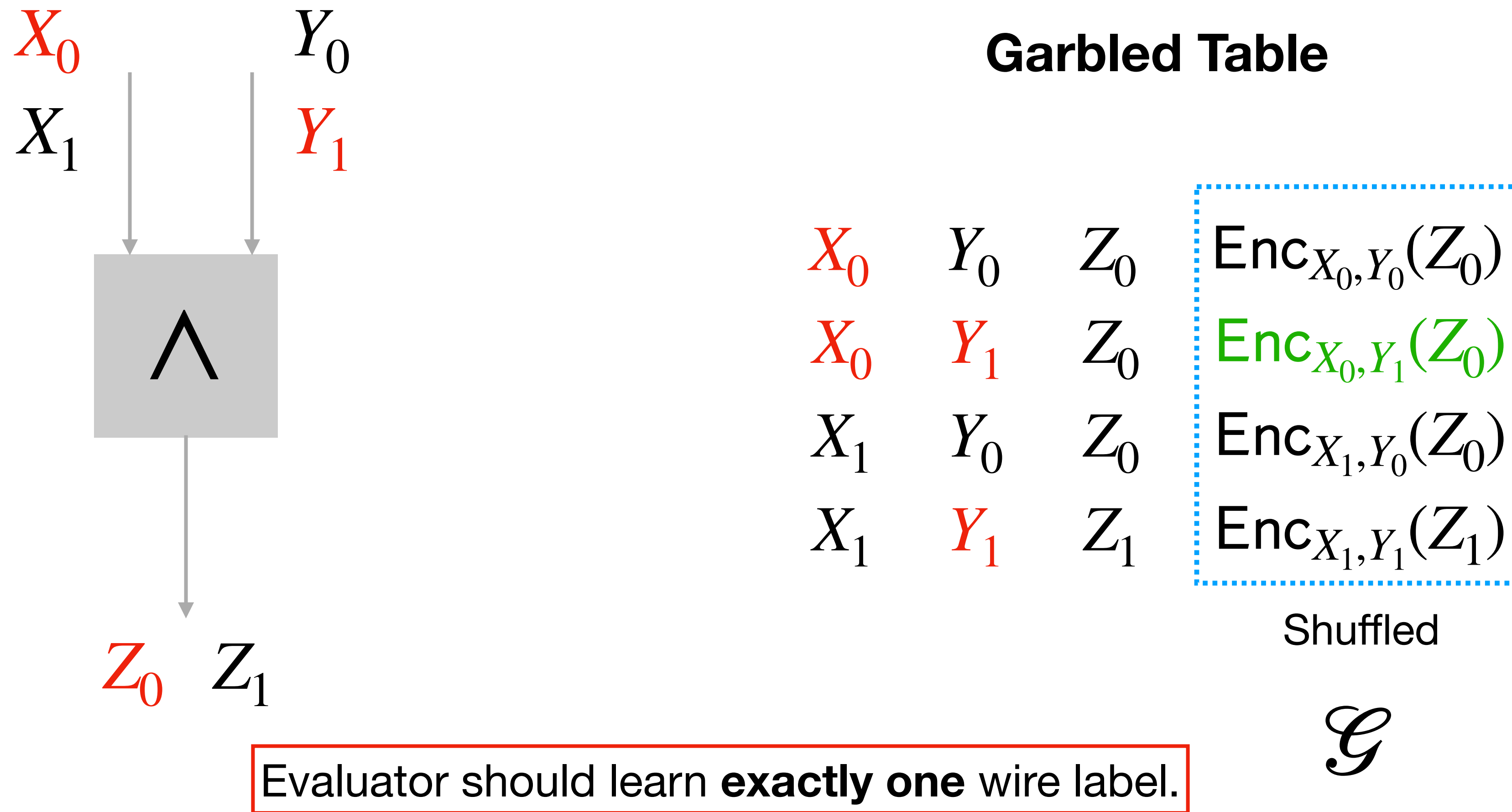
Shuffled

$\mathcal{G}$

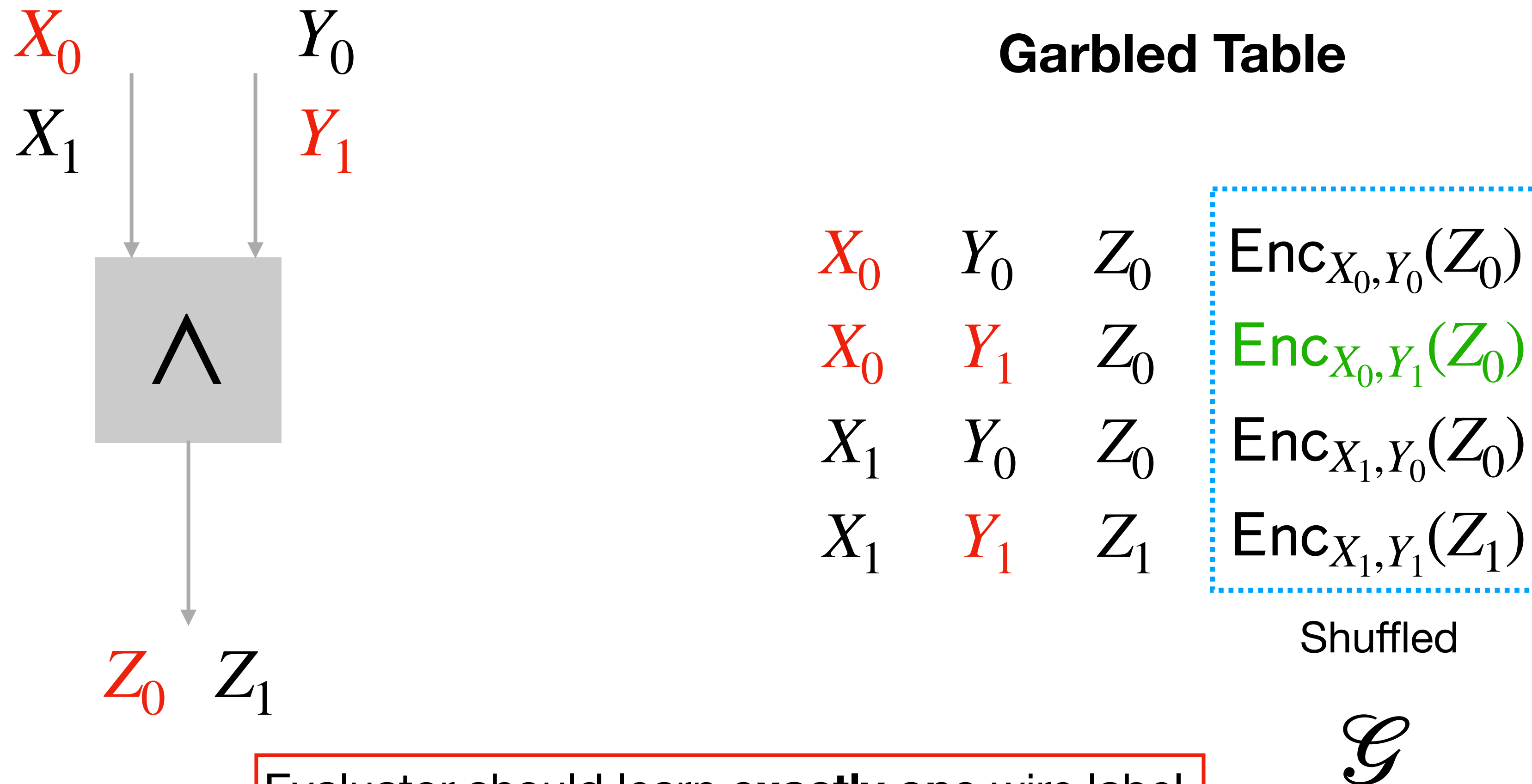
Two Party Garbling: Evaluator



Two Party Garbling: Evaluator



Two Party Garbling: Evaluator

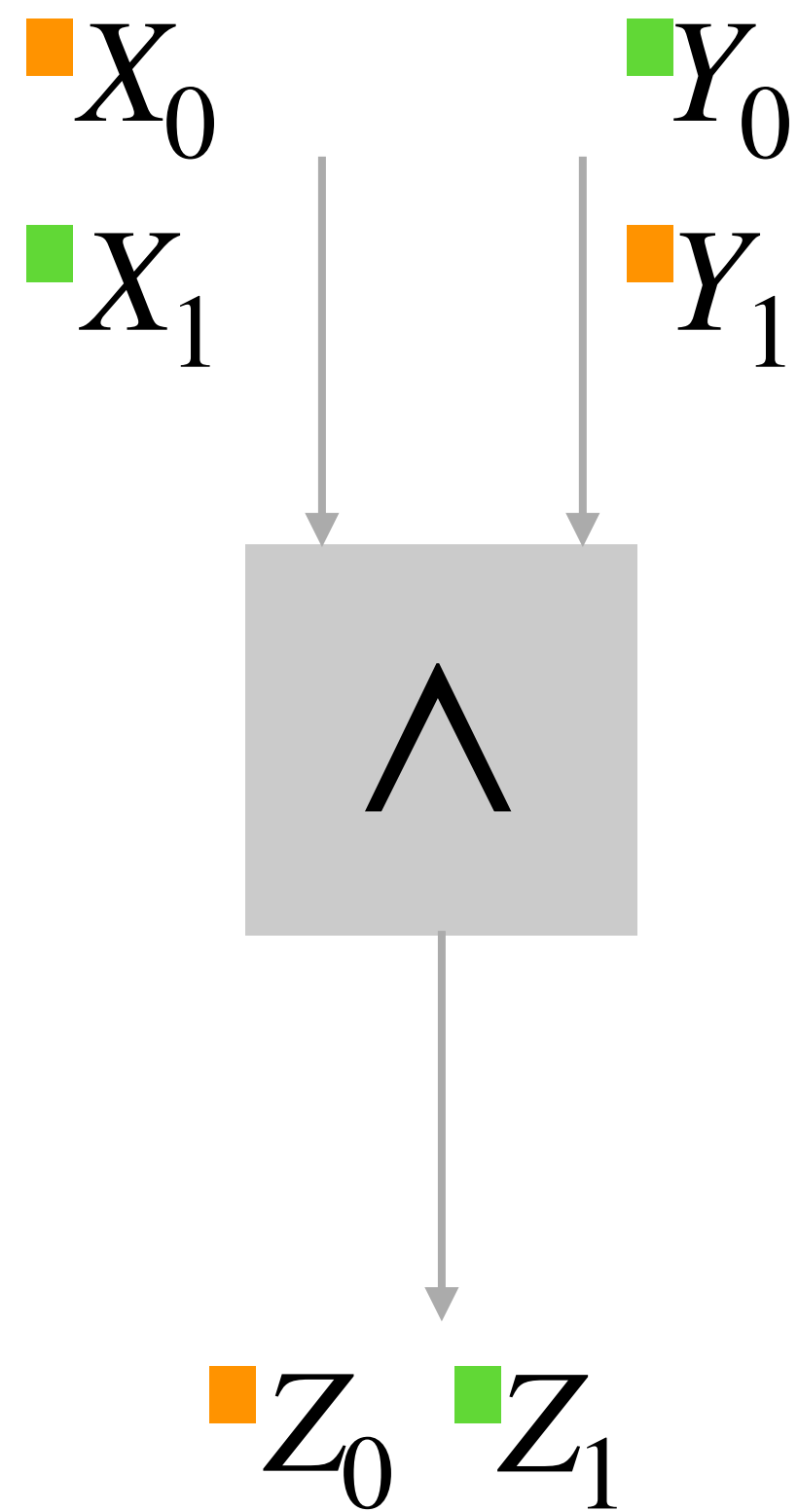


Evaluator should learn **exactly one** wire label.

Which ciphertext to decrypt?

Two Party Garbling: Point And Permute

Color wire labels to indicate which ciphertext to decrypt.



Encrypting The Truth Table

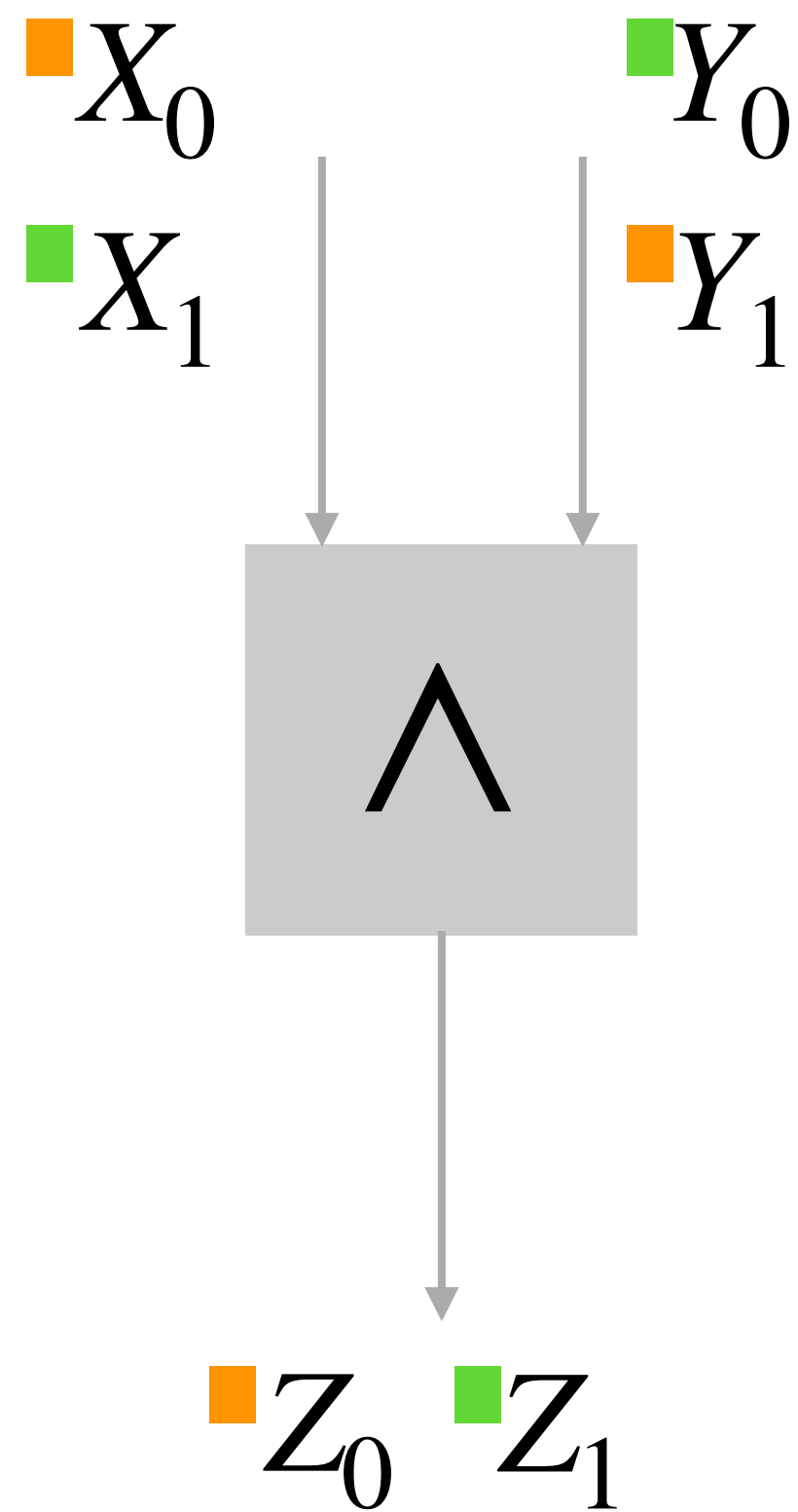
X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(Z_1)$

Shuffled

$\mathcal{G}$

Two Party Garbling: Point And Permute

Color wire labels to indicate which ciphertext to decrypt.



Encrypting The Truth Table

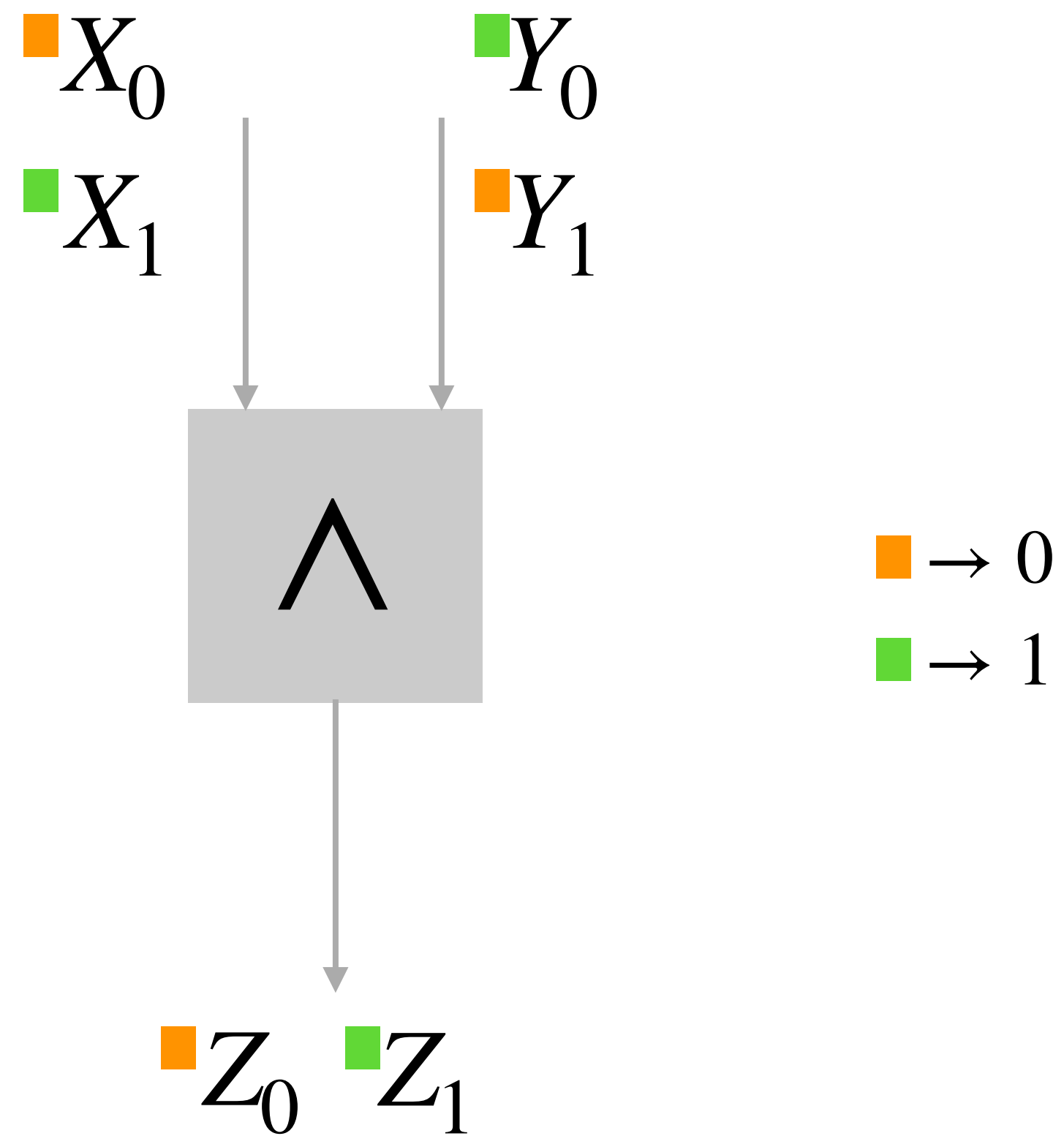
X_0	Y_0	Z_0	$\blacksquare\blacksquare\text{Enc}_{X_0,Y_0}(\blacksquare Z_0)$
X_0	Y_1	Z_0	$\blacksquare\blacksquare\text{Enc}_{X_0,Y_1}(\blacksquare Z_0)$
X_1	Y_0	Z_0	$\blacksquare\blacksquare\text{Enc}_{X_1,Y_0}(\blacksquare Z_0)$
X_1	Y_1	Z_1	$\blacksquare\blacksquare\text{Enc}_{X_1,Y_1}(\blacksquare Z_1)$

Shuffled

$\mathcal{G}$

Two Party Garbling: Point And Permute

Color wire labels to indicate which ciphertext to decrypt.



Encrypting The Truth Table

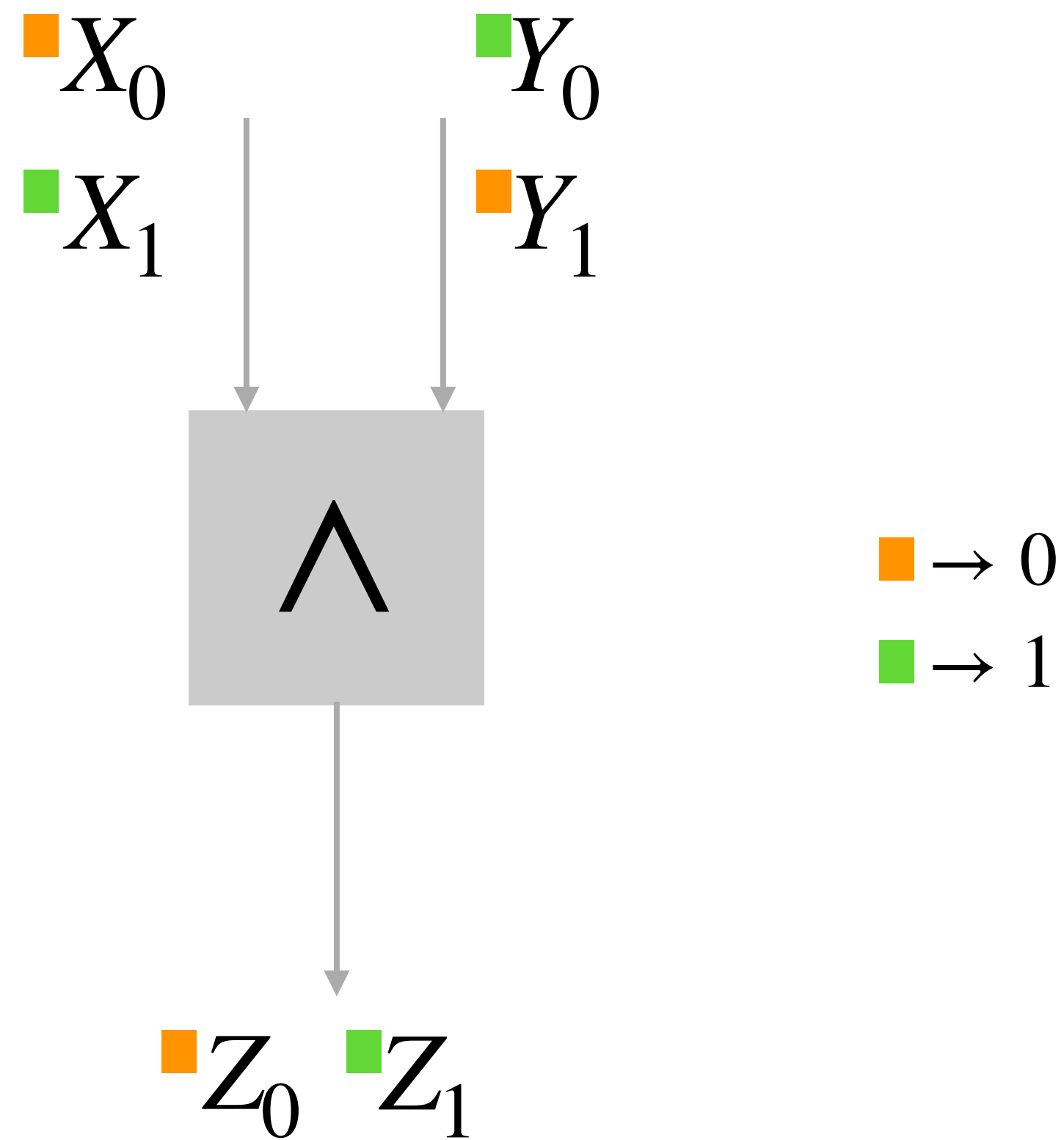
X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(\text{orange } Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(\text{orange } Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(\text{orange } Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(\text{green } Z_1)$

Shuffled

$\mathcal{G}$

Two Party Garbling: Point And Permute

Color wire labels to indicate which ciphertext to decrypt.



Encrypting The Truth Table

X_0	Y_0	Z_0	$\text{Enc}_{X_0, Y_0}(\text{ } Z_0)$
X_0	Y_1	Z_0	$\text{Enc}_{X_0, Y_1}(\text{ } Z_0)$
X_1	Y_0	Z_0	$\text{Enc}_{X_1, Y_0}(\text{ } Z_0)$
X_1	Y_1	Z_1	$\text{Enc}_{X_1, Y_1}(\text{ } Z_1)$

Shuffled

$\mathcal{G}$

Coloring is independent of wire label assignment.

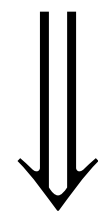
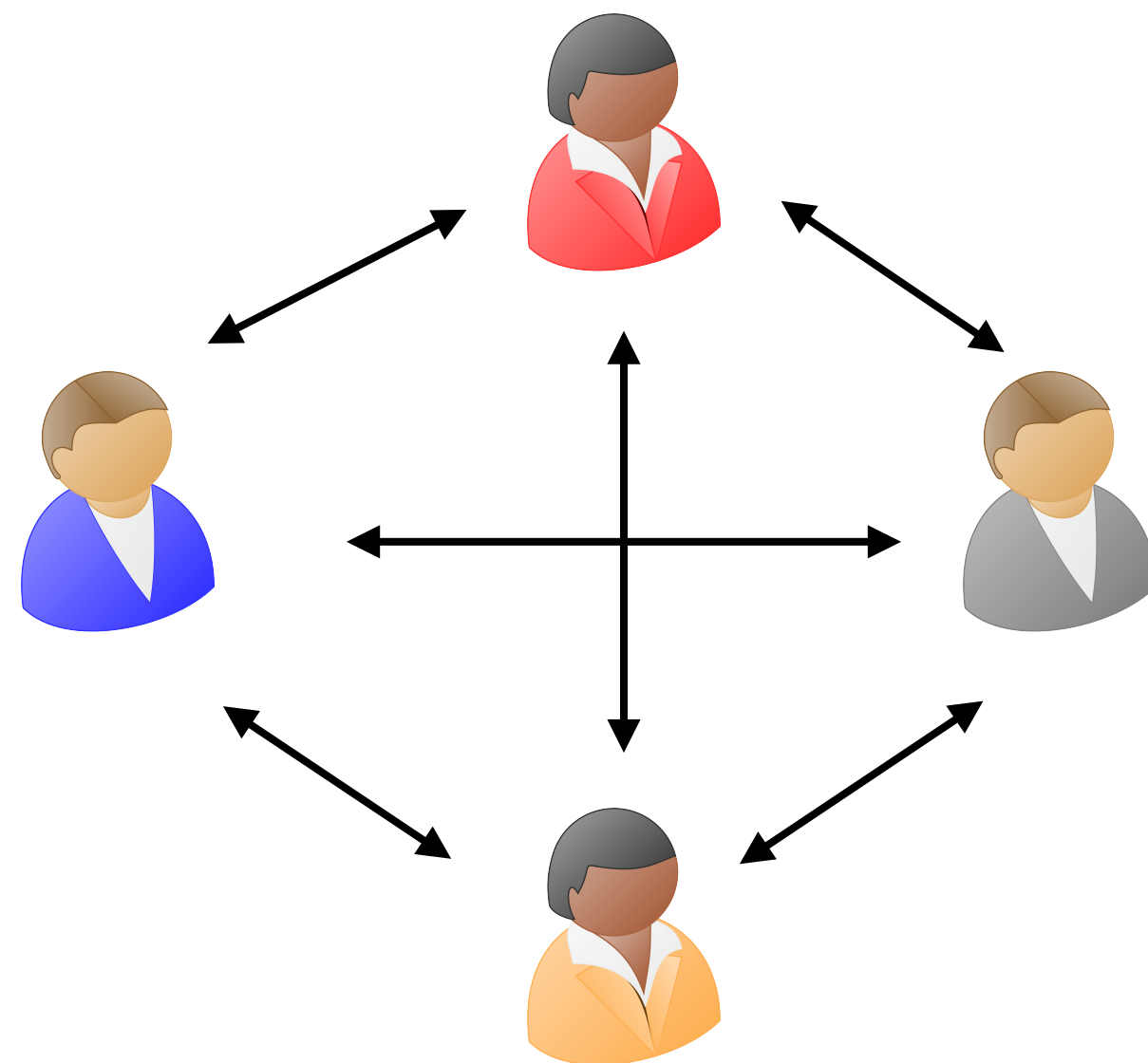
A Template For Multiparty Garbling [BMR90]

Non-constant round MPC → Constant round MPC

A Template For Multiparty Garbling [BMR90]

Non-constant round MPC $\rightarrow$ Constant round MPC

Garbling Phase

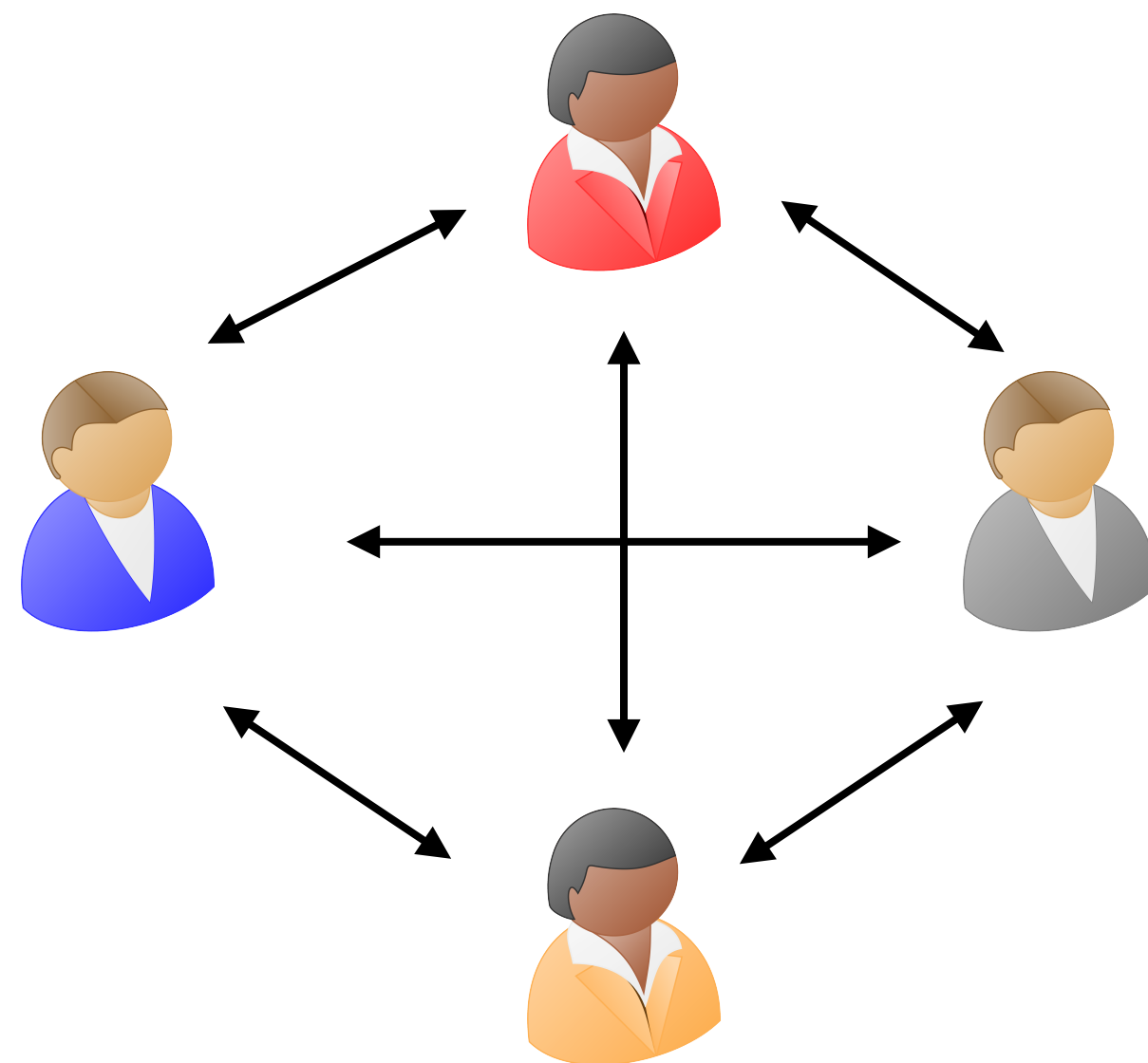


$\mathcal{G}$ X_0, Y_1

A Template For Multiparty Garbling [BMR90]

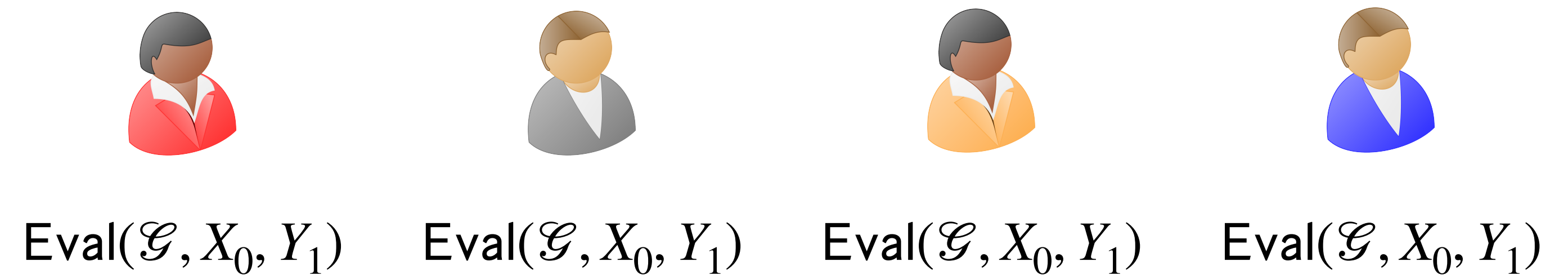
Non-constant round MPC $\rightarrow$ Constant round MPC

Garbling Phase



$\Downarrow$
 $\mathcal{G} \quad X_0, Y_1$

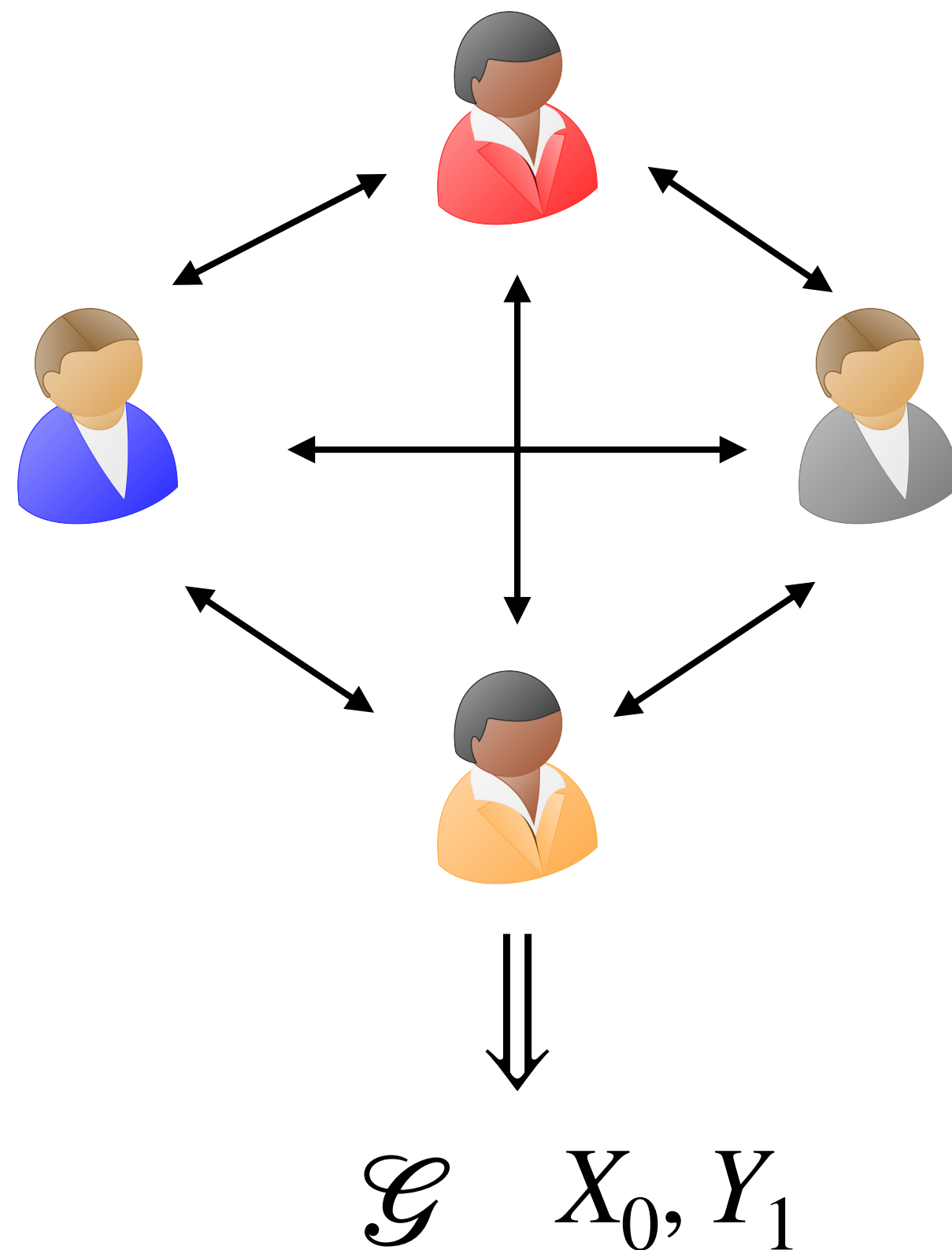
Evaluation Phase



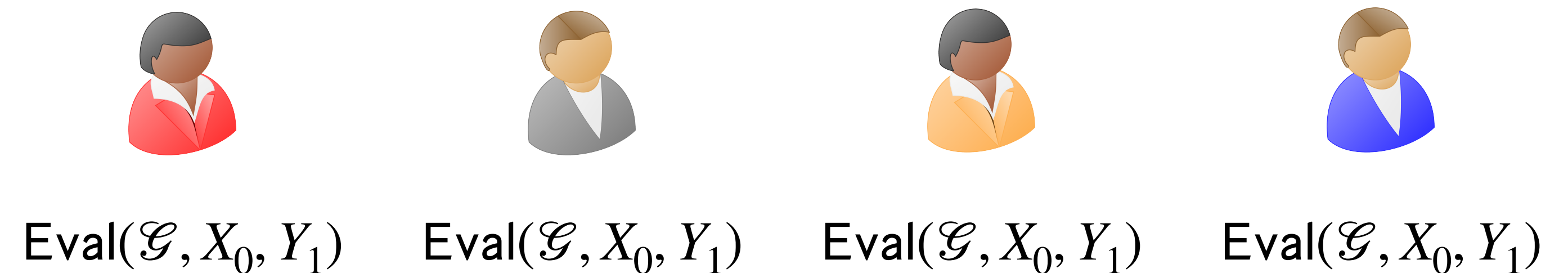
A Template For Multiparty Garbling [BMR90]

Non-constant round MPC $\rightarrow$ Constant round MPC

Garbling Phase



Evaluation Phase

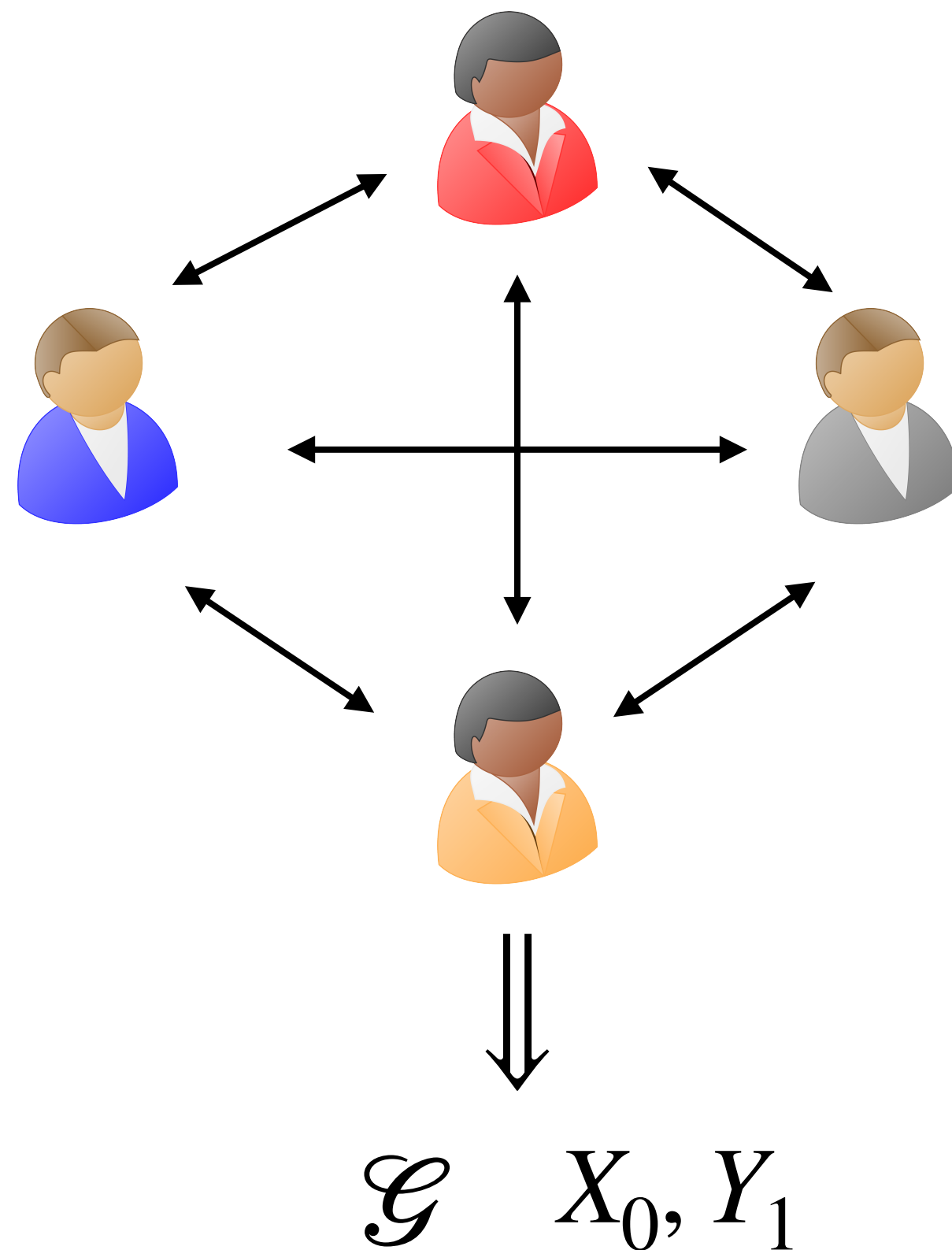


- MPC ensures **wire labels are private**.
- All gates can be **garbled in parallel** $\implies$ constant round MPC.

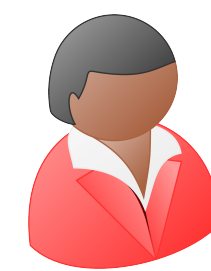
A Template For Multiparty Garbling [BMR90]

Non-constant round MPC $\rightarrow$ Constant round MPC

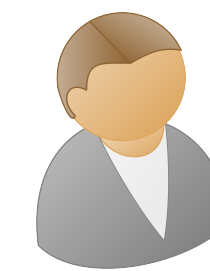
Garbling Phase



Evaluation Phase



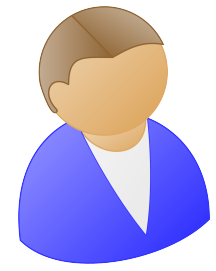
$\text{Eval}(\mathcal{G}, X_0, Y_1)$



$\text{Eval}(\mathcal{G}, X_0, Y_1)$



$\text{Eval}(\mathcal{G}, X_0, Y_1)$



$\text{Eval}(\mathcal{G}, X_0, Y_1)$

- MPC ensures **wire labels are private**.
- All gates can be **garbled in parallel** $\Rightarrow$ constant round MPC.

$O(|C|)$ MPC for garbling $\Rightarrow O(|C|)$ constant round MPC?

Non-black box use of encryption.

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05]

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

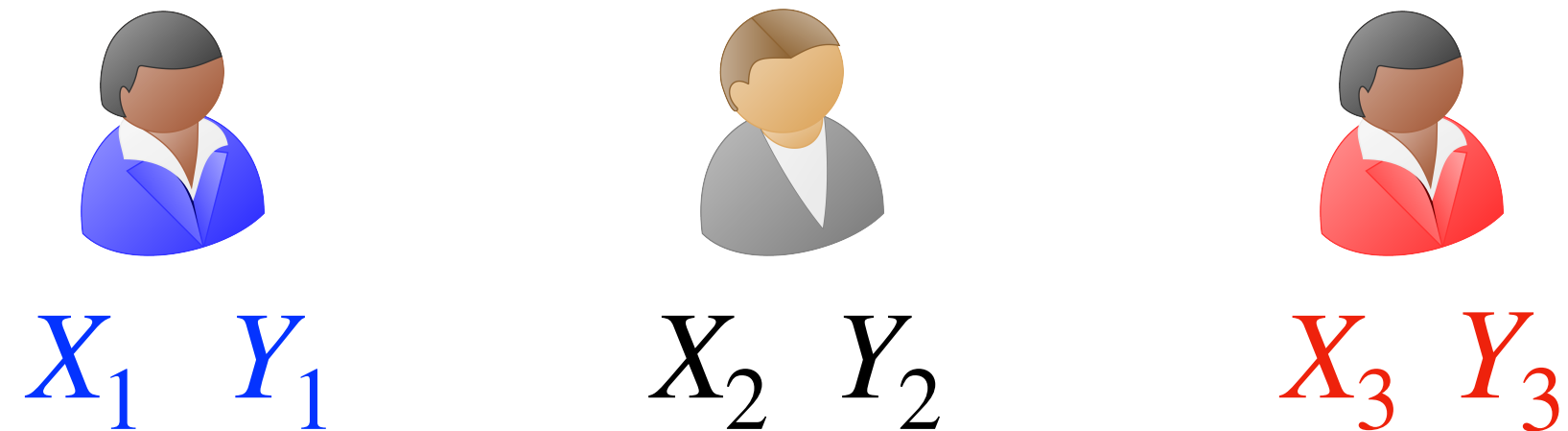
Wire labels can't be known in the clear by any single party.

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Wire labels can't be known in the clear by any single party.



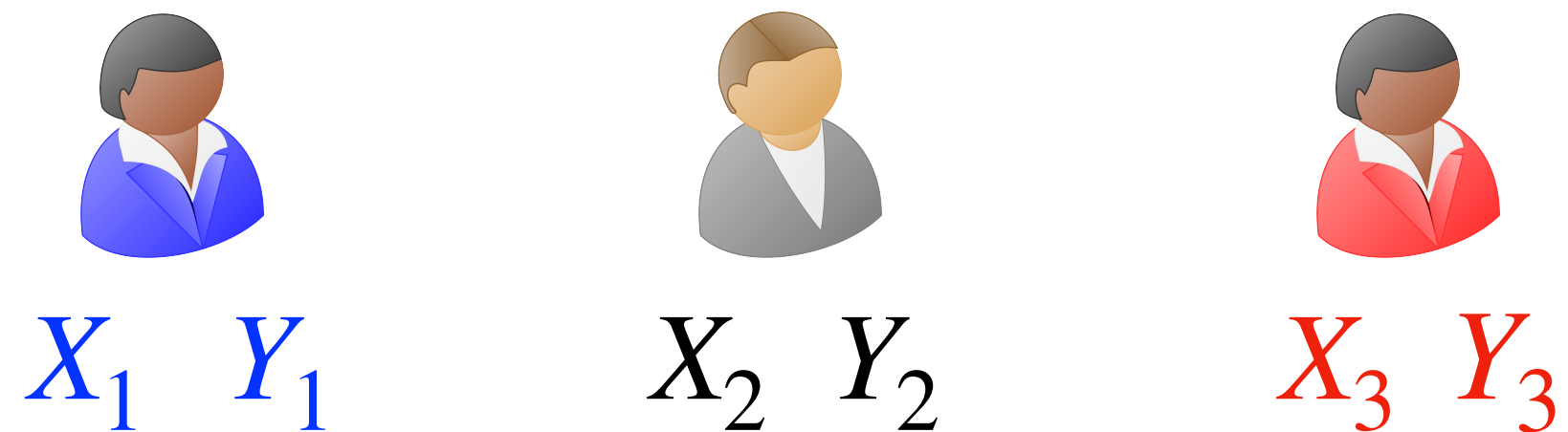
$$X = X_1 \| X_2 \| X_3 \quad Y = Y_1 \| Y_2 \| Y_3$$

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Wire labels can't be known in the clear by any single party.



$$X = X_1 \parallel X_2 \parallel X_3 \quad Y = Y_1 \parallel Y_2 \parallel Y_3$$

$$\text{PRF}_X(g) = \text{PRF}_{X_1}(g) \oplus \text{PRF}_{X_2}(g) \oplus \text{PRF}_{X_3}(g)$$

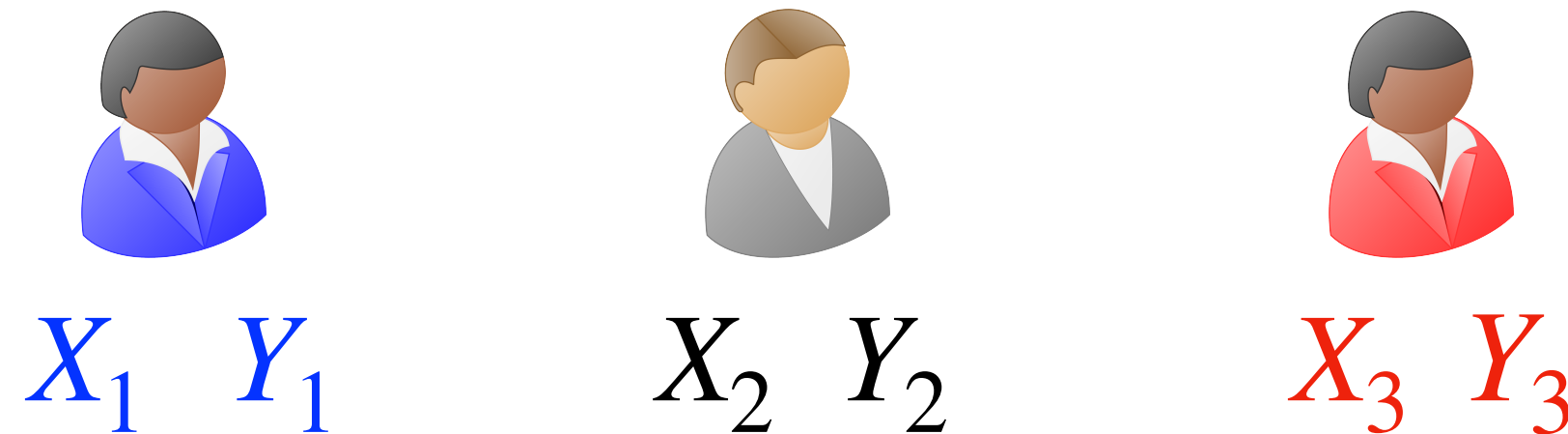
Each party can locally evaluate PRF.

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Wire labels can't be known in the clear by any single party.



$$X = X_1 \parallel X_2 \parallel X_3 \quad Y = Y_1 \parallel Y_2 \parallel Y_3$$

$$\text{PRF}_X(g) = \text{PRF}_{X_1}(g) \oplus \text{PRF}_{X_2}(g) \oplus \text{PRF}_{X_3}(g)$$

Each party can locally evaluate PRF.

Wire labels and hence encryptions are linear in n .

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

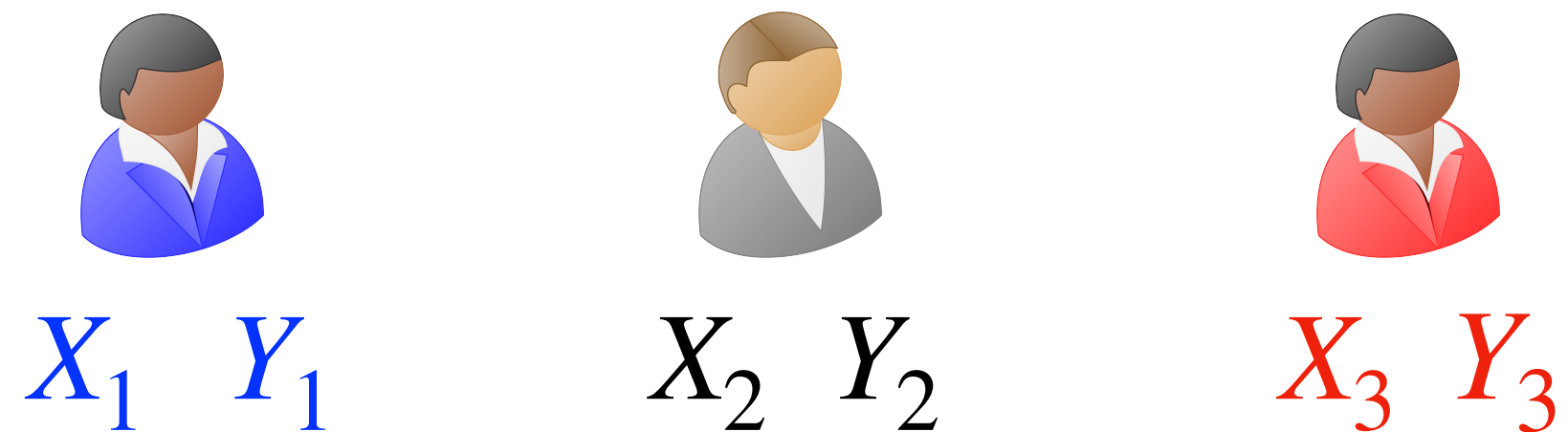
Use key-homomorphic PRFs [BLO17] $\rightarrow |\mathcal{G}|$ independent of n .

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Use key-homomorphic PRFs [BLO17] $\rightarrow |\mathcal{G}|$ independent of n .



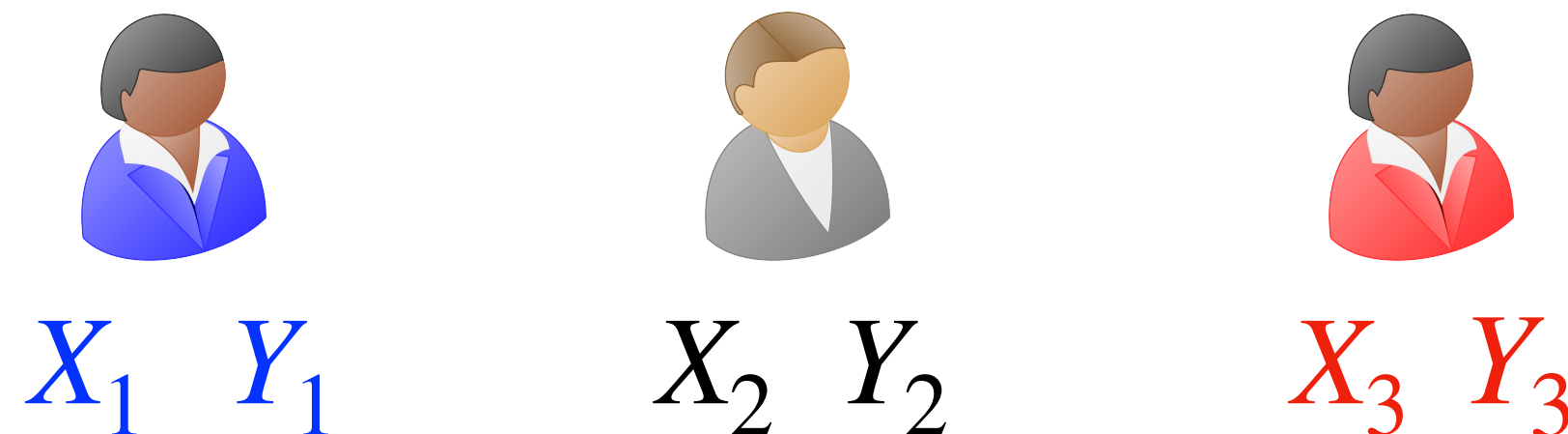
$$X = X_1 + X_2 + X_3 \quad Y = Y_1 + Y_2 + Y_3$$

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Use key-homomorphic PRFs [BLO17] $\rightarrow |\mathcal{G}|$ independent of n .



Homomorphism operator

$$X = X_1 + X_2 + X_3 \quad Y = Y_1 + Y_2 + Y_3$$

$$\text{PRF}_{X_1}(g) \tilde{+} \text{PRF}_{X_2}(g) \tilde{+} \text{PRF}_{X_3}(g) = \text{PRF}_X(g)$$

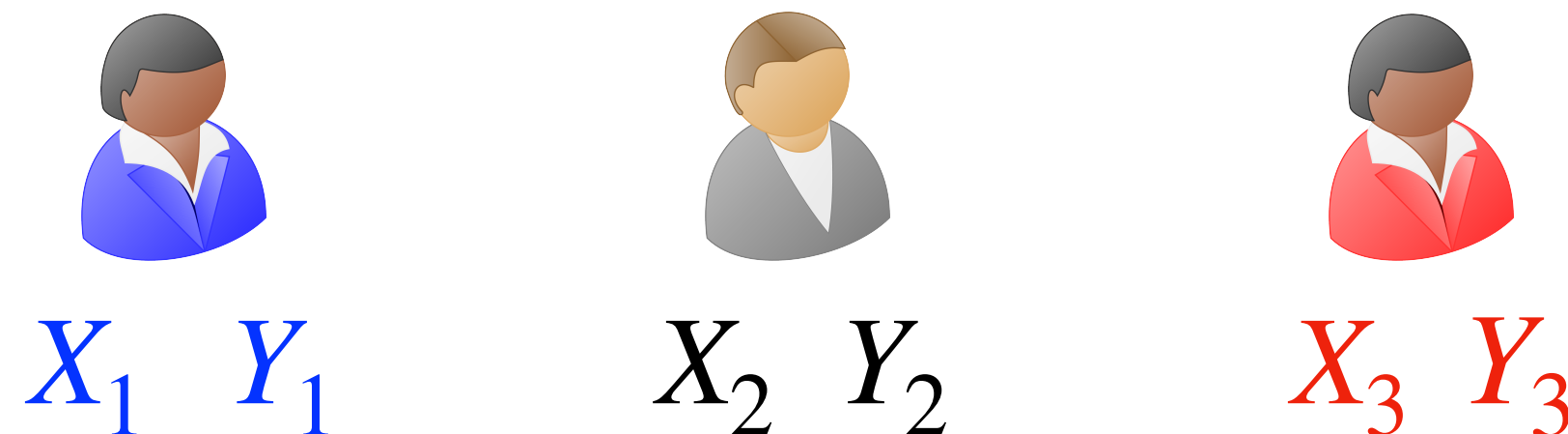
Each party can locally evaluate PRF on its share.

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Use key-homomorphic PRFs [BLO17] $\rightarrow |\mathcal{G}|$ independent of n .



Homomorphism operator

$$X = X_1 + X_2 + X_3 \quad Y = Y_1 + Y_2 + Y_3$$

$$\text{PRF}_{X_1}(g) \tilde{+} \text{PRF}_{X_2}(g) \tilde{+} \text{PRF}_{X_3}(g) = \text{PRF}_X(g)$$

Wire labels independent of n .

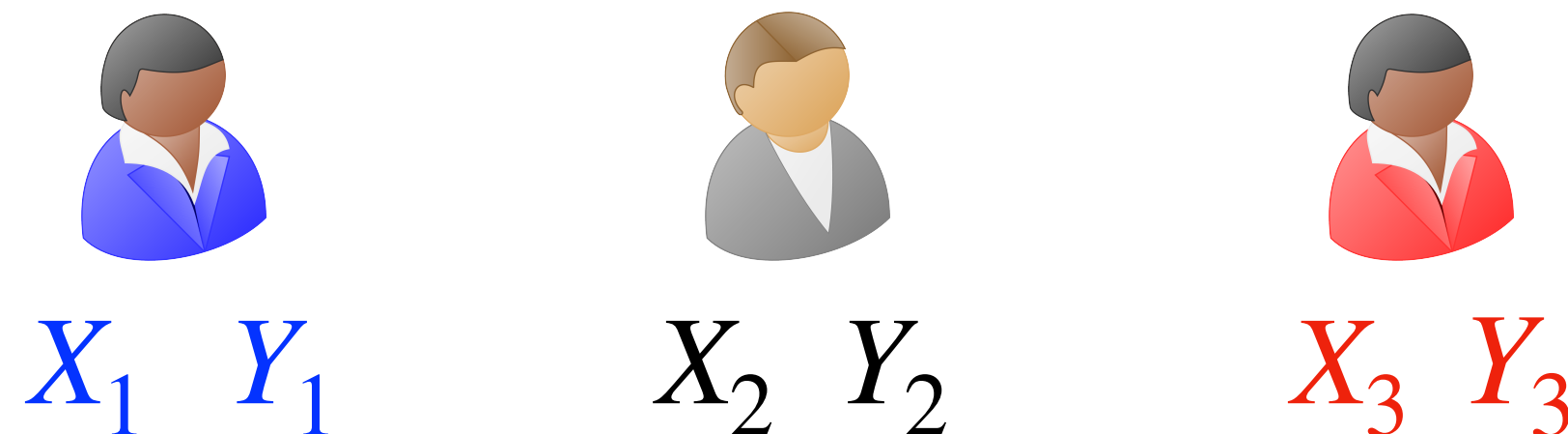
Each party can locally evaluate PRF on its share.

Moving Away From Non-black Box Constructions

Garbling g^{th} gate: $\text{Enc}_{X,Y}(Z) = \text{PRF}_X(g) \oplus \text{PRF}_Y(g) \oplus Z$

Evaluate the PRF outside the MPC [DI05] $\rightarrow |\mathcal{G}|$ is linear in n .

Use key-homomorphic PRFs [BLO17] $\rightarrow |\mathcal{G}|$ independent of n .



Homomorphism operator

$$X = X_1 + X_2 + X_3 \quad Y = Y_1 + Y_2 + Y_3$$

$$\text{PRF}_{X_1}(g) \tilde{+} \text{PRF}_{X_2}(g) \tilde{+} \text{PRF}_{X_3}(g) = \text{PRF}_X(g)$$

Each party can locally evaluate PRF on its share.

Wire labels independent of n .

Does not extend to threshold setting.

Garbling Using Doubly Linear Homomorphic PRFs

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

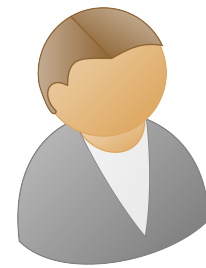
Garbling Using Doubly Linear Homomorphic PRFs

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

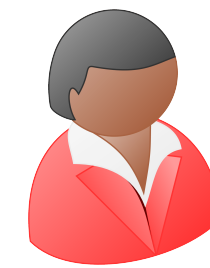
$$\text{PRF}_{[X]}(g) = [PRF_X(g)]$$



$[X][Y][Z]$



$[X][Y][Z]$

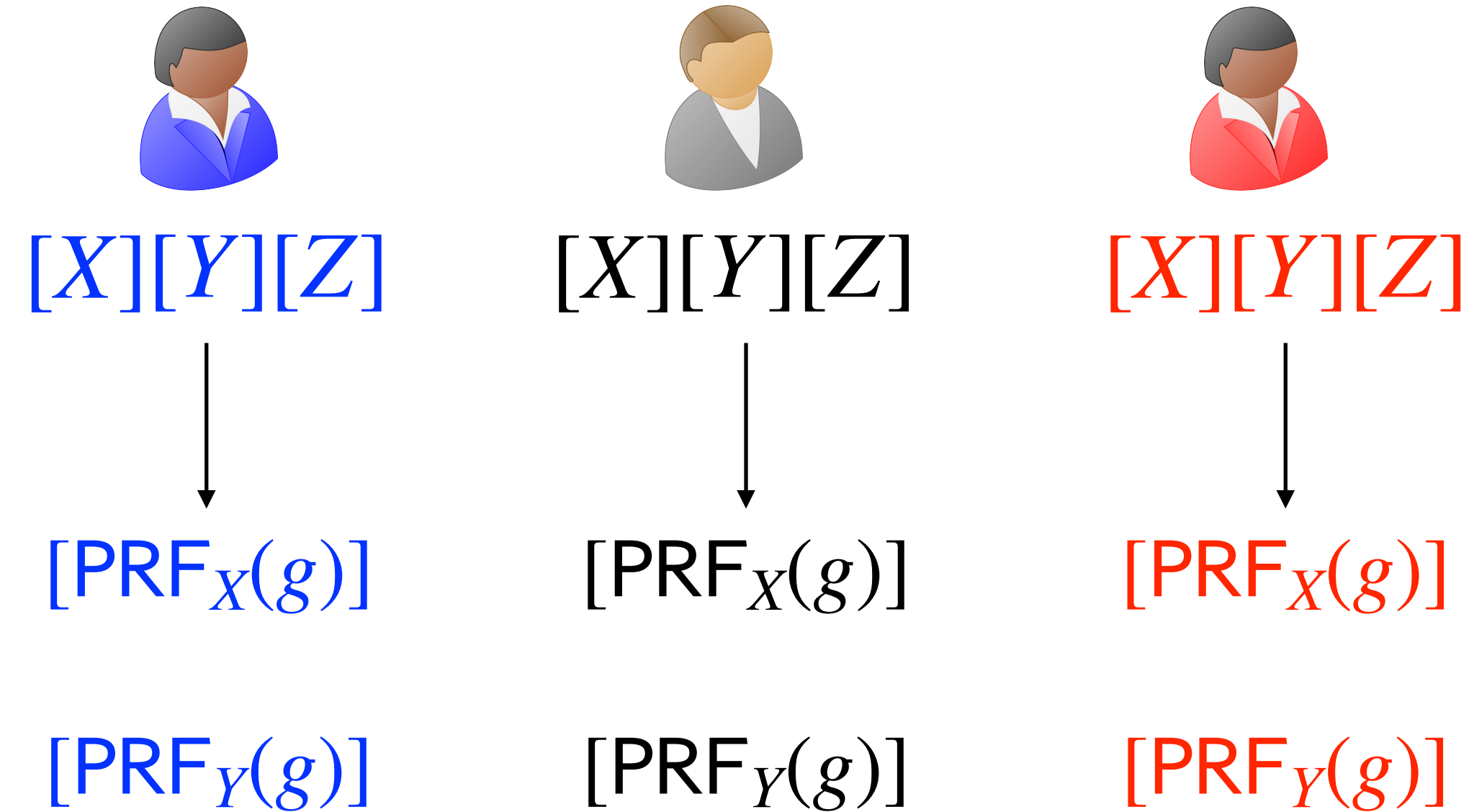


$[X][Y][Z]$

Garbling Using Doubly Linear Homomorphic PRFs

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

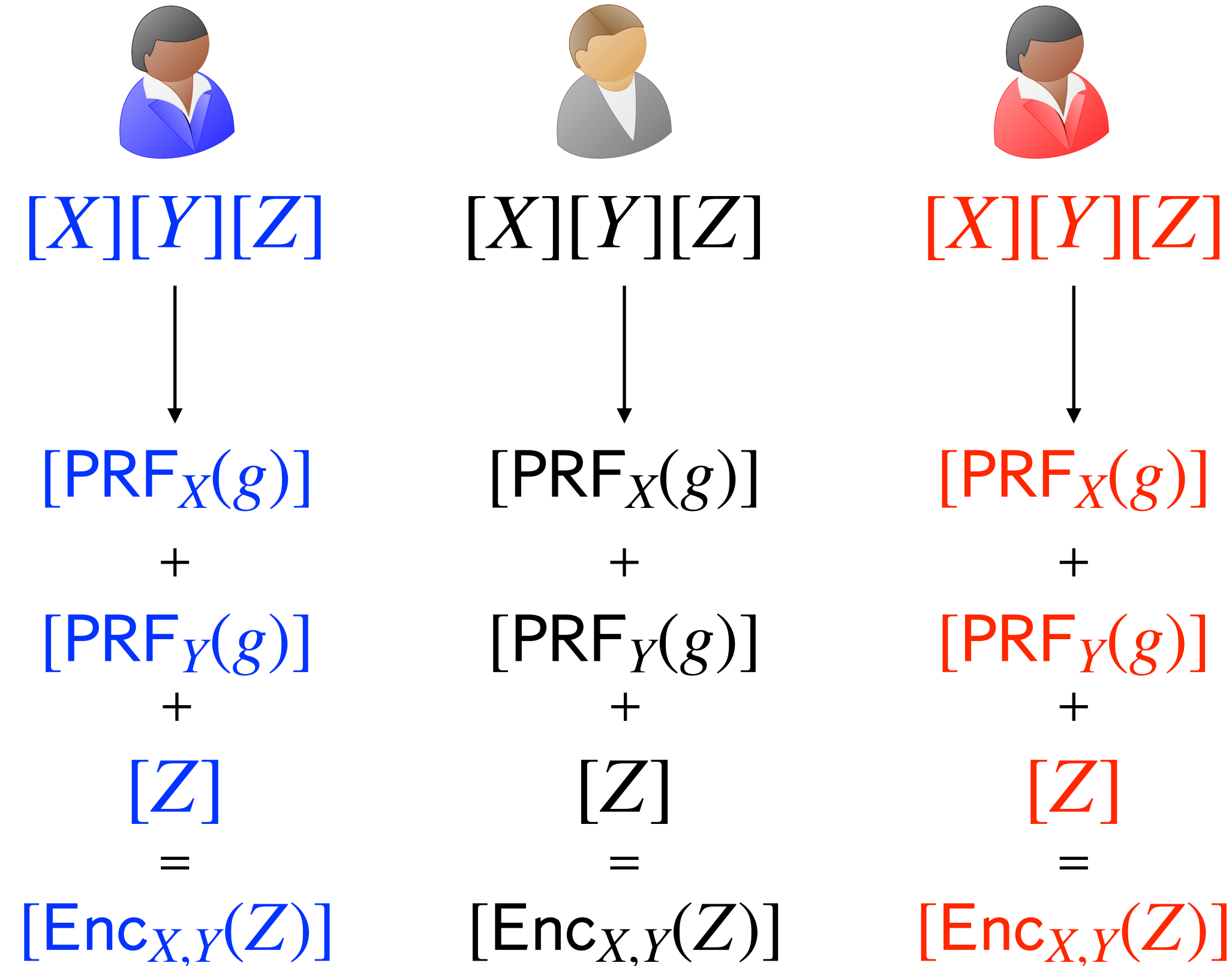
$$\text{PRF}_{[X]}(g) = [PRF_X(g)]$$



Garbling Using Doubly Linear Homomorphic PRFs

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

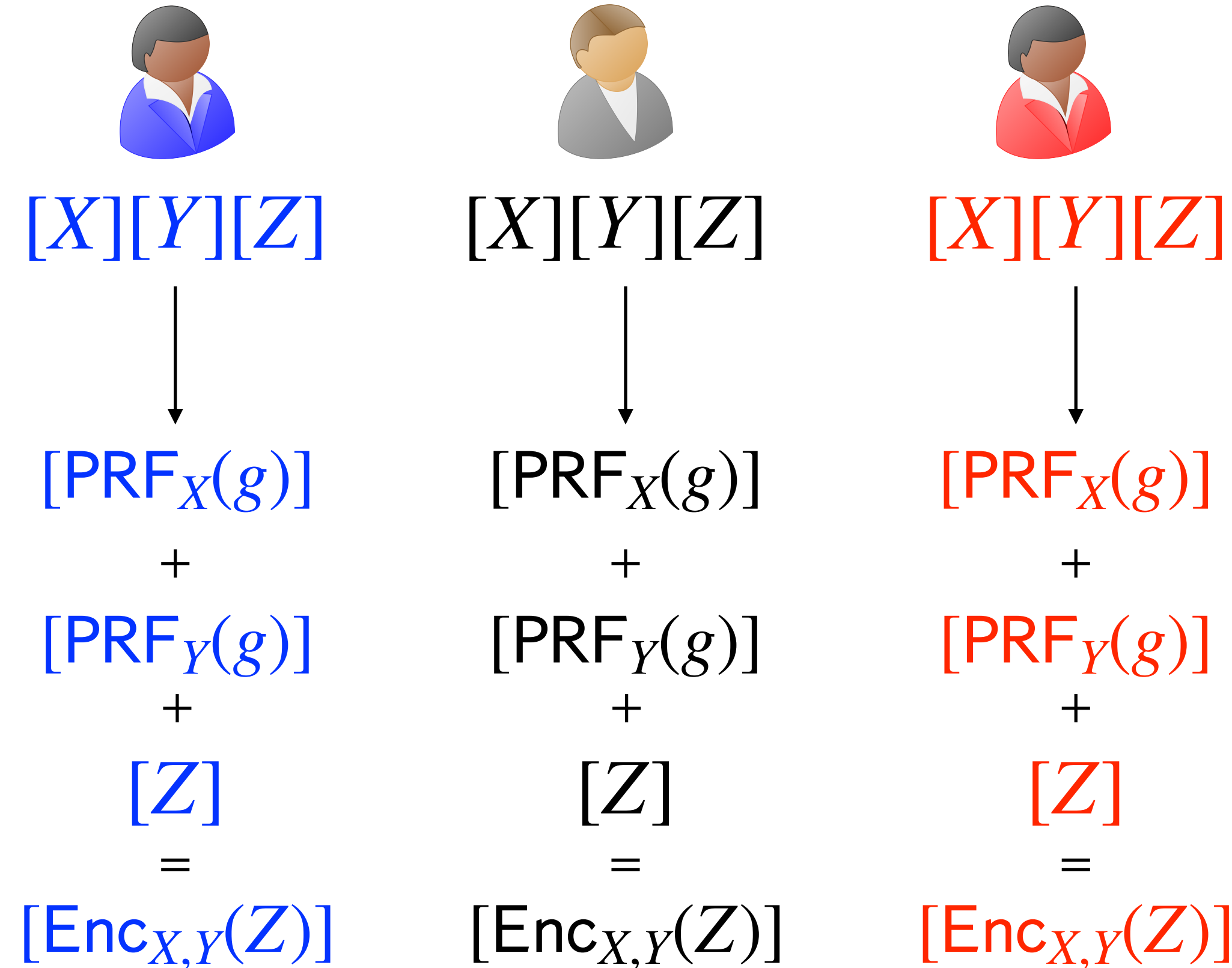
$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$



Garbling Using Doubly Linear Homomorphic PRFs

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [PRF_X(g)]$$



No known constructions of
DLH-PRFs.

Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

Expand labels using LPN: $R_k = k \cdot A + \epsilon$

Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$

Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

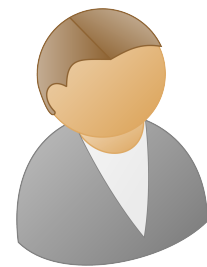
$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

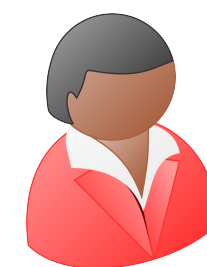
Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$



$[X][Y][Z]$



$[X][Y][Z]$



$[X][Y][Z]$

Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

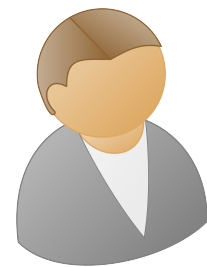
$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

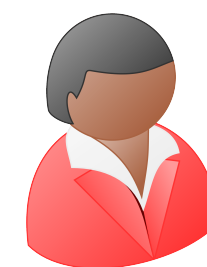
Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$



$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$



$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$



$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$

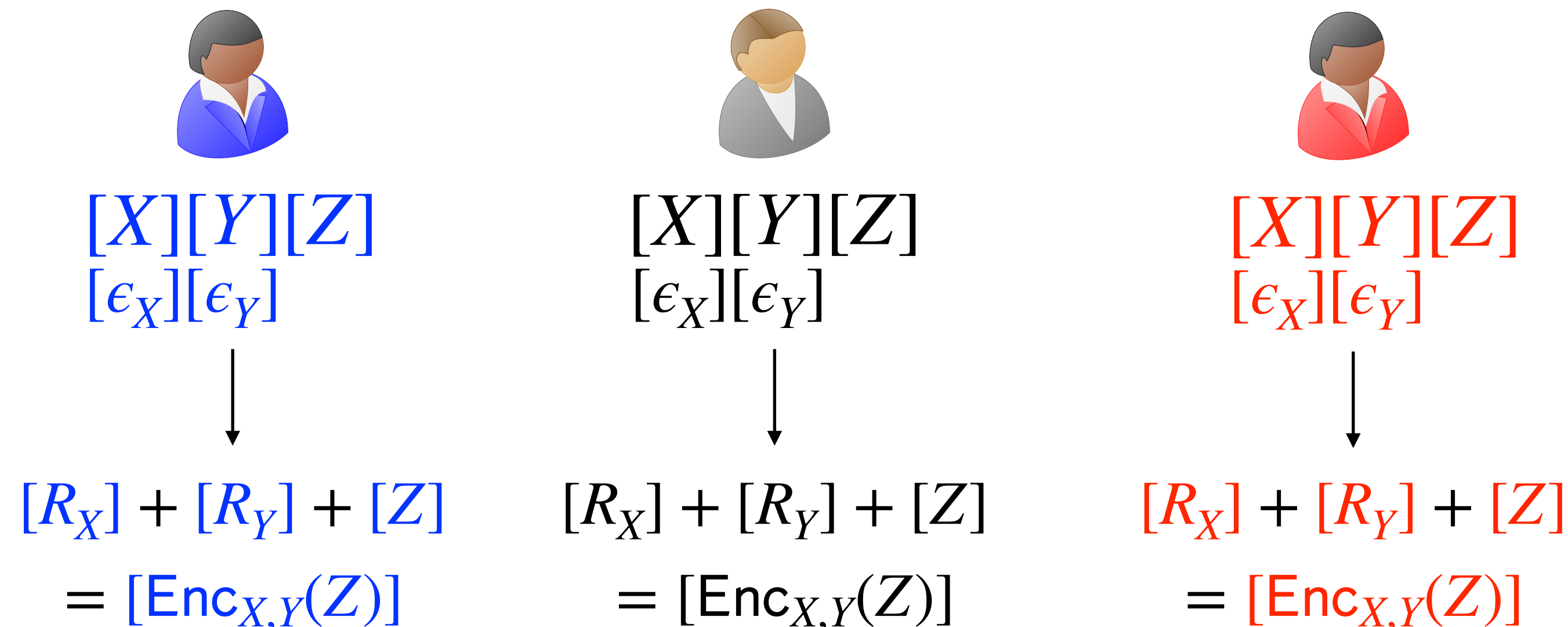
Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$



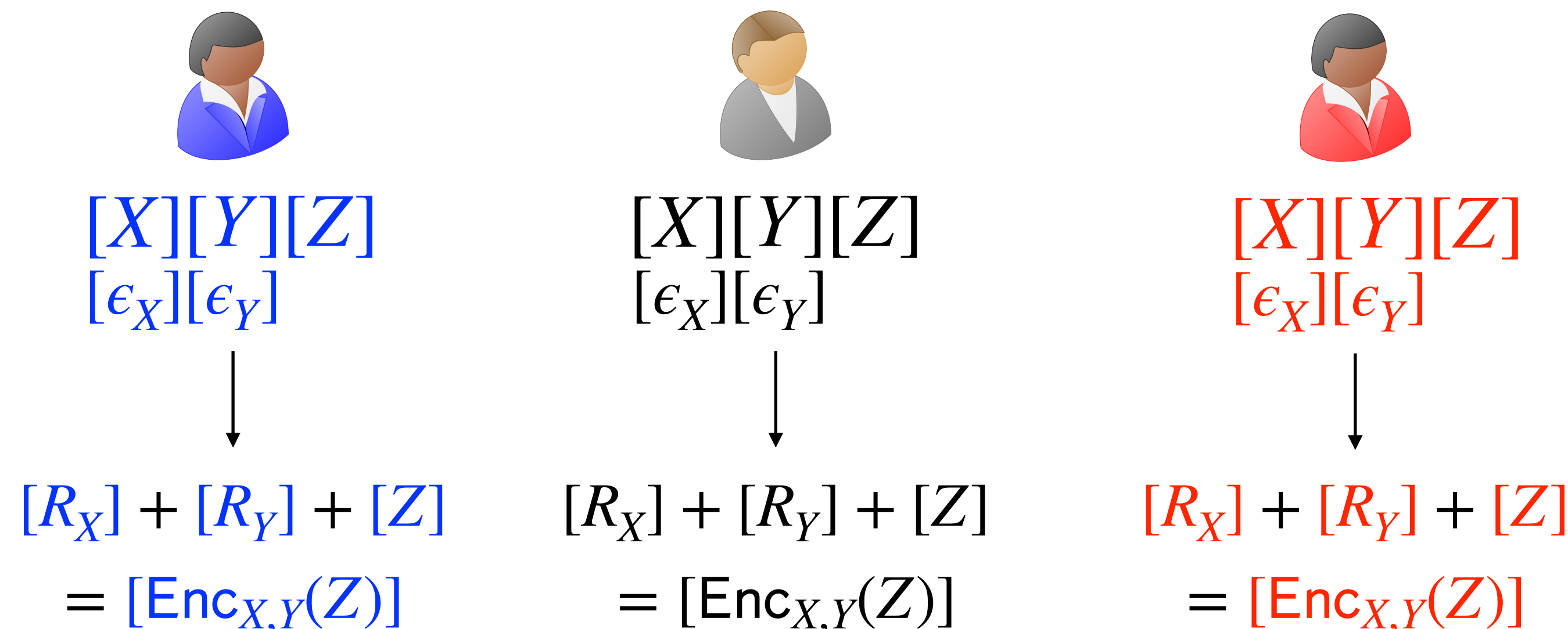
Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [\text{PRF}_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$



Avoid non-black box usage of encryption.

$|\mathcal{G}|$ is independent of number of parties.

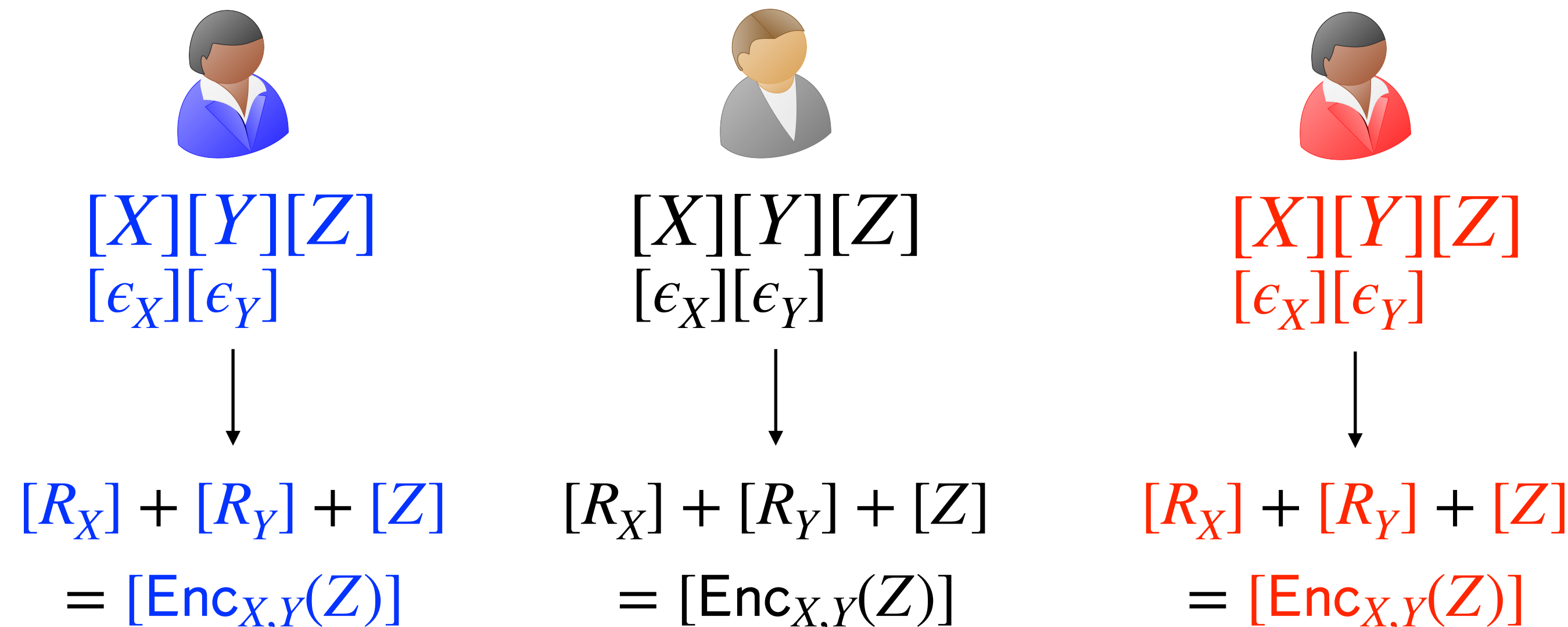
Garbling Using LPN

DLH-PRF: Secret sharing of the key $\rightarrow$ Secret sharing of PRF expansion.

$$\text{PRF}_{[X]}(g) = [PRF_X(g)]$$

Use pseudo-randomness from LPN instead of PRF evaluations.

Expand labels using LPN: $R_k = k \cdot A + \epsilon$ $[k] \cdot A + [\epsilon] = [R_k]$



Avoid non-black box usage of encryption.

$|\mathcal{G}|$ is independent of number of parties.

[BCOOSS21] used LPN for garbling in the dishonest majority setting.

Agenda

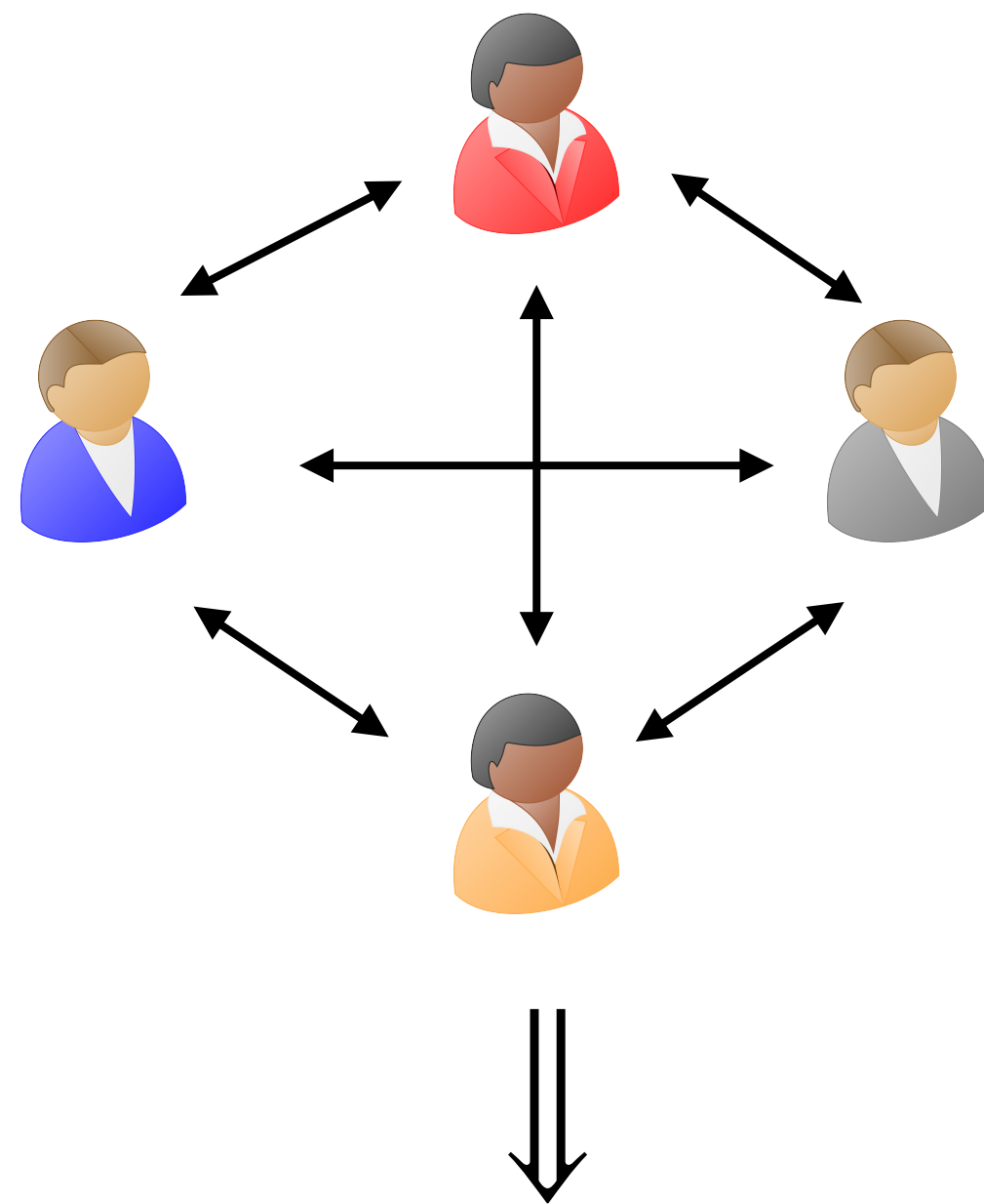
- Structure Of Garbled Circuit
 - Secret sharing of LPN keys and errors $\rightarrow$ secret sharing of ciphertext.
- Multiparty Garbling Protocol
- Efficiency analysis

Agenda

- Structure Of Garbled Circuit
- Multiparty Garbling Protocol
- Efficiency Analysis

Computing The Garbled Circuit

Pre-processing Phase



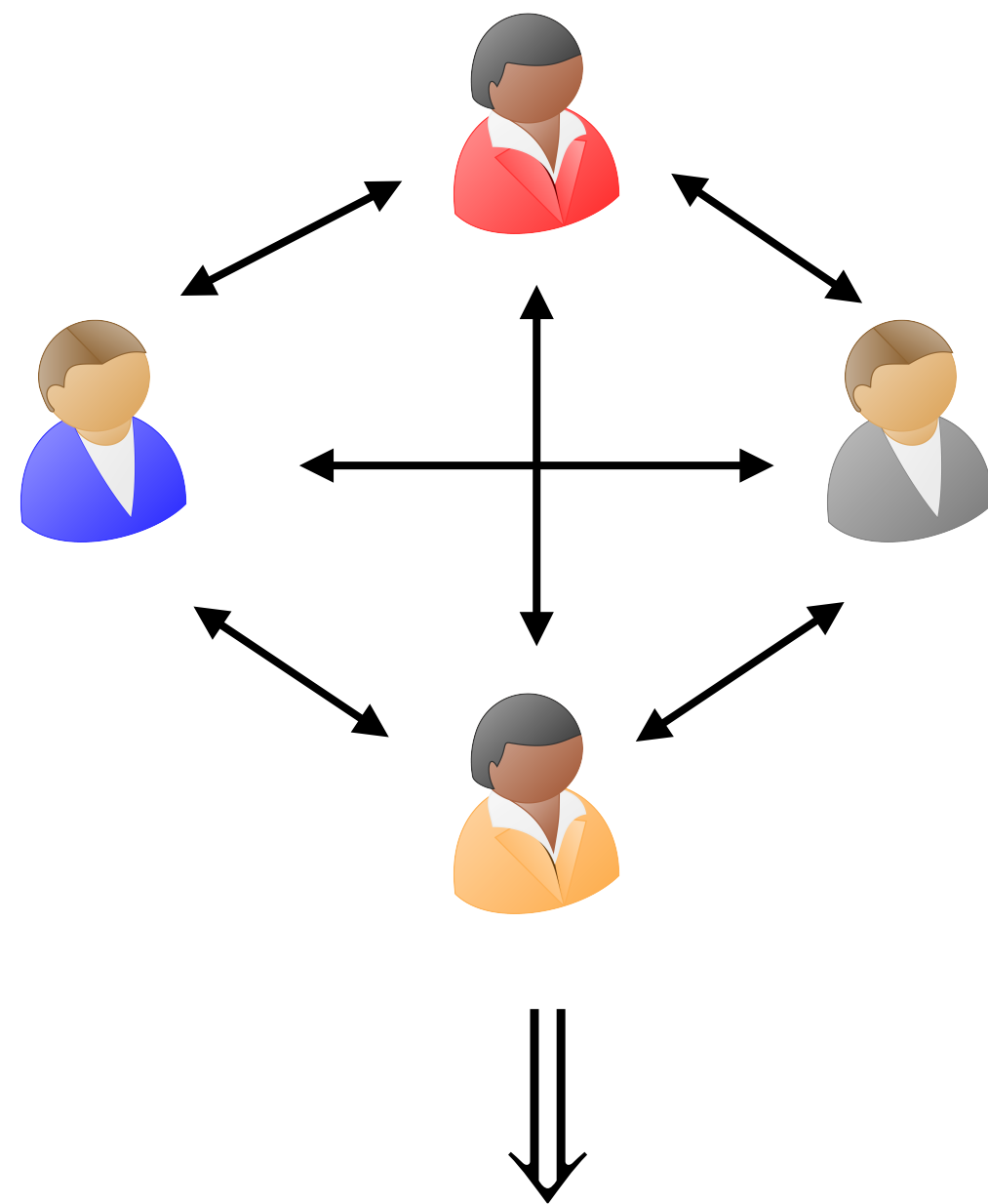
$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

Computing The Garbled Circuit

Pre-processing Phase

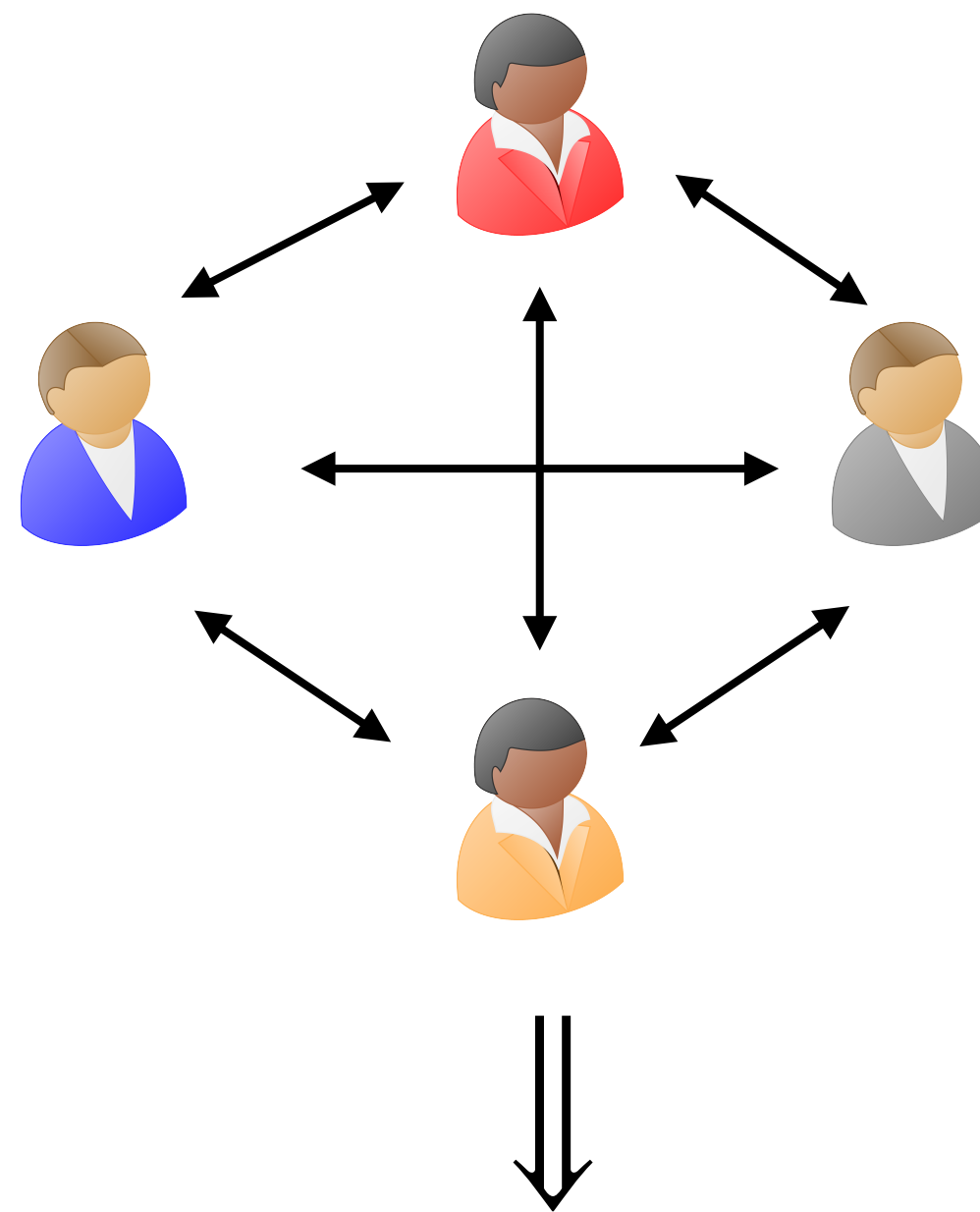


$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

Garbling Phase

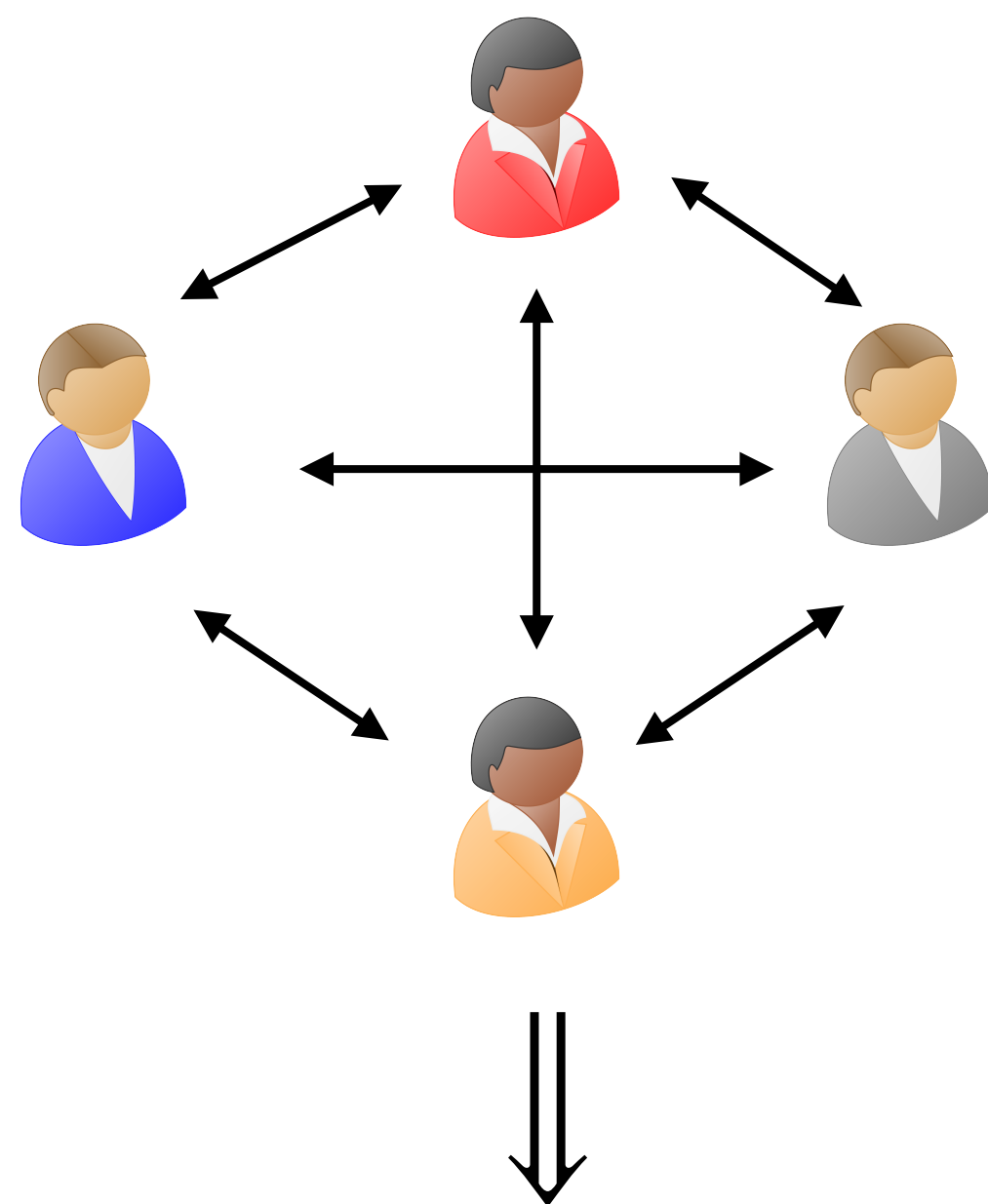


$\mathcal{G}$ - Garbled circuit

$\{X_{\text{inp}}\}$ - Wire labels for inputs

Computing The Garbled Circuit

Pre-processing Phase

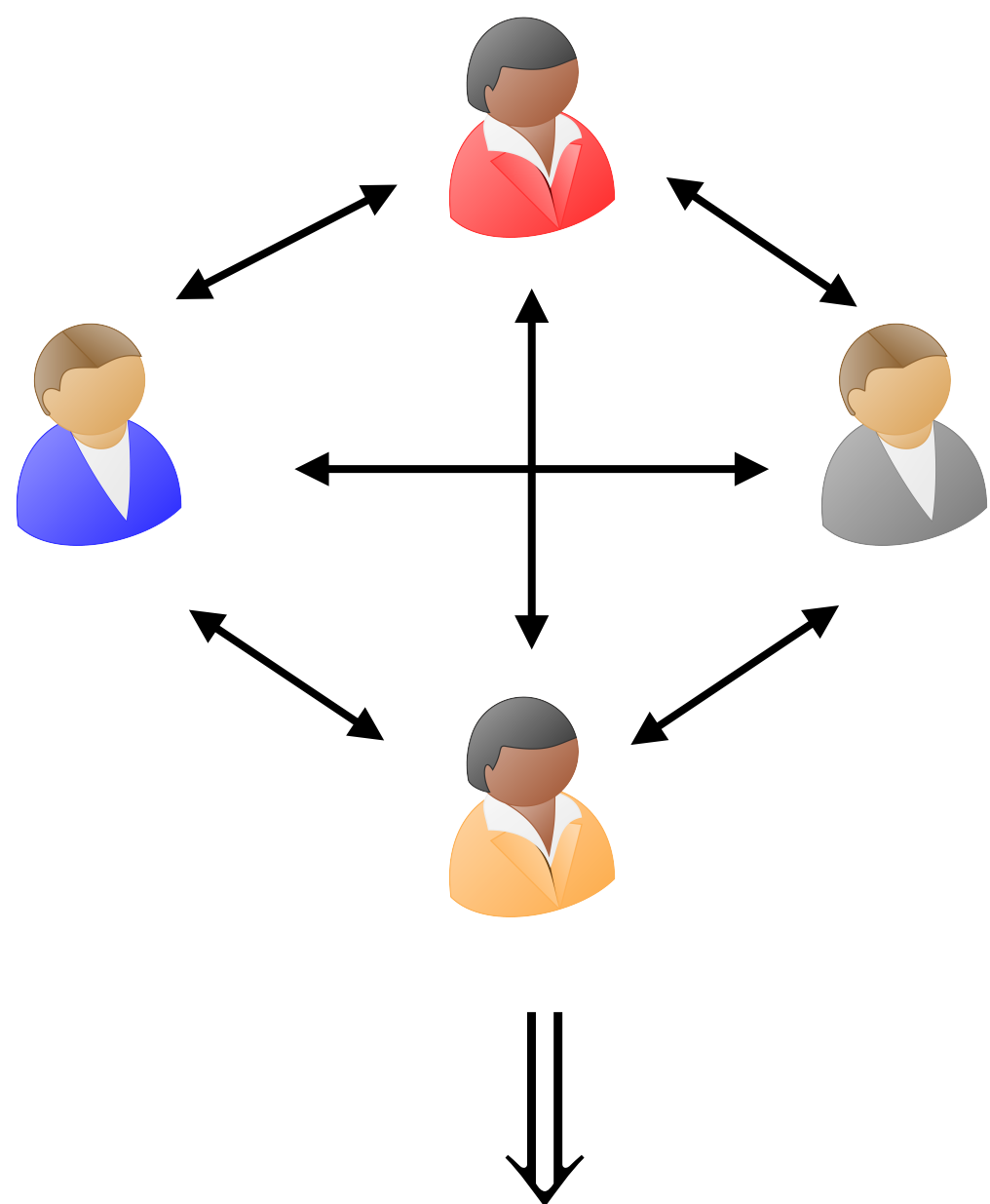


$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

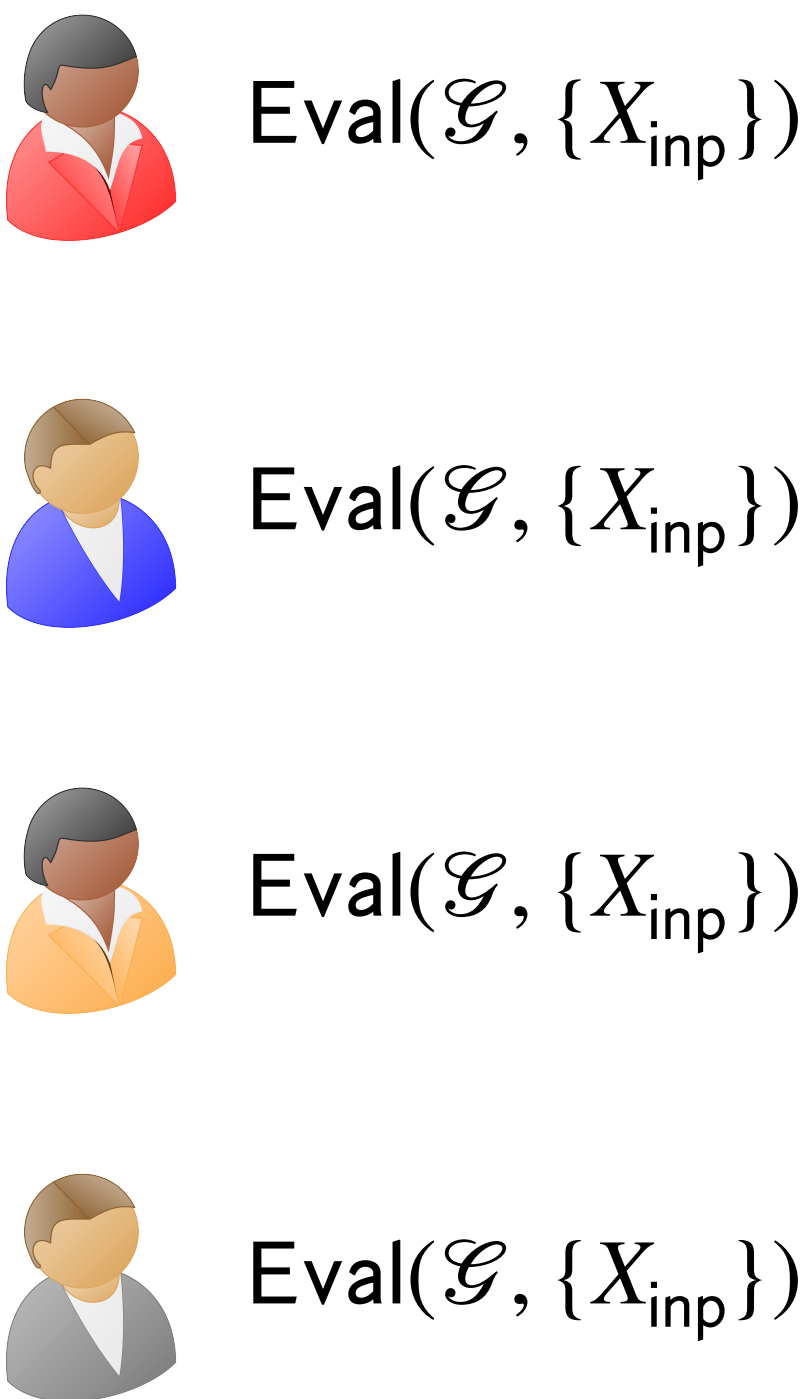
Garbling Phase



$\mathcal{G}$ - Garbled circuit

$\{X_{\text{inp}}\}$ - Wire labels for inputs

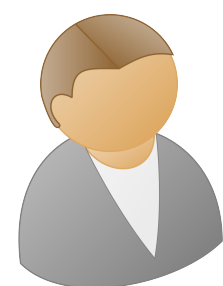
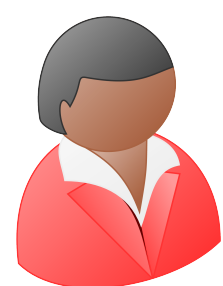
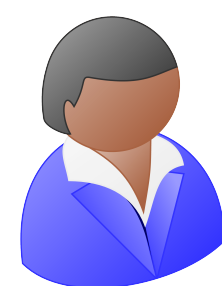
Evaluation Phase



Packed Secret Sharing [FY92]

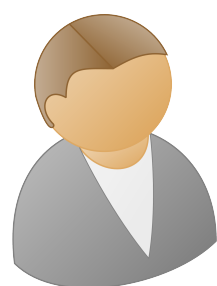
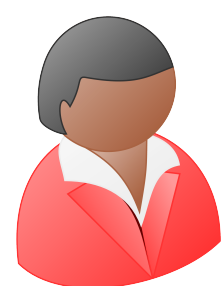
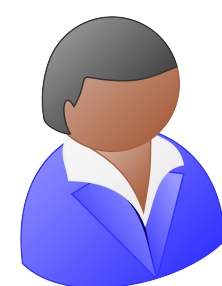
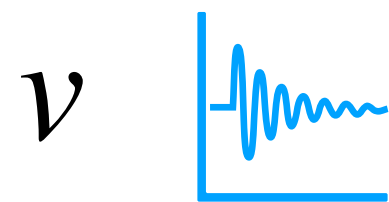
Regular Secret Sharing

v



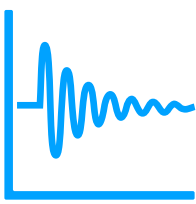
Packed Secret Sharing [FY92]

Regular Secret Sharing



Packed Secret Sharing [FY92]

Regular Secret Sharing

v 



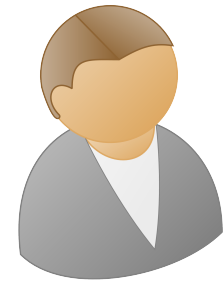
s_1



s_2



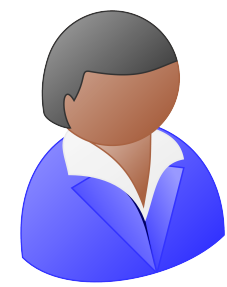
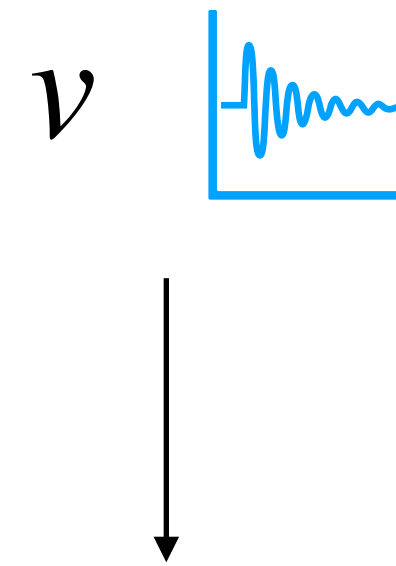
s_3



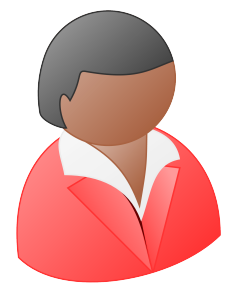
s_4

Packed Secret Sharing [FY92]

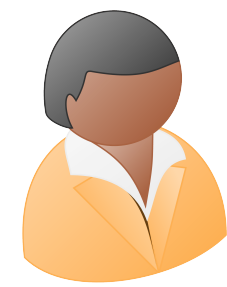
Regular Secret Sharing



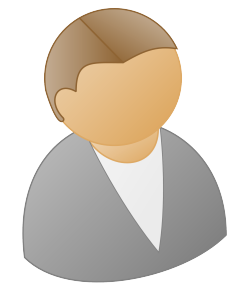
s_1



s_2



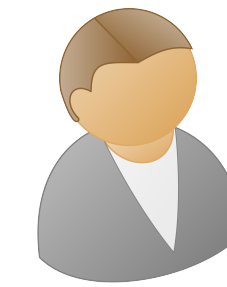
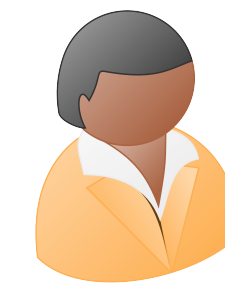
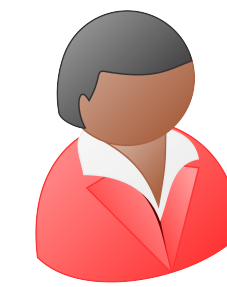
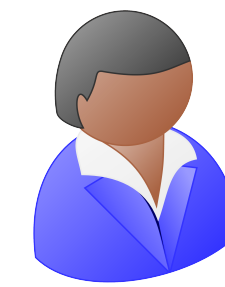
s_3



s_4

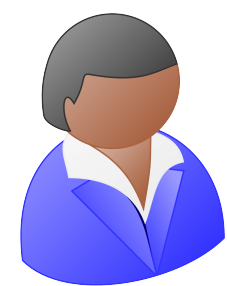
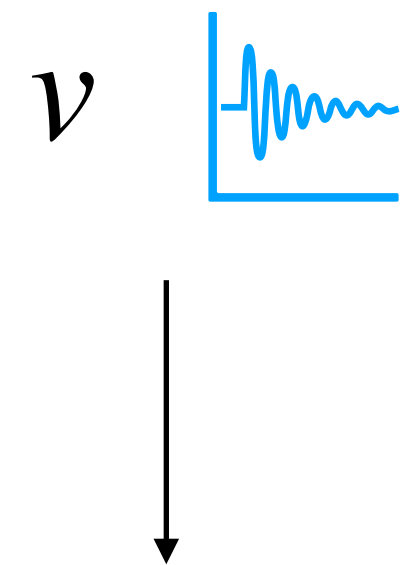
Packed Secret Sharing

v_1 v_2 v_3

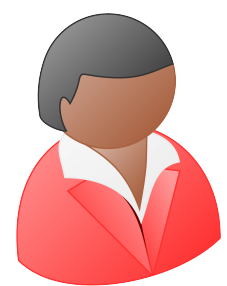


Packed Secret Sharing [FY92]

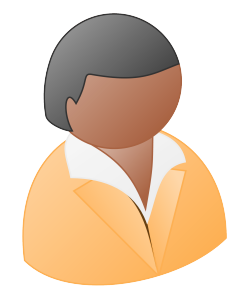
Regular Secret Sharing



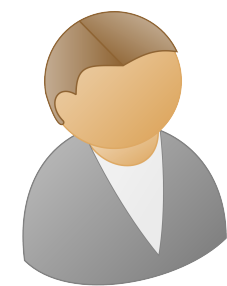
s_1



s_2

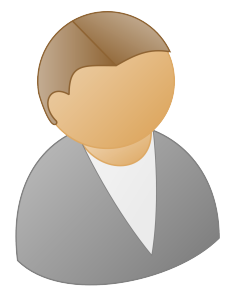
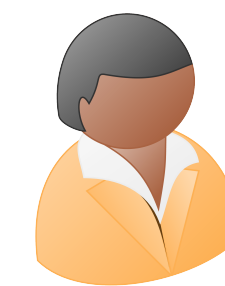
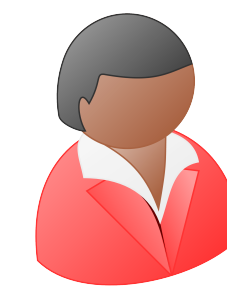
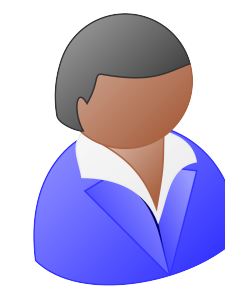
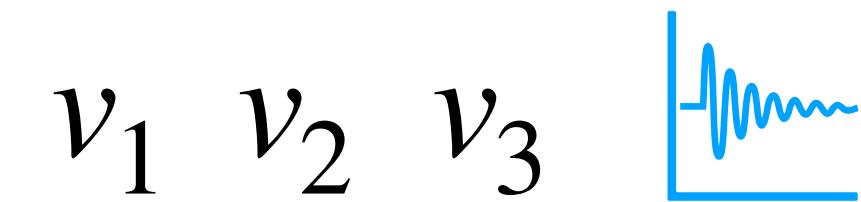


s_3



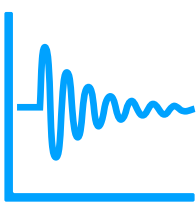
s_4

Packed Secret Sharing



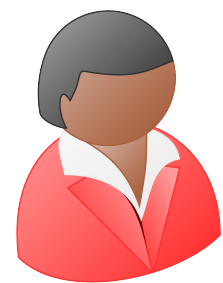
Packed Secret Sharing [FY92]

Regular Secret Sharing

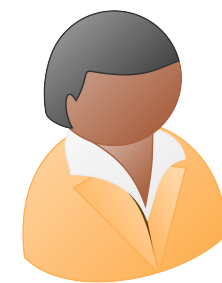
v 



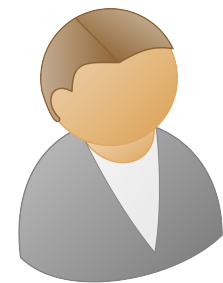
s_1



s_2

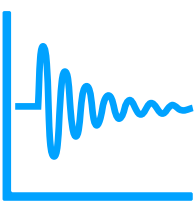


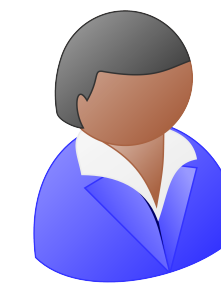
s_3



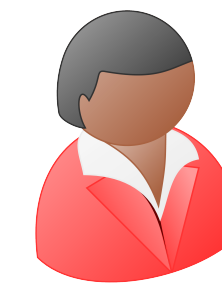
s_4

Packed Secret Sharing

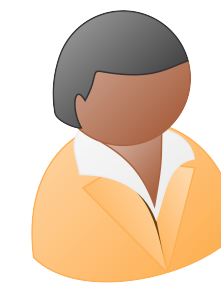
v_1 v_2 v_3 



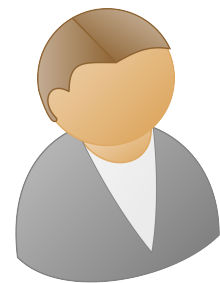
s_1



s_2



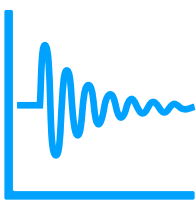
s_3

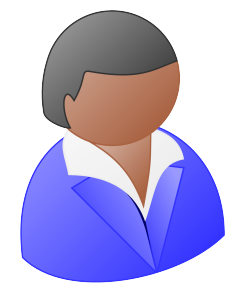


s_4

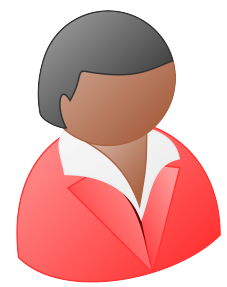
Packed Secret Sharing [FY92]

Regular Secret Sharing

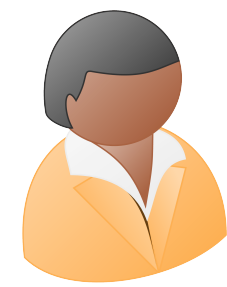
v 



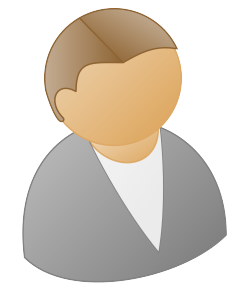
s_1



s_2



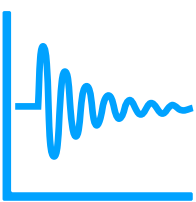
s_3

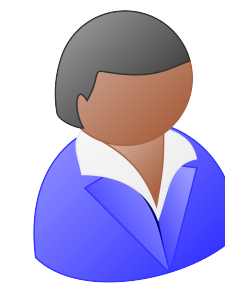


s_4

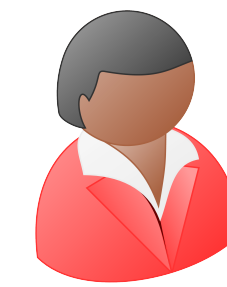
n shares $\rightarrow$ 1 secret

Packed Secret Sharing

v_1 v_2 v_3 



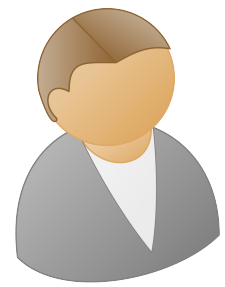
s_1



s_2



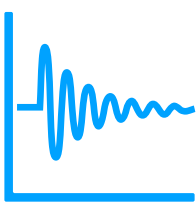
s_3

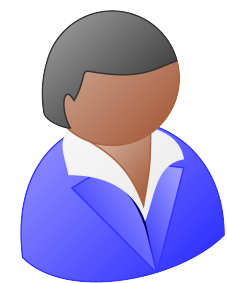


s_4

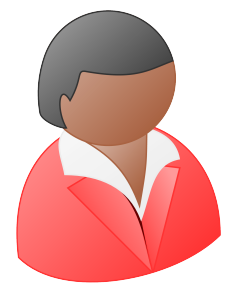
Packed Secret Sharing [FY92]

Regular Secret Sharing

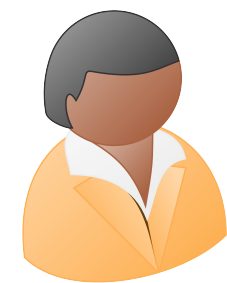
v 



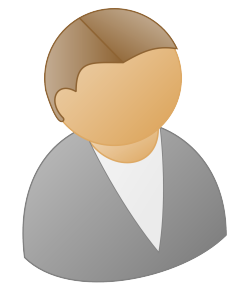
s_1



s_2



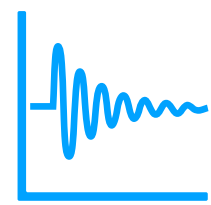
s_3

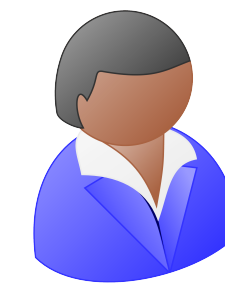


s_4

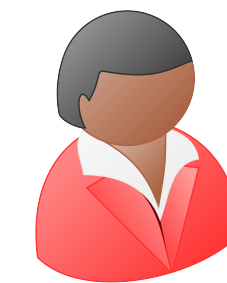
n shares $\rightarrow$ 1 secret

Packed Secret Sharing

v_1 v_2 v_3 



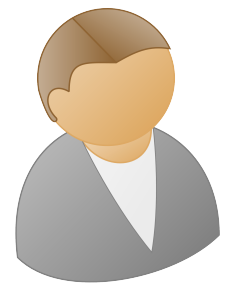
s_1



s_2



s_3

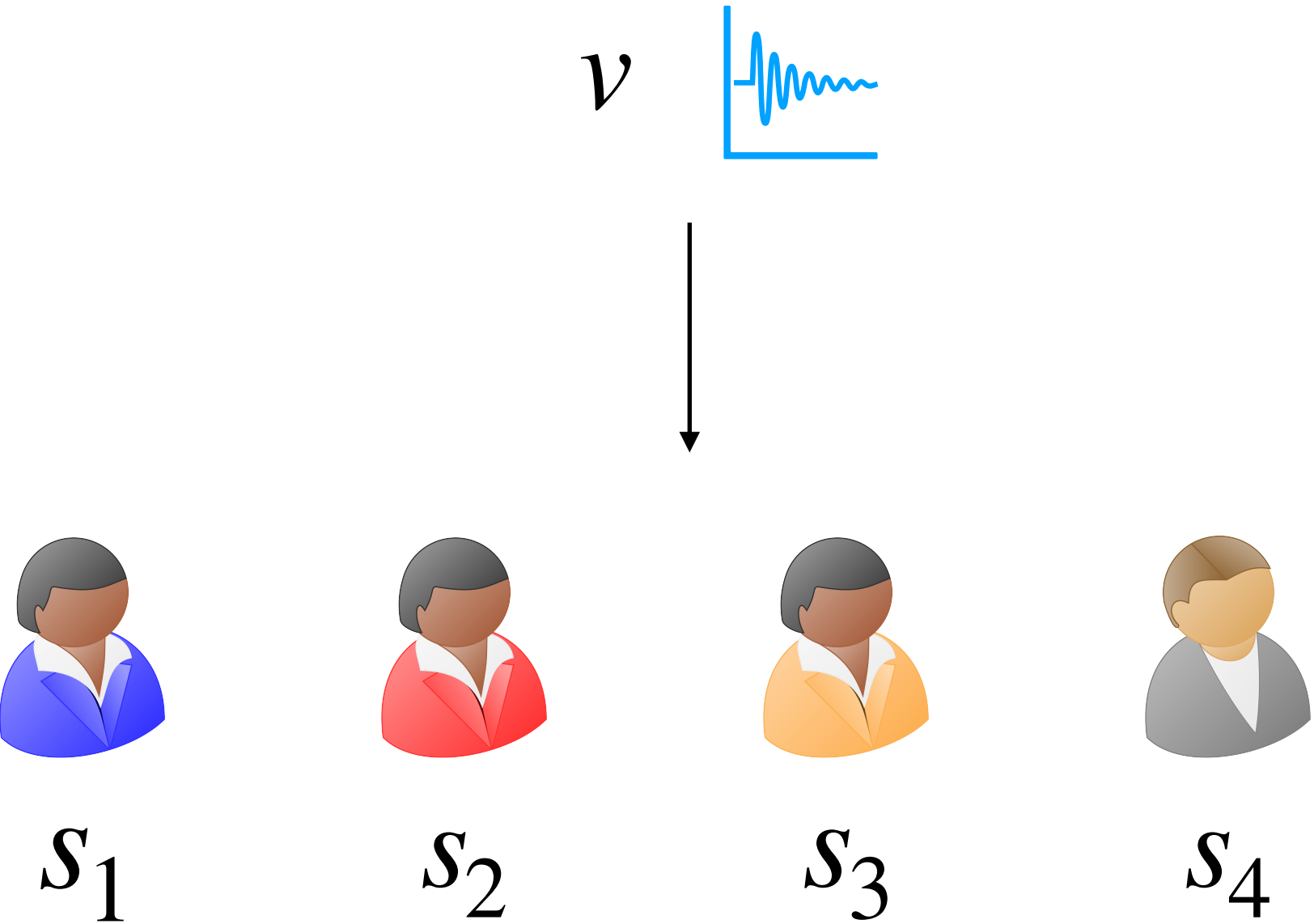


s_4

n shares $\rightarrow \ell$ secrets

Packed Secret Sharing [FY92]

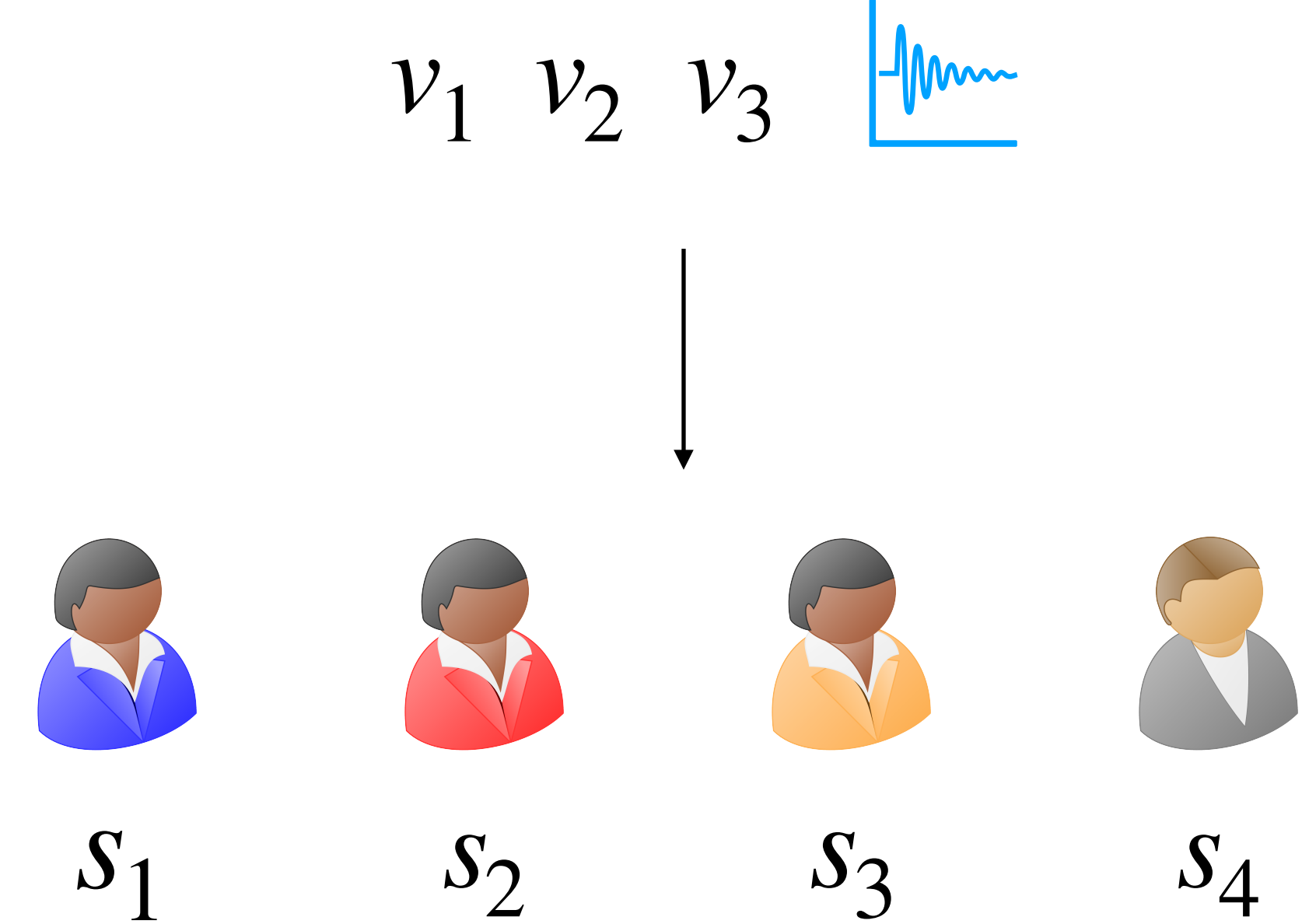
Regular Secret Sharing



n shares $\rightarrow$ 1 secret

Cannot
reconstruct $\leq t$

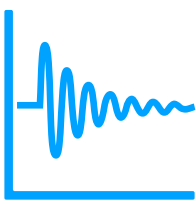
Packed Secret Sharing

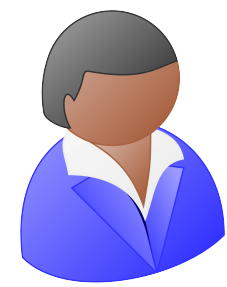


n shares $\rightarrow \ell$ secrets

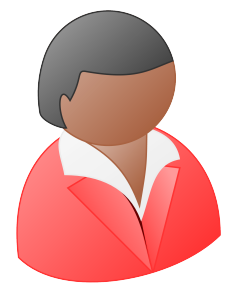
Packed Secret Sharing [FY92]

Regular Secret Sharing

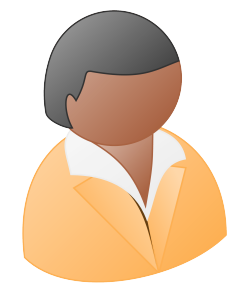
v 



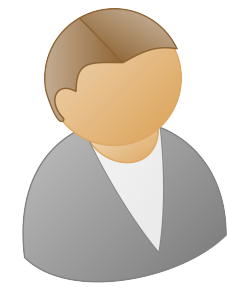
s_1



s_2



s_3

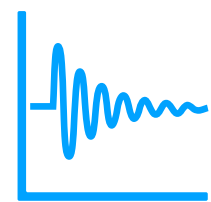


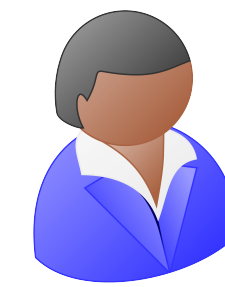
s_4

n shares $\rightarrow$ 1 secret

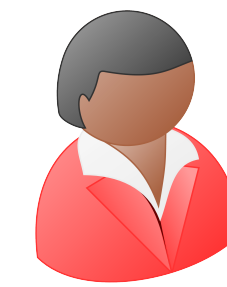
Cannot
reconstruct $\leq t <$ Can
reconstruct

Packed Secret Sharing

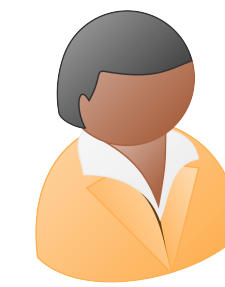
v_1 v_2 v_3 



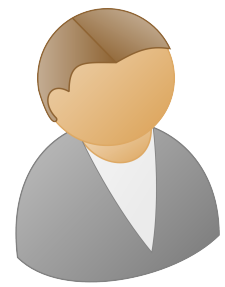
s_1



s_2



s_3

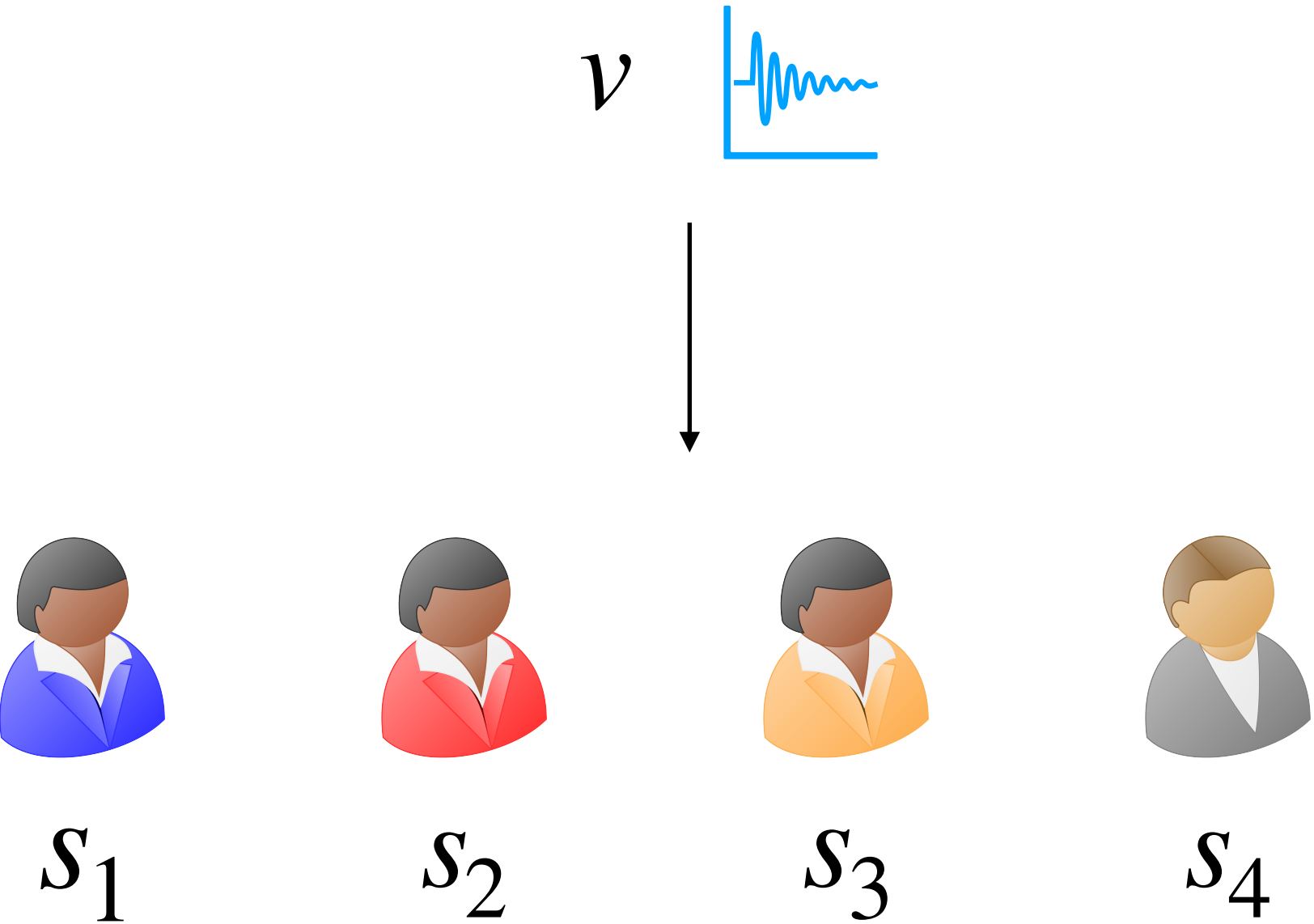


s_4

n shares $\rightarrow \ell$ secrets

Packed Secret Sharing [FY92]

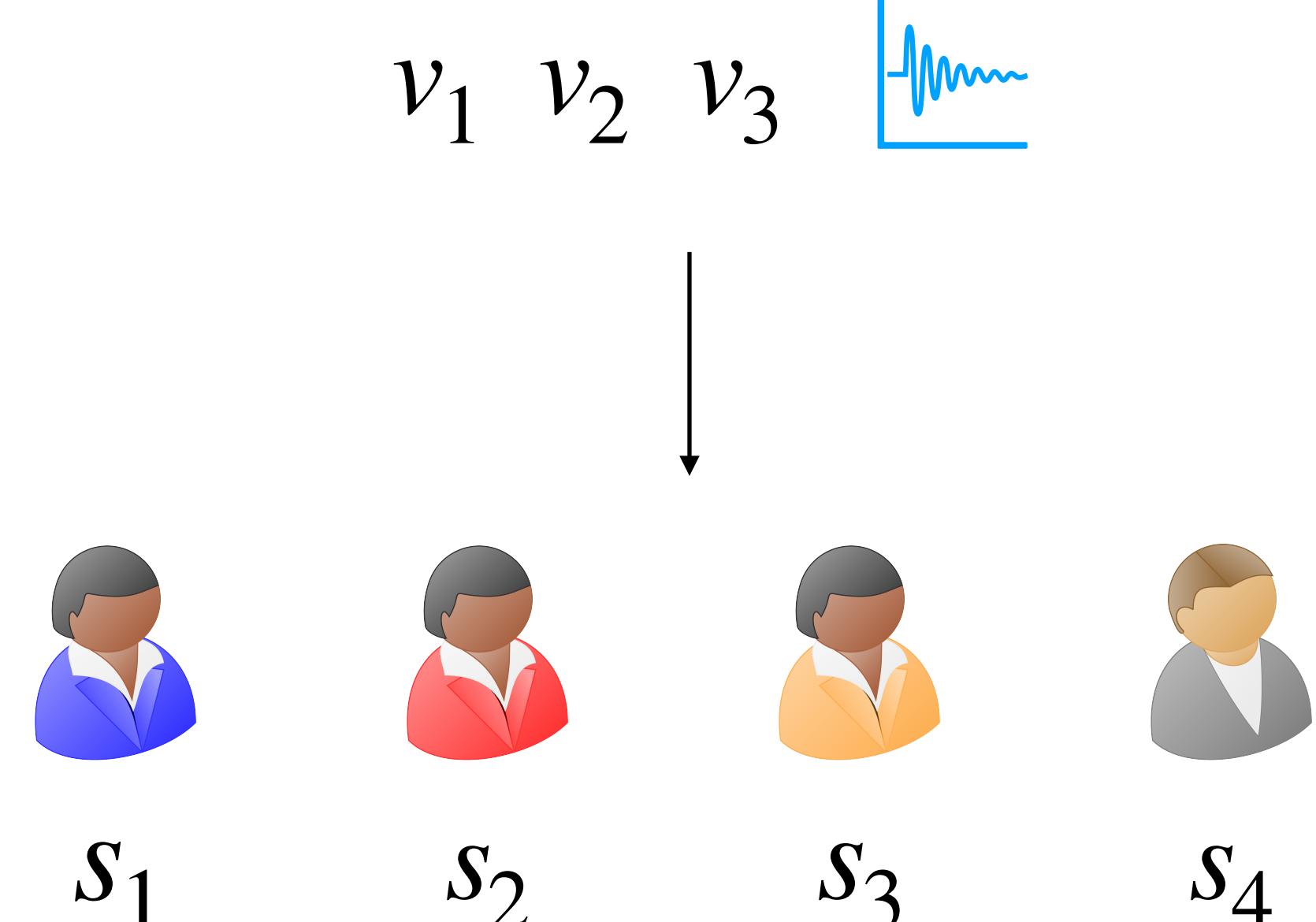
Regular Secret Sharing



n shares $\rightarrow$ 1 secret

Cannot reconstruct $\leq t <$ Can reconstruct

Packed Secret Sharing

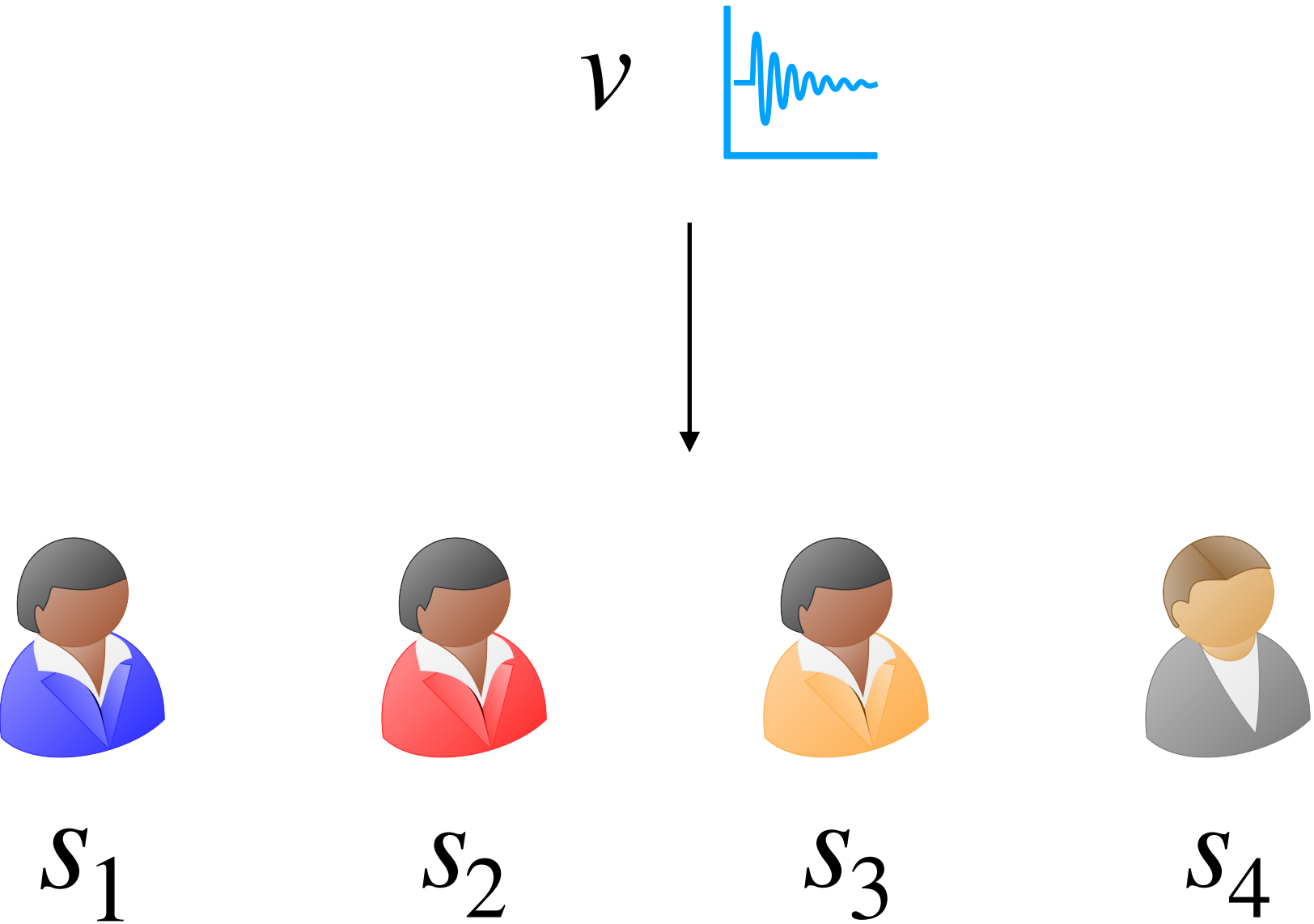


n shares $\rightarrow \ell$ secrets

Cannot reconstruct $\leq t$

Packed Secret Sharing [FY92]

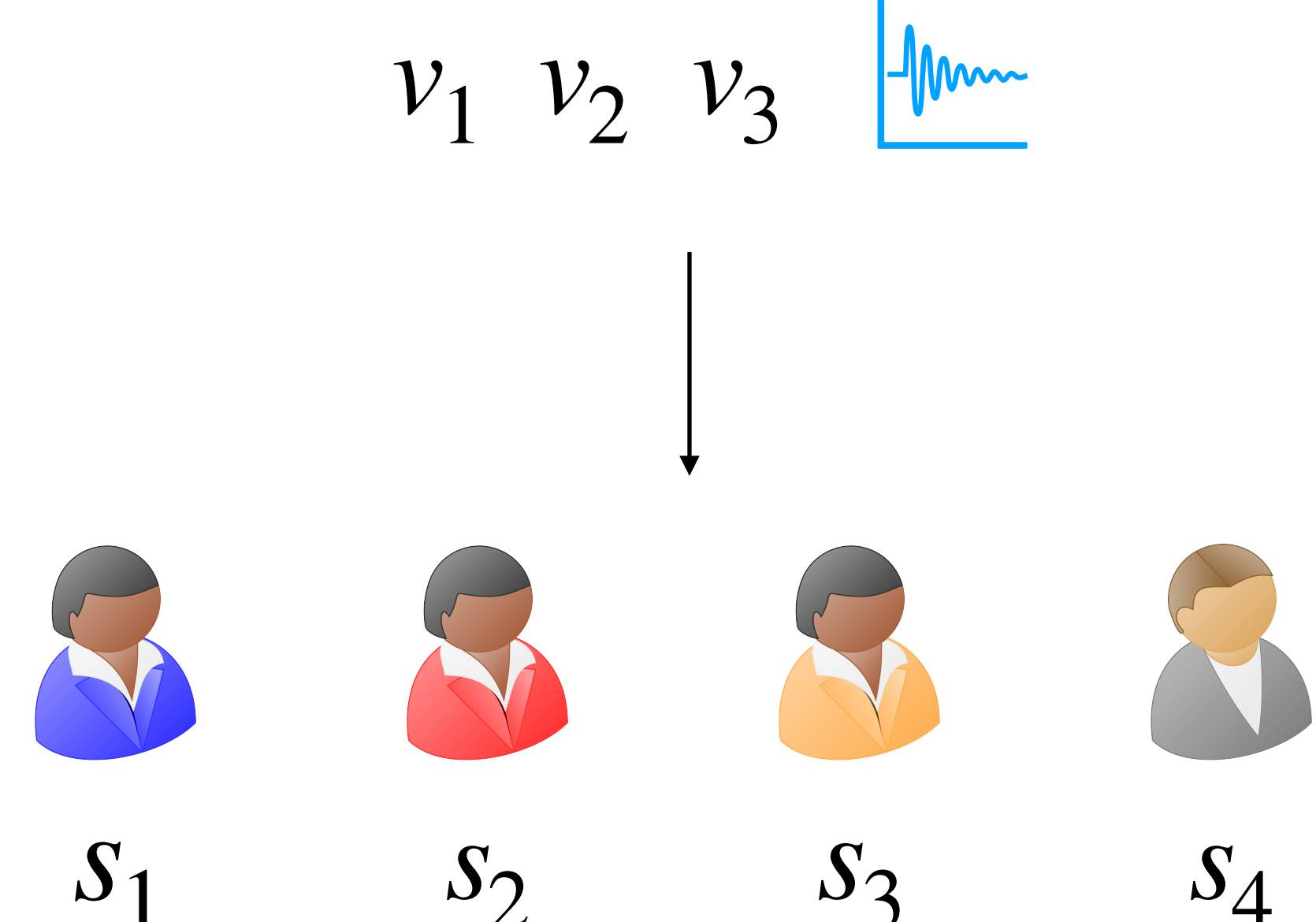
Regular Secret Sharing



n shares $\rightarrow$ 1 secret

Cannot reconstruct $\leq t <$ Can reconstruct

Packed Secret Sharing

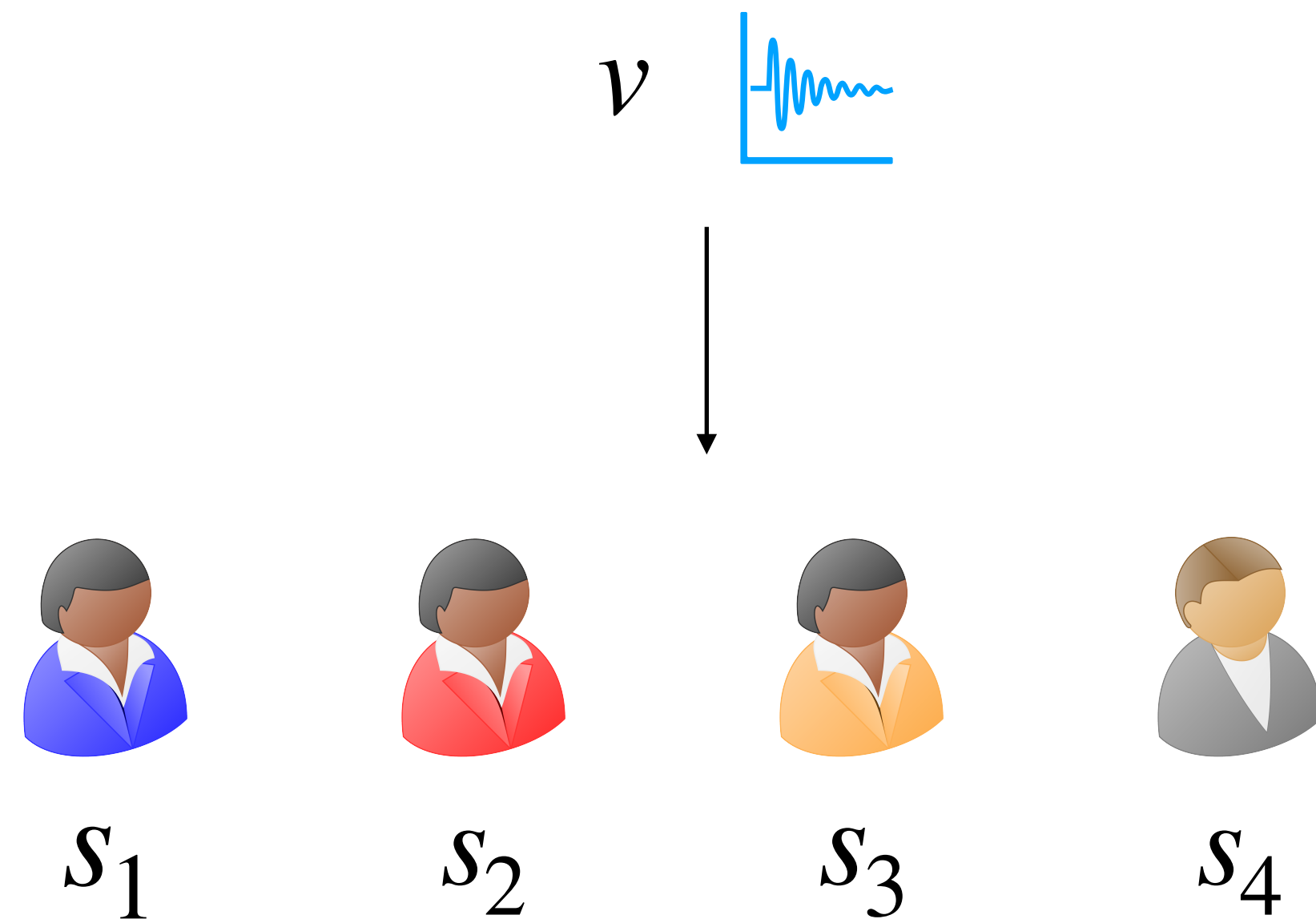


n shares $\rightarrow \ell$ secrets

Cannot reconstruct $\leq t$ $t + \ell \leq$ Can reconstruct

Packed Secret Sharing [FY92]

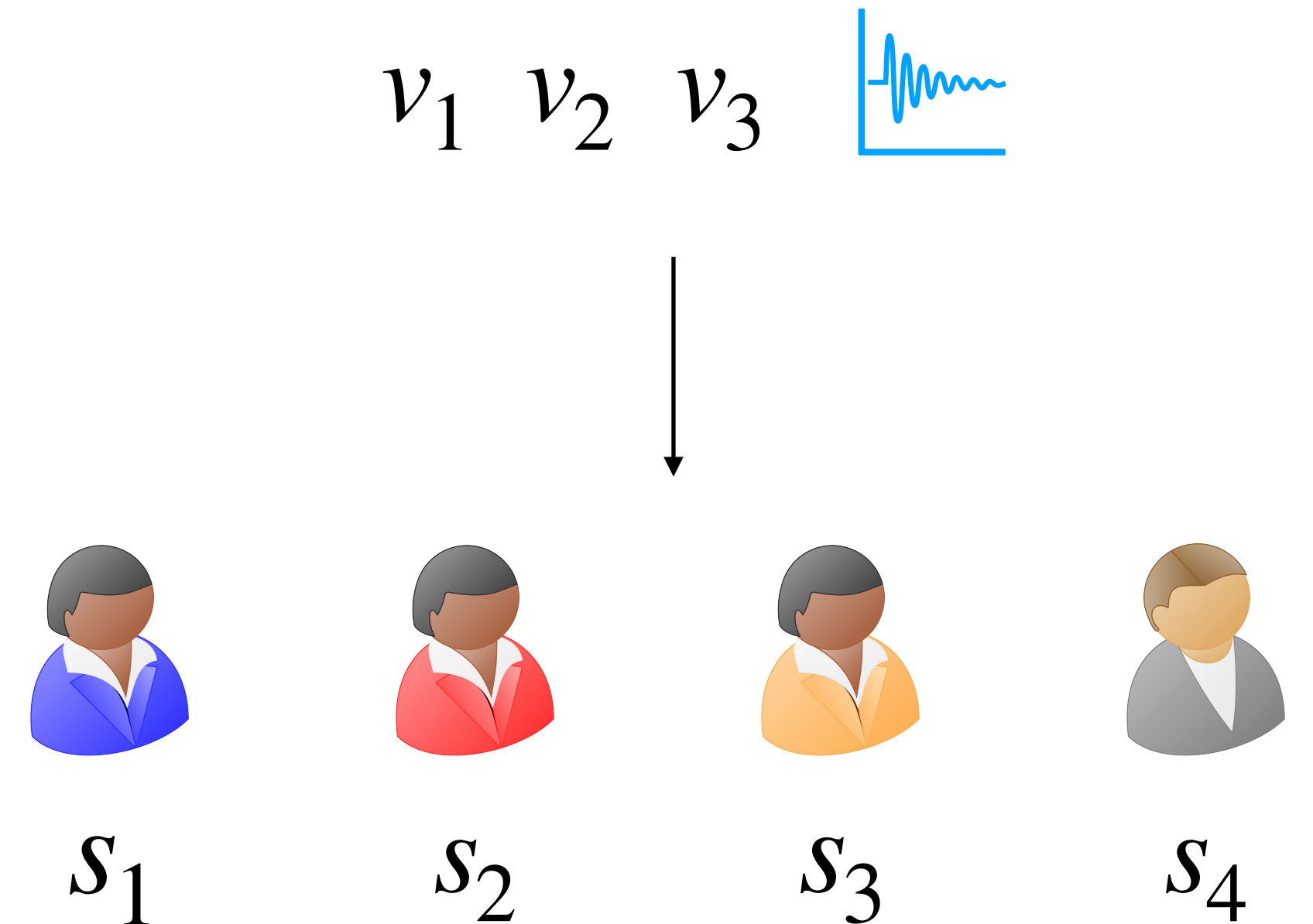
Regular Secret Sharing



n shares $\rightarrow$ 1 secret

Cannot reconstruct $\leq t <$ Can reconstruct

Packed Secret Sharing



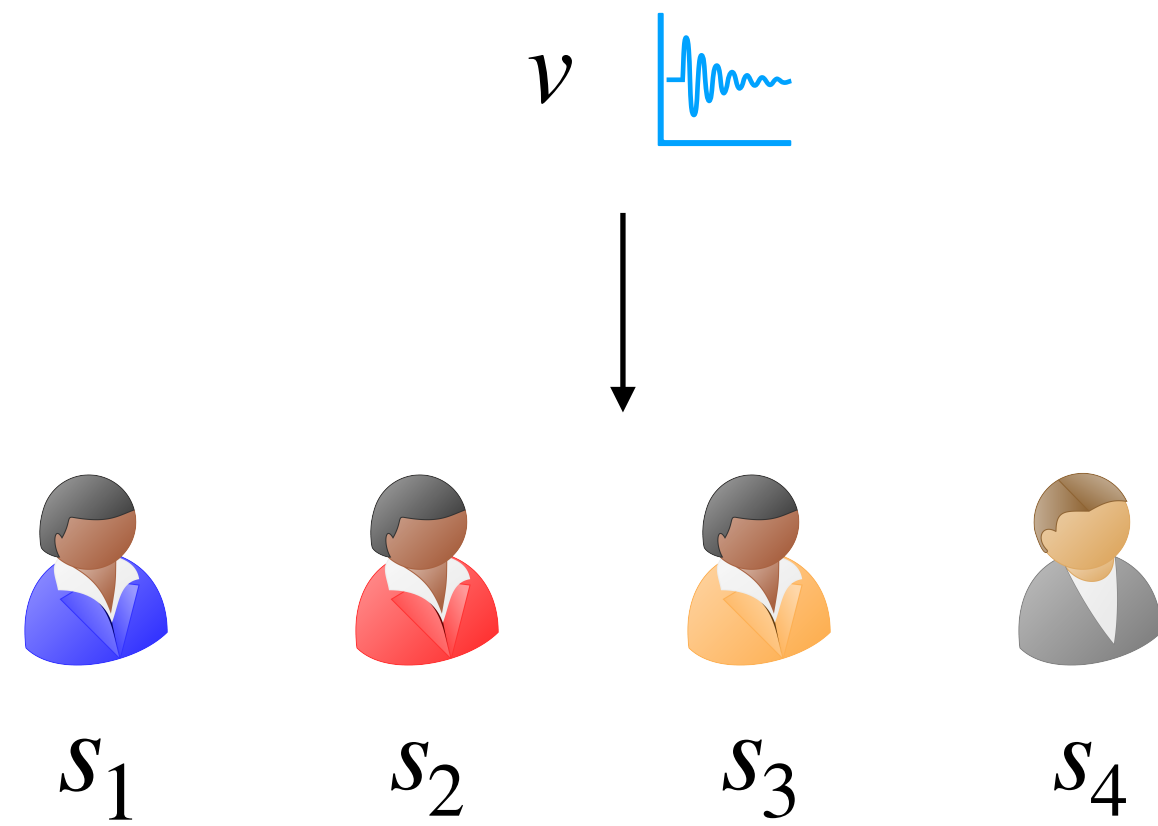
n shares $\rightarrow \ell$ secrets

Cannot reconstruct $\leq t$ $t + \ell \leq$ Can reconstruct

$$|\mathbb{F}| > n + \ell$$

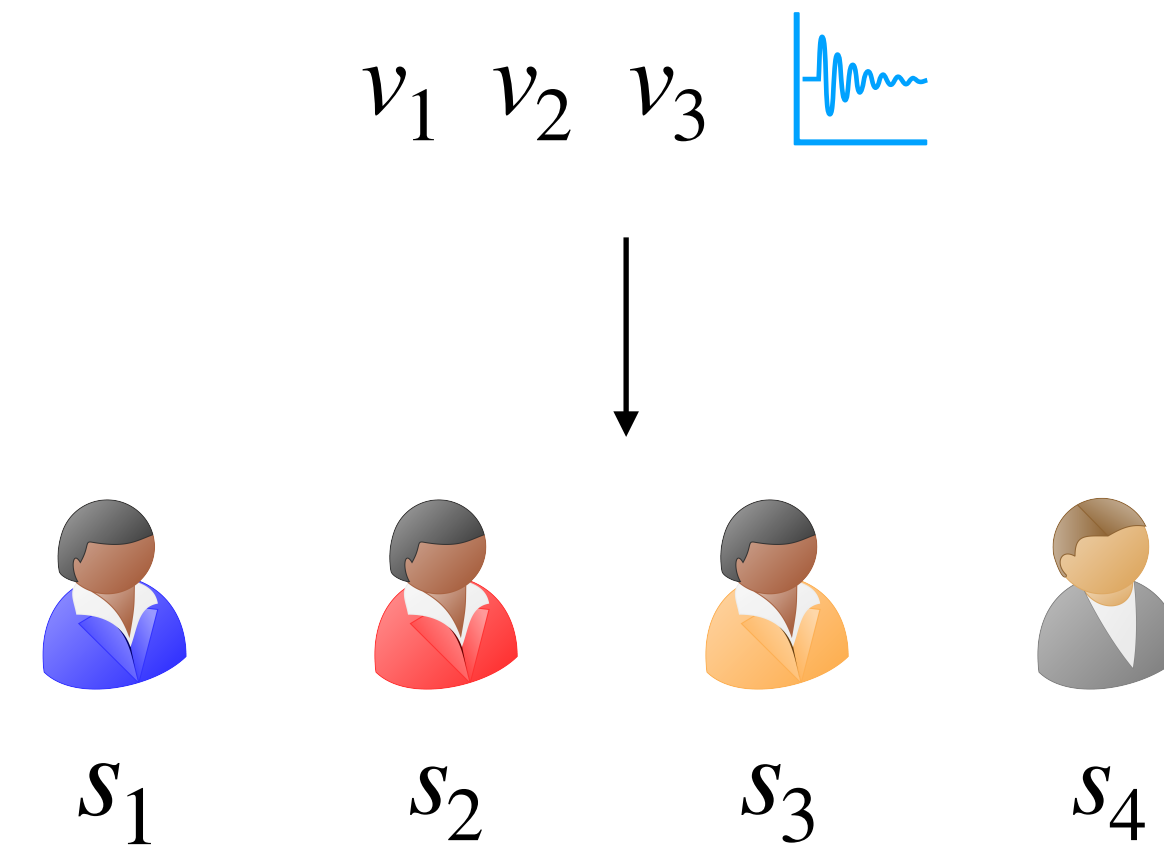
MPC Using Packed Secret Sharing

Regular Sharing



$O(n |C|)$ communication MPC

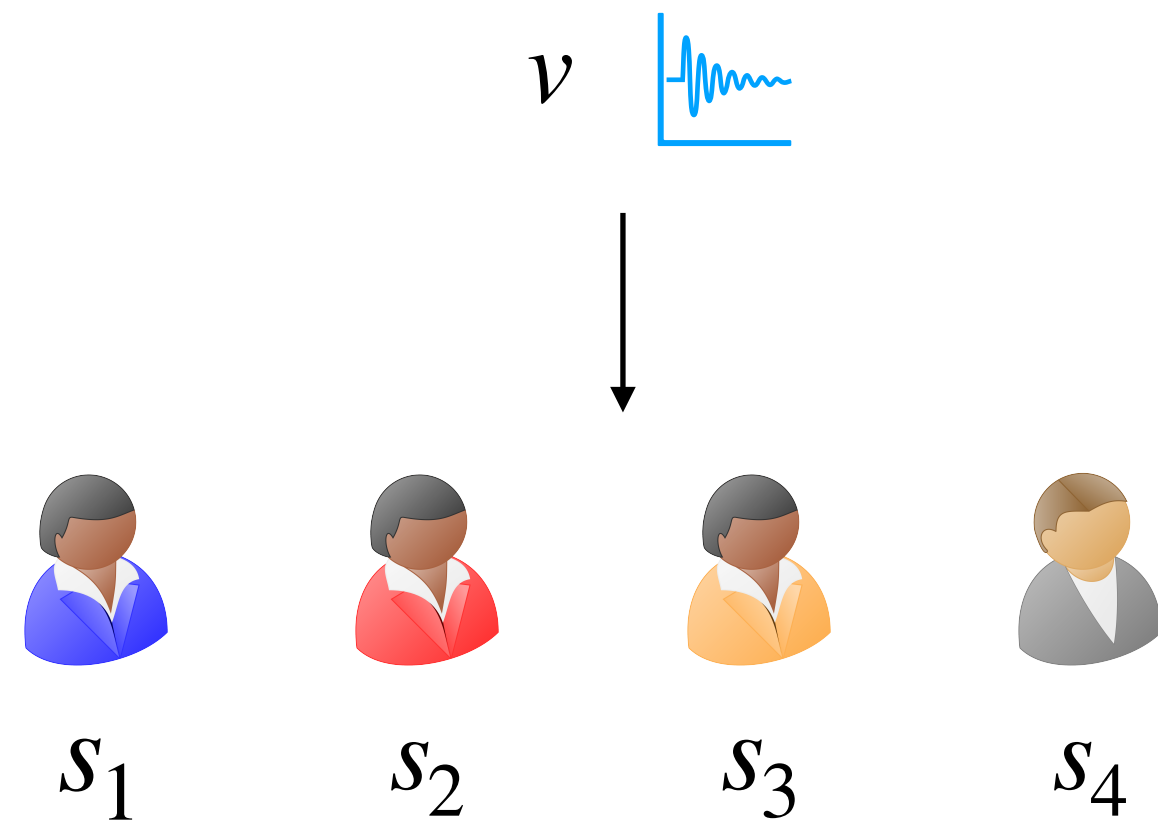
Packed Sharing



$\ell = O(n) \implies O(|C|)$ communication MPC

MPC Using Packed Secret Sharing

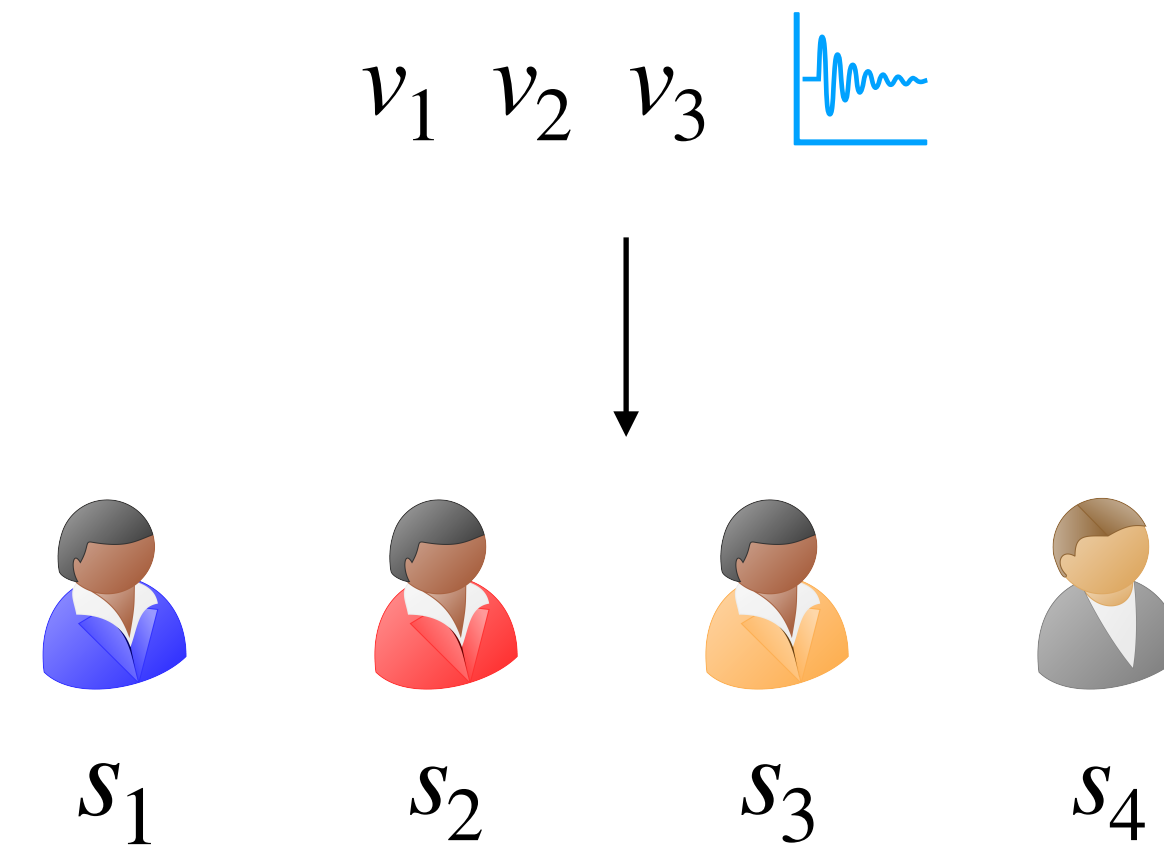
Regular Sharing



$O(n | C |)$ communication MPC

$$t = \frac{n-1}{2}$$

Packed Sharing

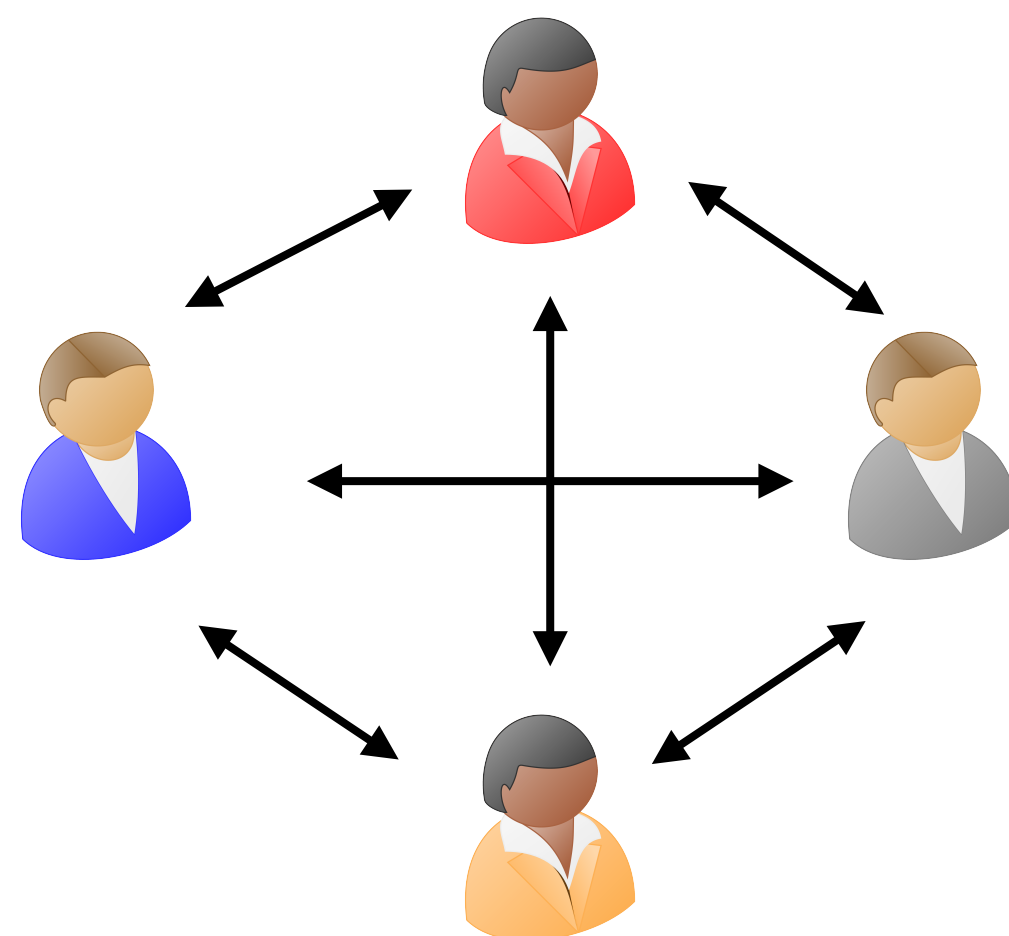


$\ell = O(n) \implies O(| C |)$ communication MPC

$$t = \left(\frac{1}{2} - \epsilon \right) \cdot n$$

Computing The Garbled Circuit

Pre-processing Phase

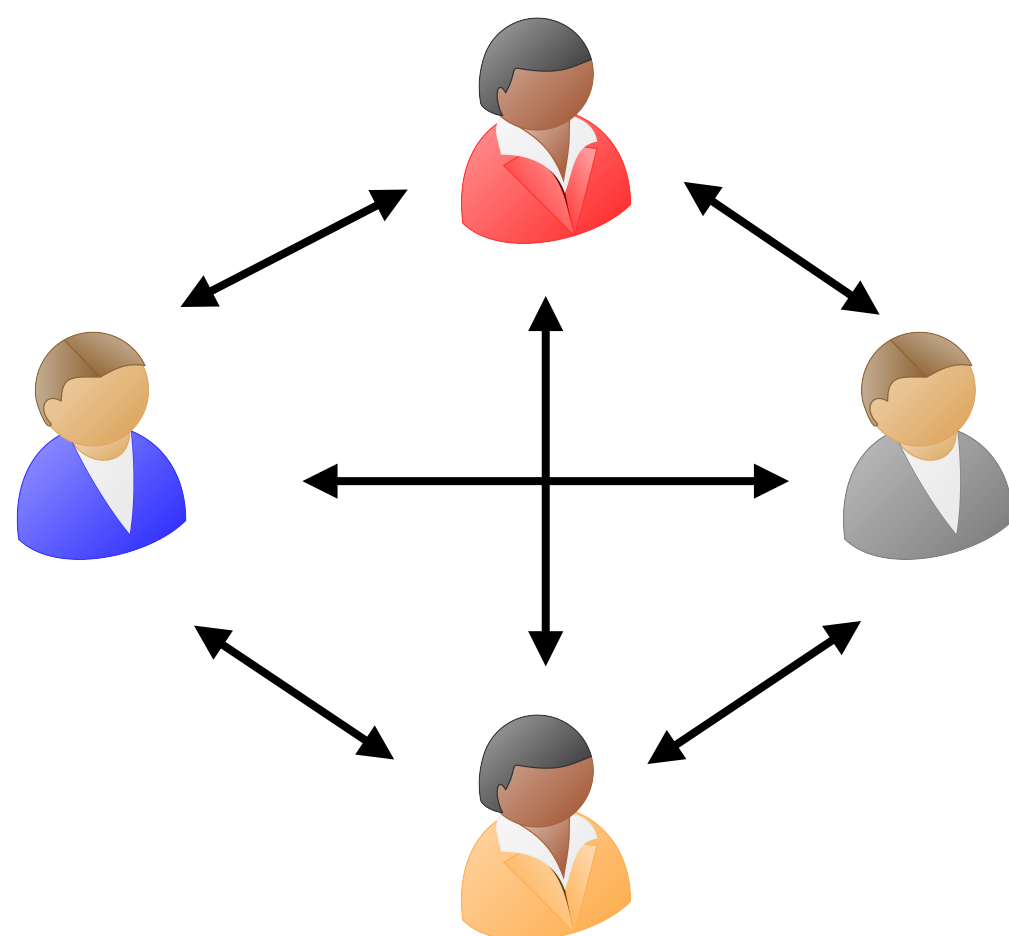


$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

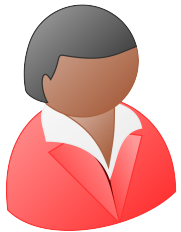
Garbling Phase



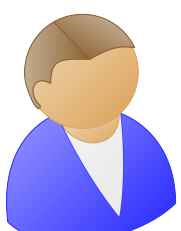
$\mathcal{G}$ - Garbled circuit

$\{X_{\text{inp}}\}$ - Wire labels for inputs

Evaluation Phase



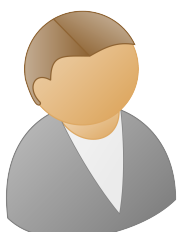
$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$



$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$



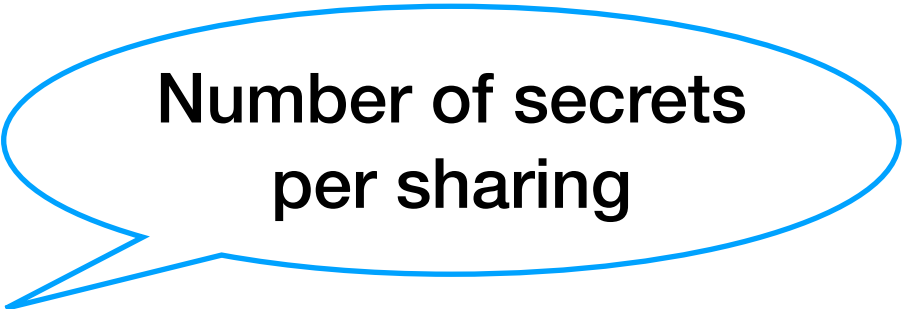
$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$



$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.



Number of secrets
per sharing

Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

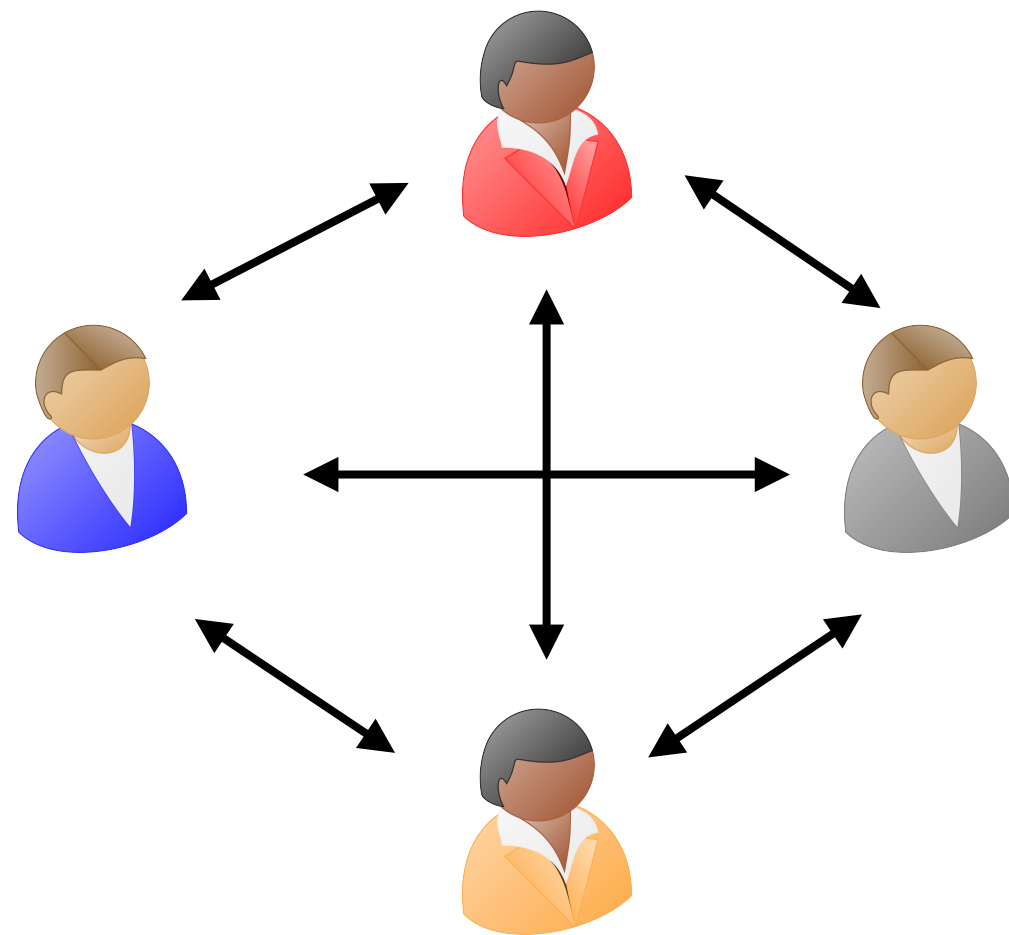
Generating shares of random elements in $\mathbb{F}^\ell$ [HN06].

Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random elements in $\mathbb{F}^\ell$ [HN06].

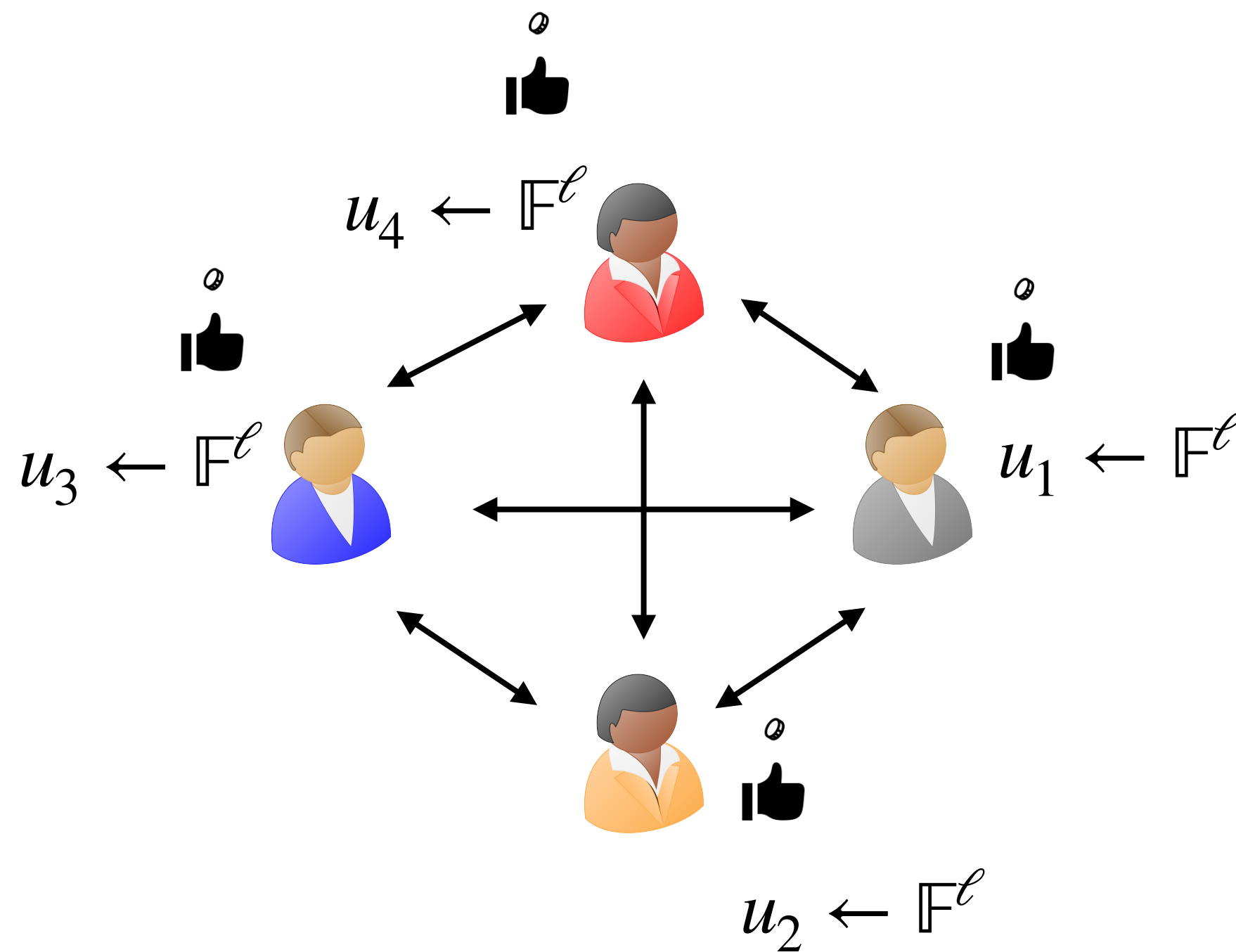


Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random elements in $\mathbb{F}^\ell$ [HN06].

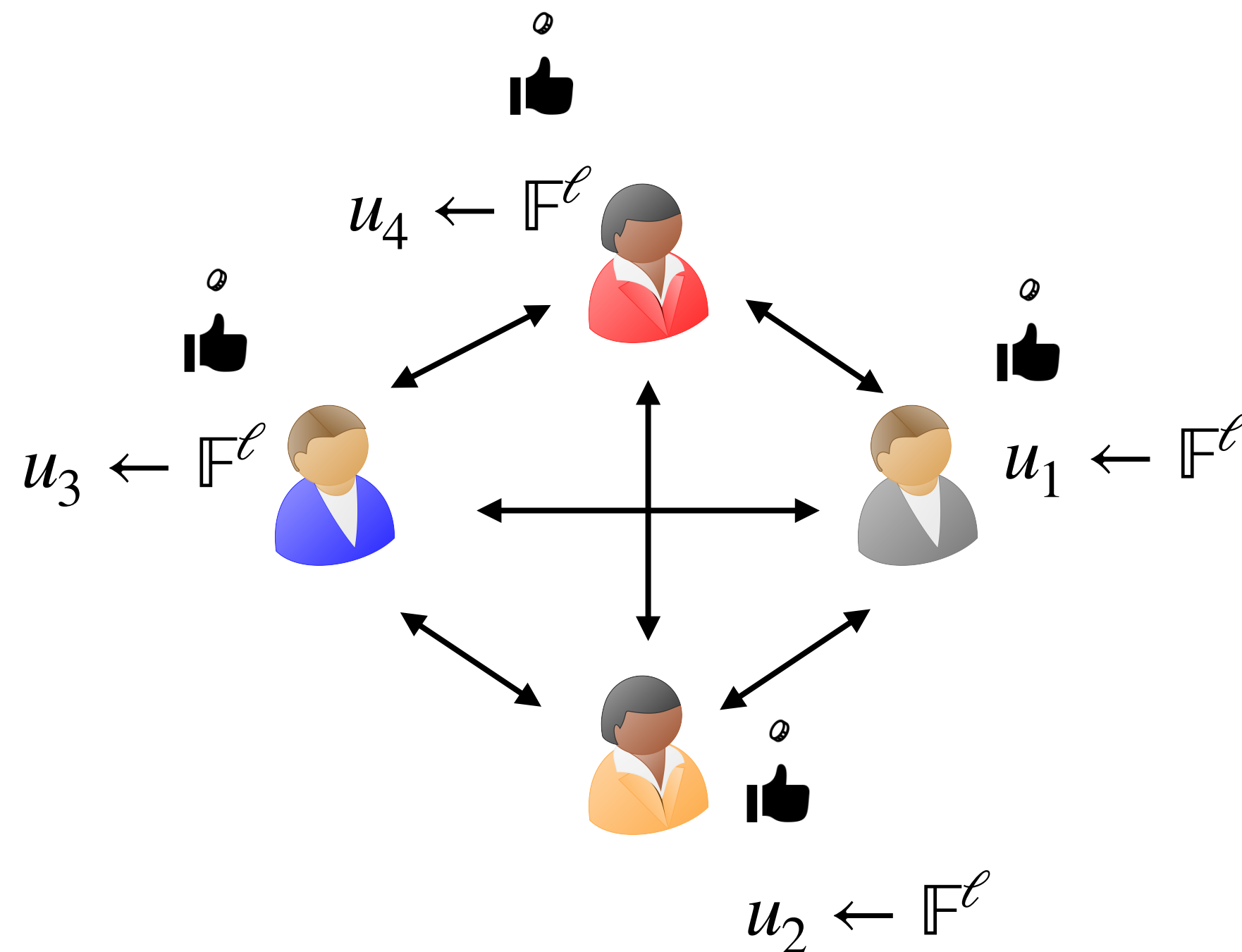


Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random elements in $\mathbb{F}^\ell$ [HN06].



$[u_1]$

$[u_2]$

$[u_3]$

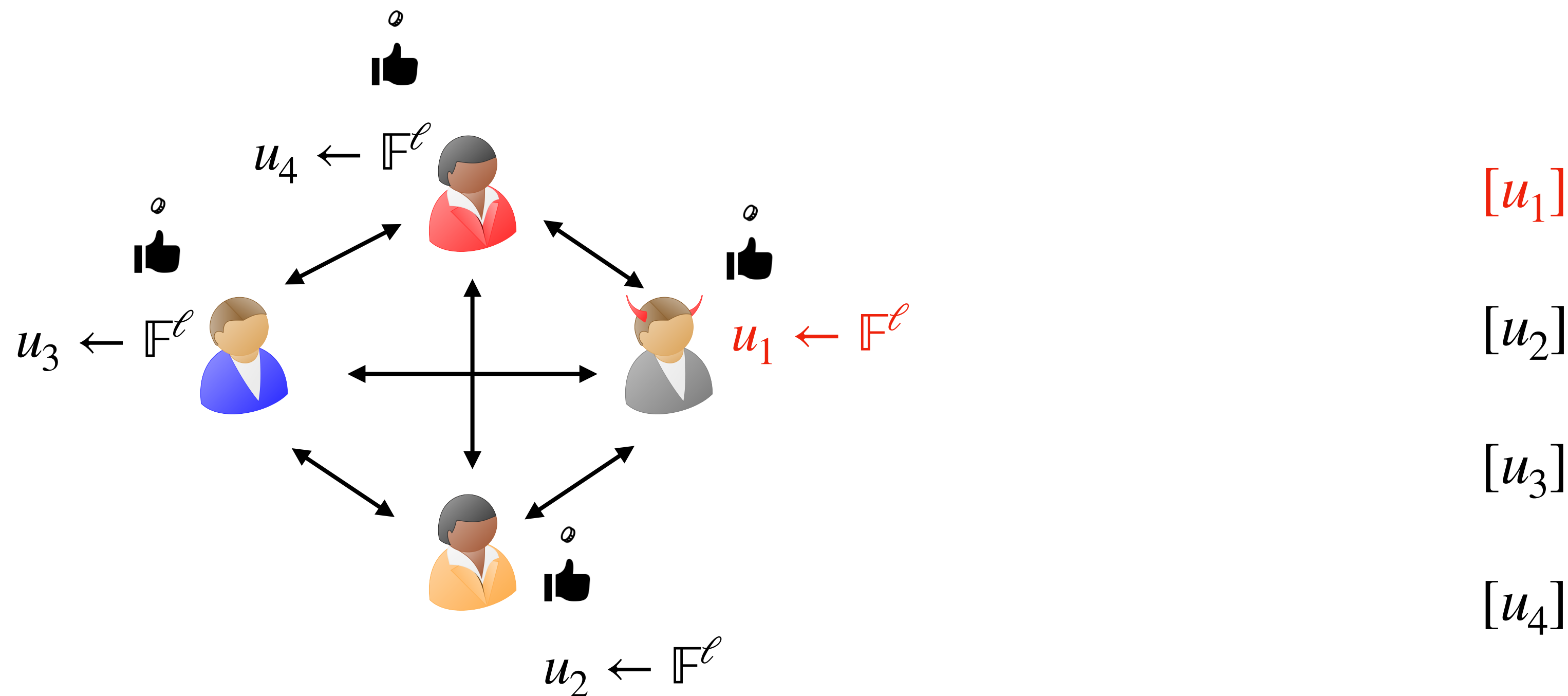
$[u_4]$

Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random vectors in $\mathbb{F}^\ell$ [HN06].

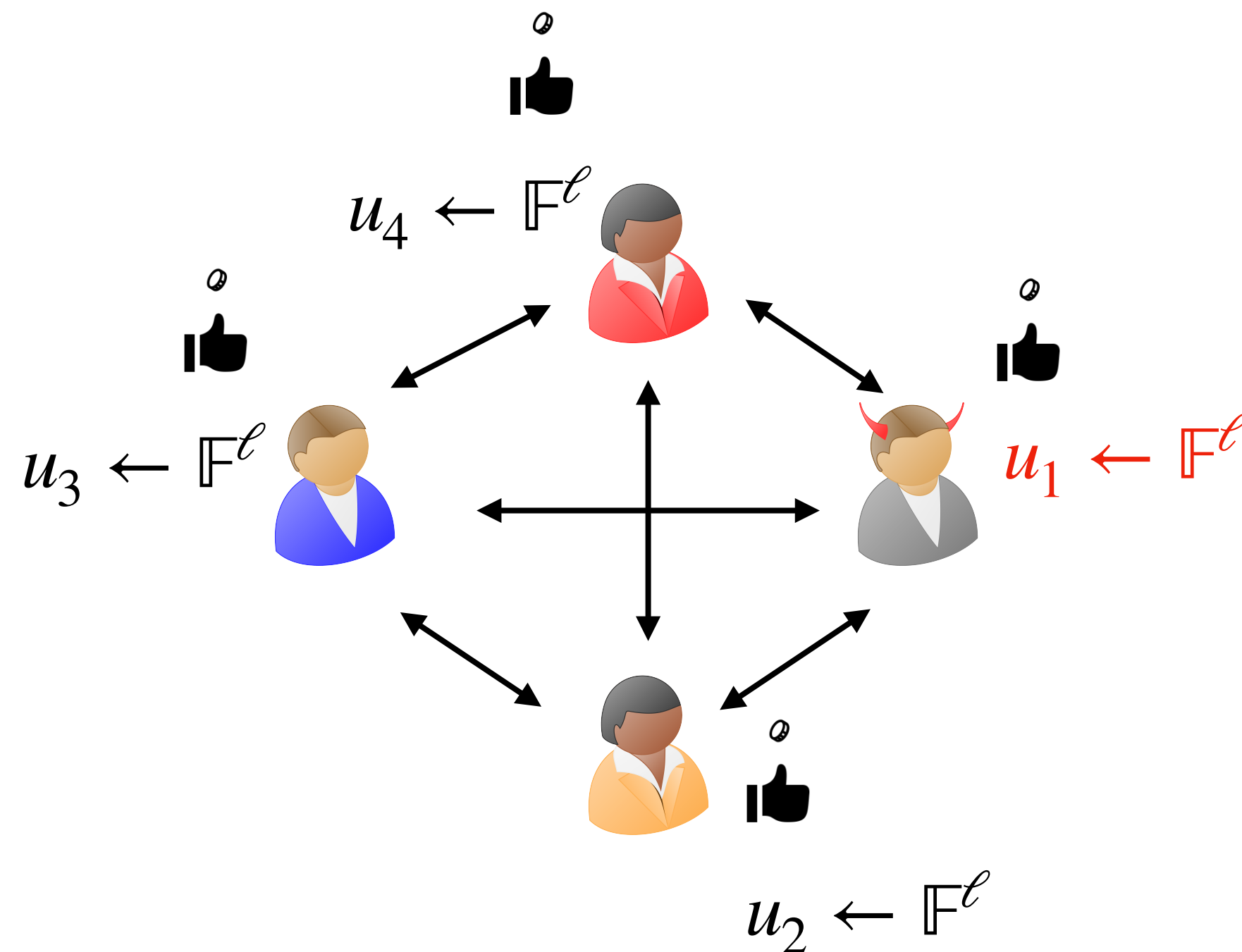


Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random vectors in $\mathbb{F}^\ell$ [HN06].



Super-invertible
matrix over $\mathbb{F}$

$[u_1]$

$[u_2]$

$[u_3]$

$[u_4]$

$n - t$ outputs

$[r_1]$

$= [r_2]$

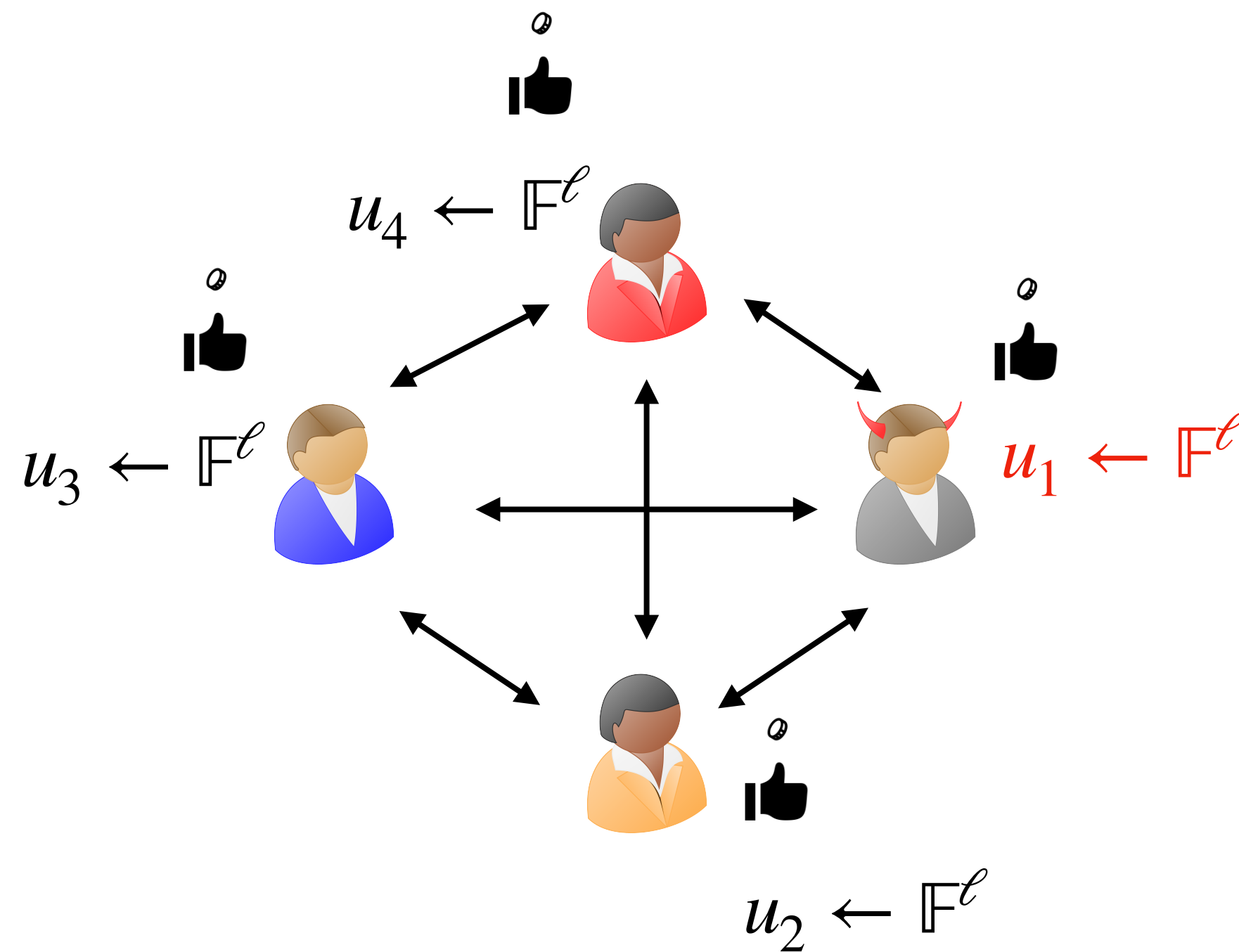
$[r_3]$

Computing Shares of Wire Labels

- Wire labels are LPN keys.
- LPN over larger fields since $|\mathbb{F}| > n + \ell$ due to packed secret sharing.

Number of secrets
per sharing

Generating shares of random vectors in $\mathbb{F}^\ell$ [HN06].



Super-invertible
matrix over $\mathbb{F}$

$[u_1]$

$[u_2]$

$[u_3]$

$[u_4]$

$n - t$ outputs

$[r_1]$

$= [r_2]$

$[r_3]$

Total communication is $O(n^2)$.

- $\ell = O(n)$
- $O(n)$ outputs

Amortized communication is $O(1)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - **Colors** for wire labels.
 - Compute shares of **LPN errors**.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

Super-invertible
matrix over $\mathbb{F}$

[0]

[1]

[0]

[0]

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

Super-invertible matrix over $\mathbb{F}$ m_1 m_2 m_3 m_4 $m_i \in \mathbb{F}$	[0]	=	$[m_2]$
	[1]		
	[0]		
	[0]		

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

$$\begin{array}{c}
 \boxed{\begin{array}{c} \text{Super-invertible} \\ \text{matrix over } \mathbb{F} \\ \hline m_1 \quad m_2 \quad m_3 \quad m_4 \\ \hline \end{array}} \begin{array}{l} [0] \\ [1] \\ [0] \\ [0] \end{array} = [m_2]
 \end{array}$$

$m_i \in \mathbb{F}$

Arbitrary super-invertible matrices over $\mathbb{F}$ will not work.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

$$\begin{array}{c}
 \boxed{\begin{array}{c} \text{Super-invertible} \\ \text{matrix over } \mathbb{F} \\ \hline m_1 \quad m_2 \quad m_3 \quad m_4 \\ \hline \end{array}} \begin{array}{l} [0] \\ [1] \\ [0] \\ [0] \end{array} = [m_2]
 \end{array}$$

$m_i \in \mathbb{F}$

Arbitrary super-invertible matrices over $\mathbb{F}$ will not work.

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

$$\begin{array}{c}
 \boxed{\begin{array}{c} \text{Super-invertible} \\ \text{matrix over } \mathbb{F} \\ \hline m_1 \quad m_2 \quad m_3 \quad m_4 \\ \hline \end{array}} \begin{array}{l} [0] \\ [1] \\ [0] \\ [0] \end{array} = [m_2] \\
 m_i \in \mathbb{F}
 \end{array}$$

Arbitrary super-invertible matrices over $\mathbb{F}$ will not work.

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

MPC is done over $\mathbb{F} = \text{GF}(2^k)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

$[u_1]$

$[u_2]$

$[u_3]$

$[u_4]$

$$u_i \in \{0,1\}^\ell$$

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

Binary
super-invertible
matrix over $\mathbb{F}$

$[u_1]$ $[b_1]$

$[u_2]$ $[b_2]$

$[u_3]$ $O(n)$ outputs

$[u_4]$

$u_i \in \{0,1\}^\ell$

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

Computing Shares Of Random Bits

- Secret sharings of random bits
 - Colors for wire labels.
 - Compute shares of LPN errors.

Problem: Secret sharing of a uniformly random bit over a larger field $\mathbb{F}$.

Binary
super-invertible
matrix over $\mathbb{F}$

$[u_1]$ $[b_1]$

$[u_2]$ $[b_2]$

=

$[u_3]$ $O(n)$ outputs

$[u_4]$

$u_i \in \{0,1\}^\ell$

The generator matrix of any binary linear error correcting code is super-invertible over a field of characteristic 2.

Construct binary super-invertible matrix with order $n \times O(n)$.

Total communication is $O(n^2)$.

- $\ell = O(n)$
- $O(n)$ outputs

Amortized communication is $O(1)$.

Computing Shares Of LPN Errors

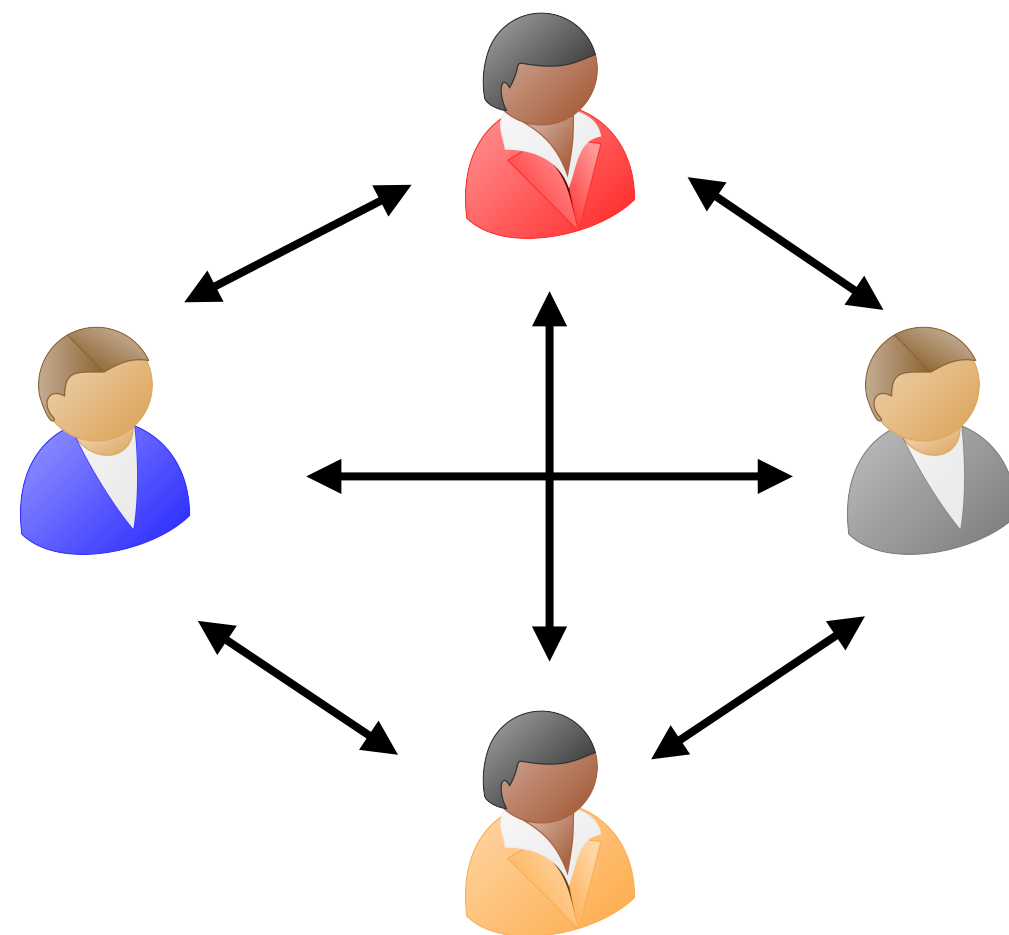
- Computing shares of secrets sampled from a **Bernoulli distribution** over $\mathbb{F}$.

$$\begin{aligned} P[\epsilon \leftarrow \mathbb{F}^\ell] &= 2^{-p} \\ P[\epsilon = 0^\ell] &= 1 - 2^{-p} \end{aligned} \quad p \text{ is a constant}$$

Computing Shares Of LPN Errors

- Computing shares of secrets sampled from a **Bernoulli distribution** over $\mathbb{F}$.

$$P[\epsilon \leftarrow \mathbb{F}^\ell] = 2^{-p}$$
$$P[\epsilon = 0^\ell] = 1 - 2^{-p} \quad p \text{ is a constant}$$



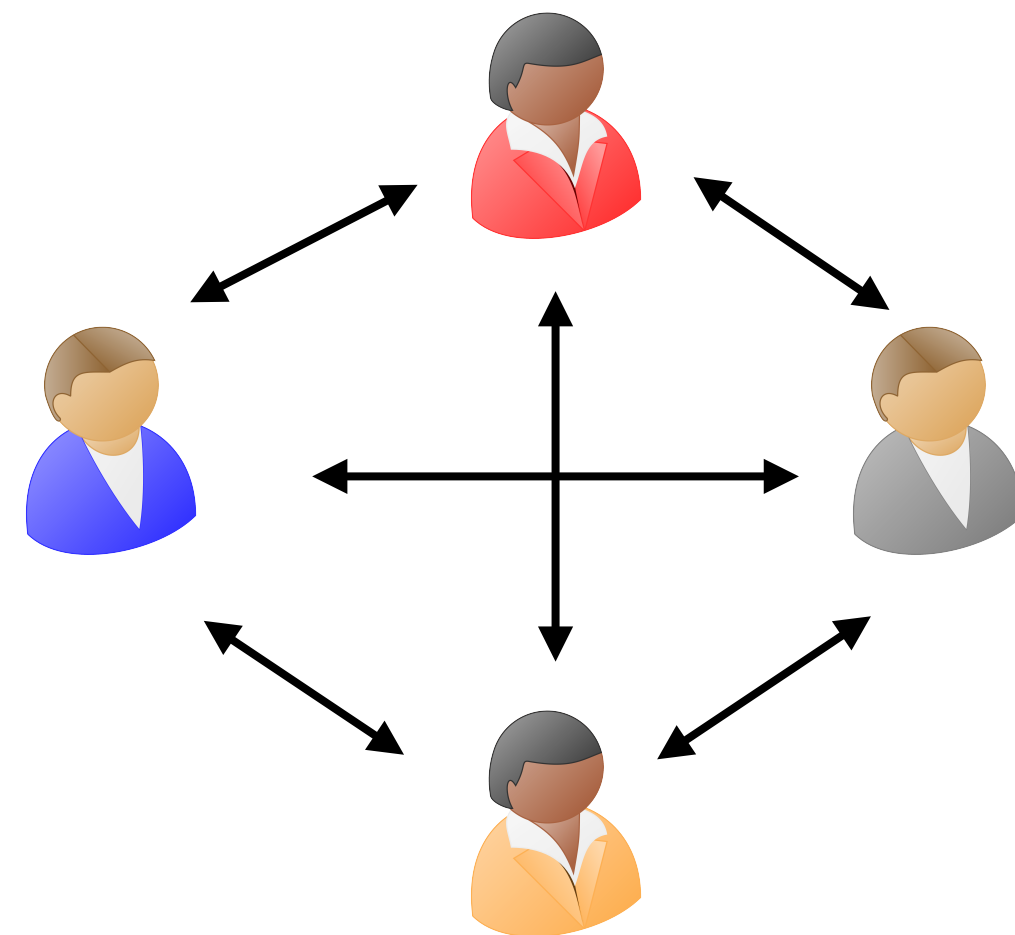
$[b_1]$ $[b_2]$ $\dots$ $[b_p]$

Packed sharing of random bits

Computing Shares Of LPN Errors

- Computing shares of secrets sampled from a **Bernoulli distribution** over $\mathbb{F}$.

$$\begin{aligned} P[\epsilon \leftarrow \mathbb{F}^\ell] &= 2^{-p} \\ P[\epsilon = 0^\ell] &= 1 - 2^{-p} \end{aligned} \quad p \text{ is a constant}$$



$$[b_1] \times [b_2] \times \dots \times [b_p]$$

$$[\beta]$$

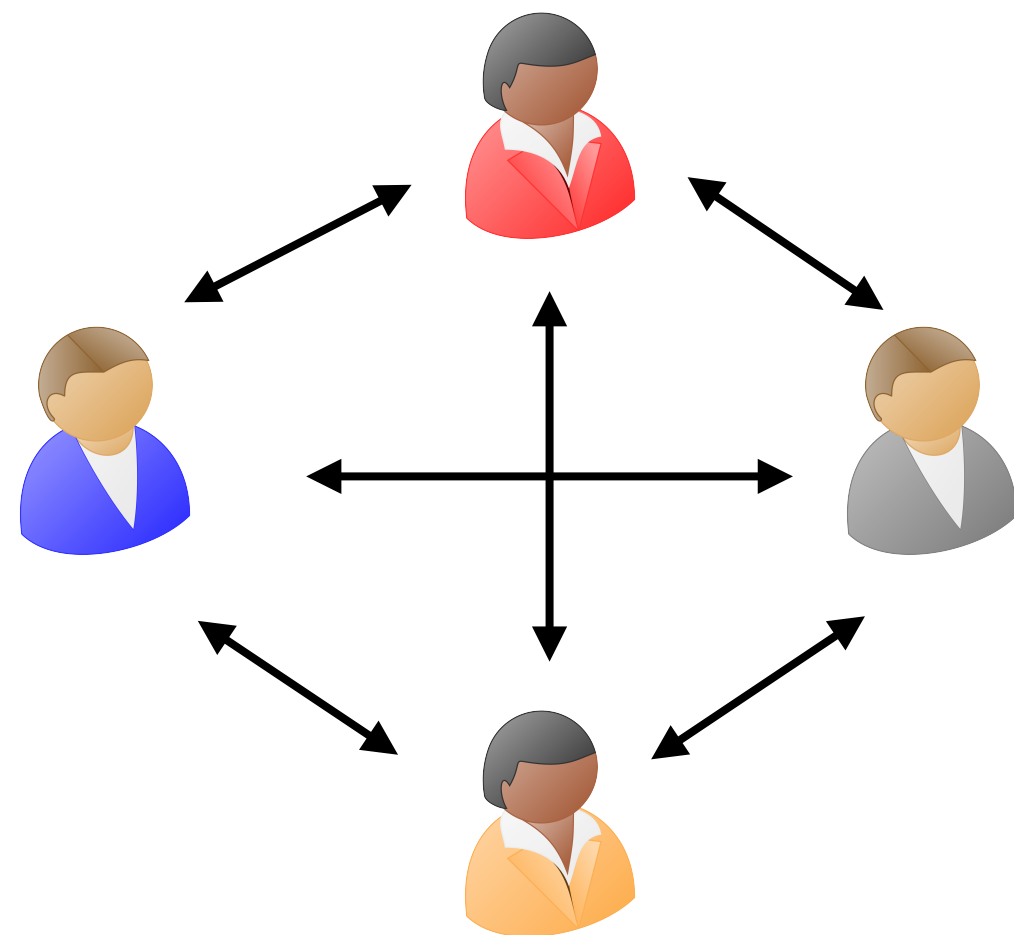
Packed sharing of random bits

$$P[\beta = 1] = 2^{-p}$$

Computing Shares Of LPN Errors

- Computing shares of secrets sampled from a **Bernoulli distribution** over $\mathbb{F}$.

$$\begin{aligned} P[\epsilon \leftarrow \mathbb{F}^\ell] &= 2^{-p} \\ P[\epsilon = 0^\ell] &= 1 - 2^{-p} \end{aligned} \quad p \text{ is a constant}$$



$$[b_1] \times [b_2] \times \dots \times [b_p]$$

Packed sharing of random bits

$$[\beta] \times [r]$$

$$P[\beta = 1] = 2^{-p}$$

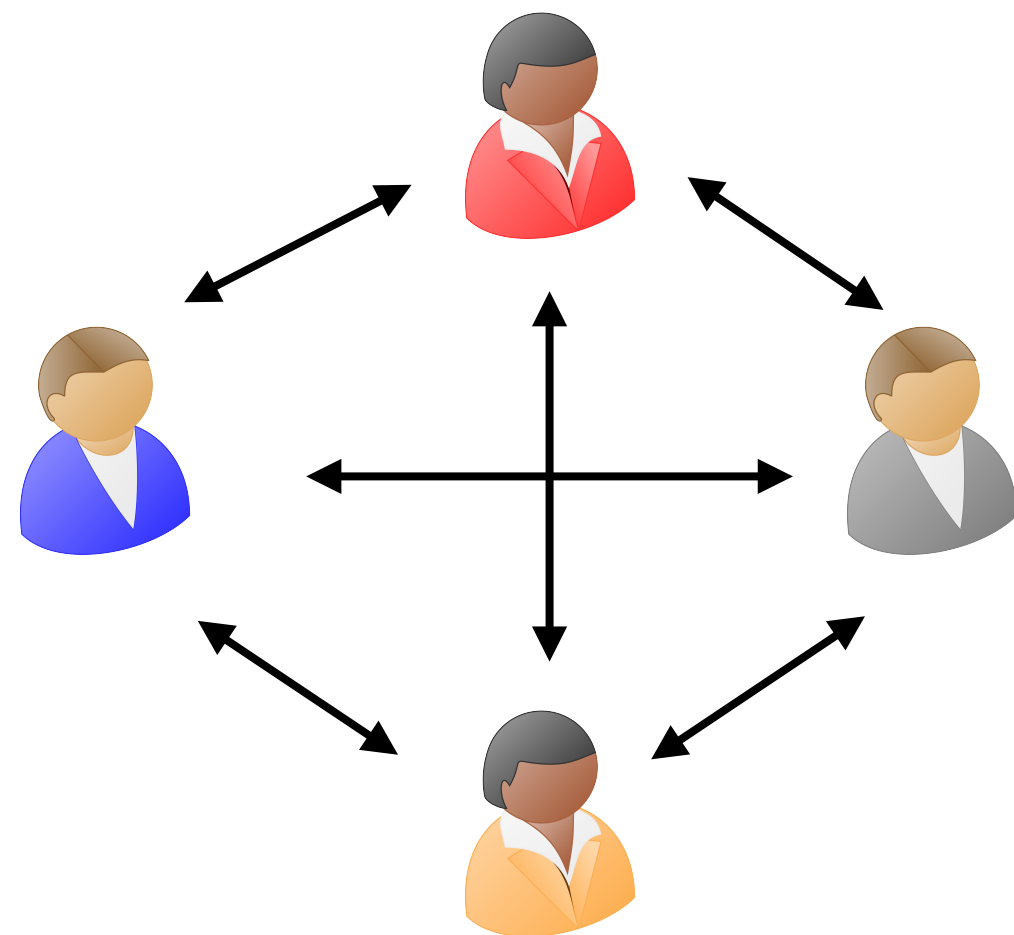
$$[\epsilon]$$

Packed sharing of random elements

Computing Shares Of LPN Errors

- Computing shares of secrets sampled from a **Bernoulli distribution** over $\mathbb{F}$.

$$\begin{aligned} P[\epsilon \leftarrow \mathbb{F}^\ell] &= 2^{-p} \\ P[\epsilon = 0^\ell] &= 1 - 2^{-p} \end{aligned} \quad p \text{ is a constant}$$



$$[b_1] \times [b_2] \times \dots \times [b_p]$$

Packed sharing of random bits

$$[\beta] \times [r]$$

$$P[\beta = 1] = 2^{-p}$$

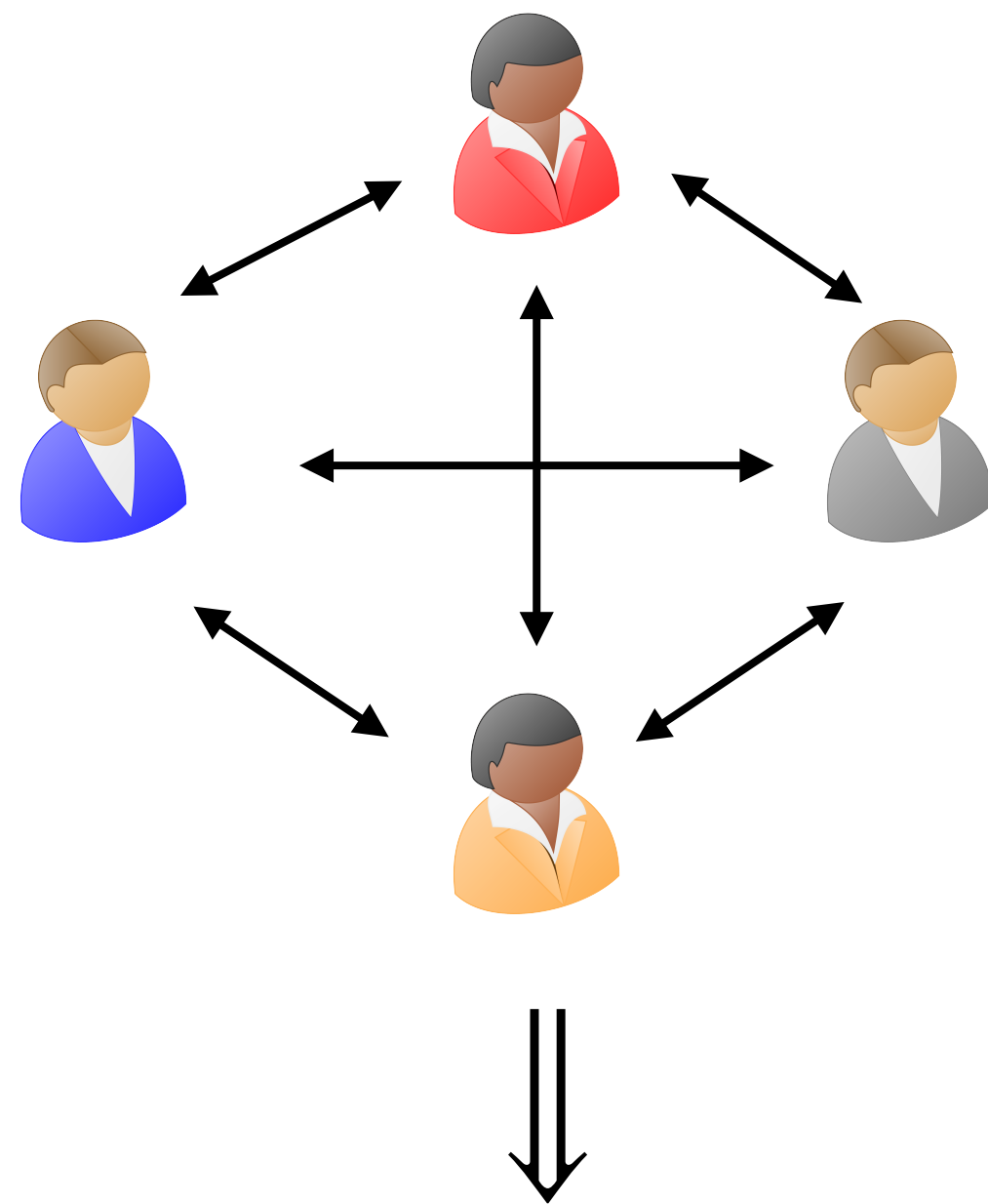
$$[\epsilon]$$

Packed sharing of random elements

$O(1)$ communication

Computing The Garbled Circuit

Pre-processing Phase

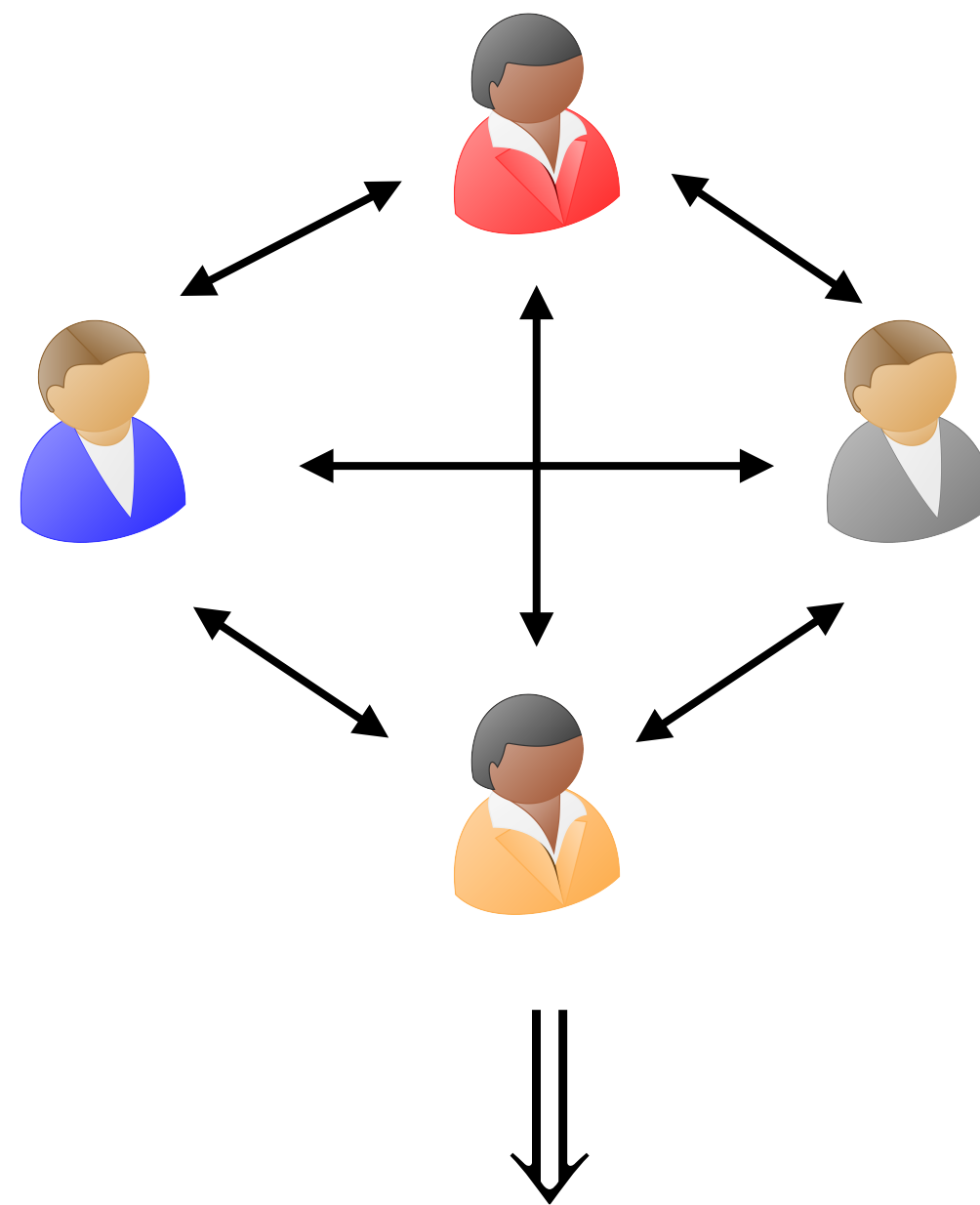


$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

Garbling Phase

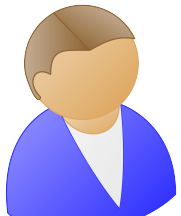


$\mathcal{G}$ - Garbled circuit

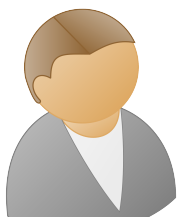
$\{X_{\text{inp}}\}$ - Wire labels for inputs

Evaluation Phase

 $\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

 $\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

 $\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

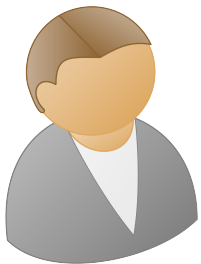
 $\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

Garbling Phase

Pre-processing phase



$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$

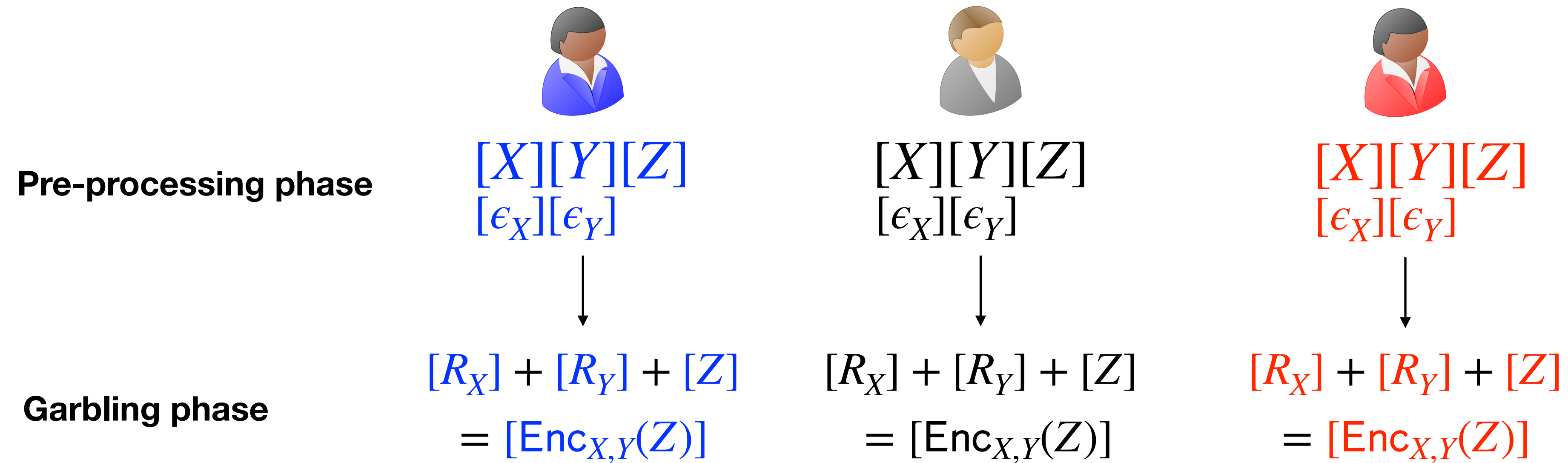


$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$

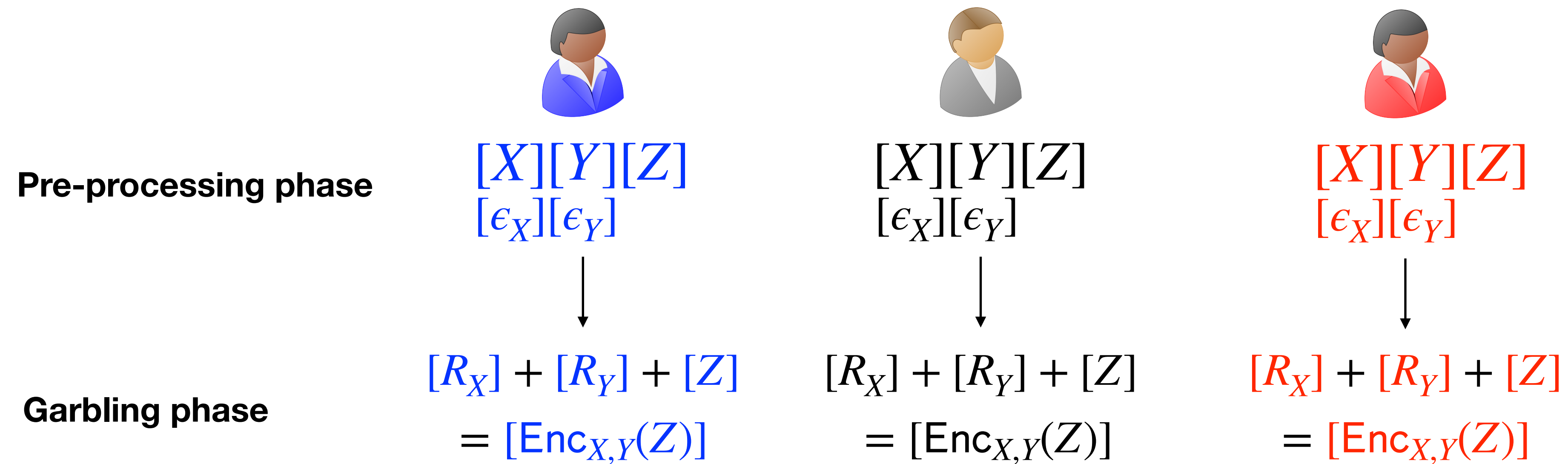


$[X][Y][Z]$
 $[\epsilon_X][\epsilon_Y]$

Garbling Phase

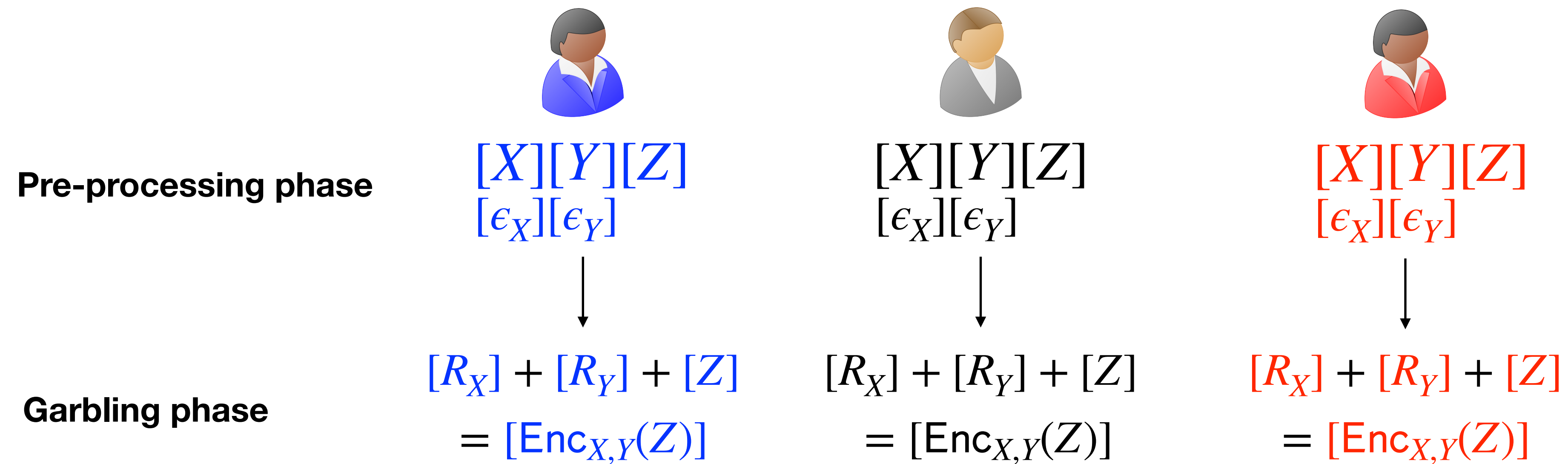


Garbling Phase



Shuffle ciphertexts to compute $\mathcal{G}$

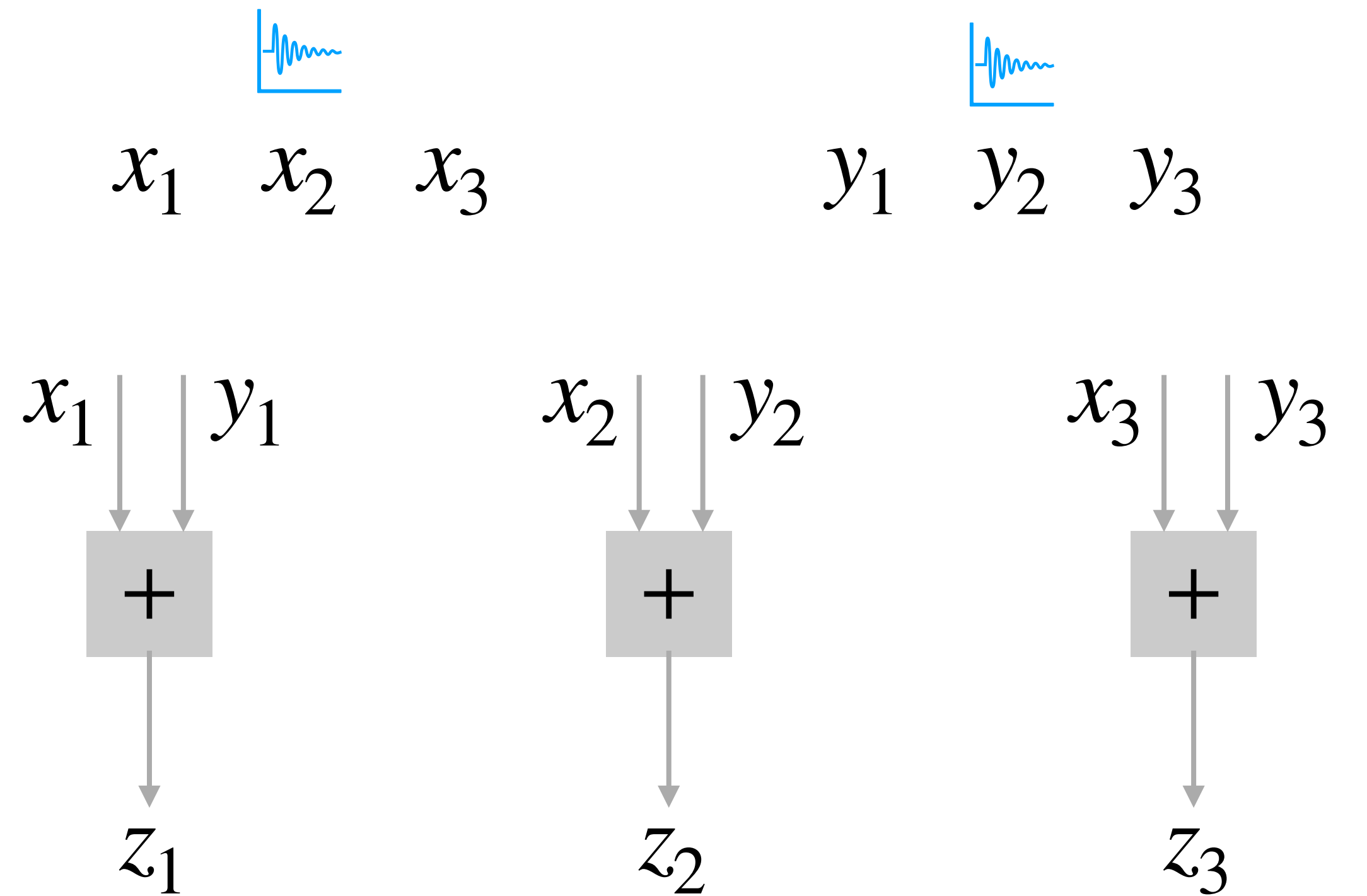
Garbling Phase



Shuffle ciphertexts to compute $\mathcal{G}$

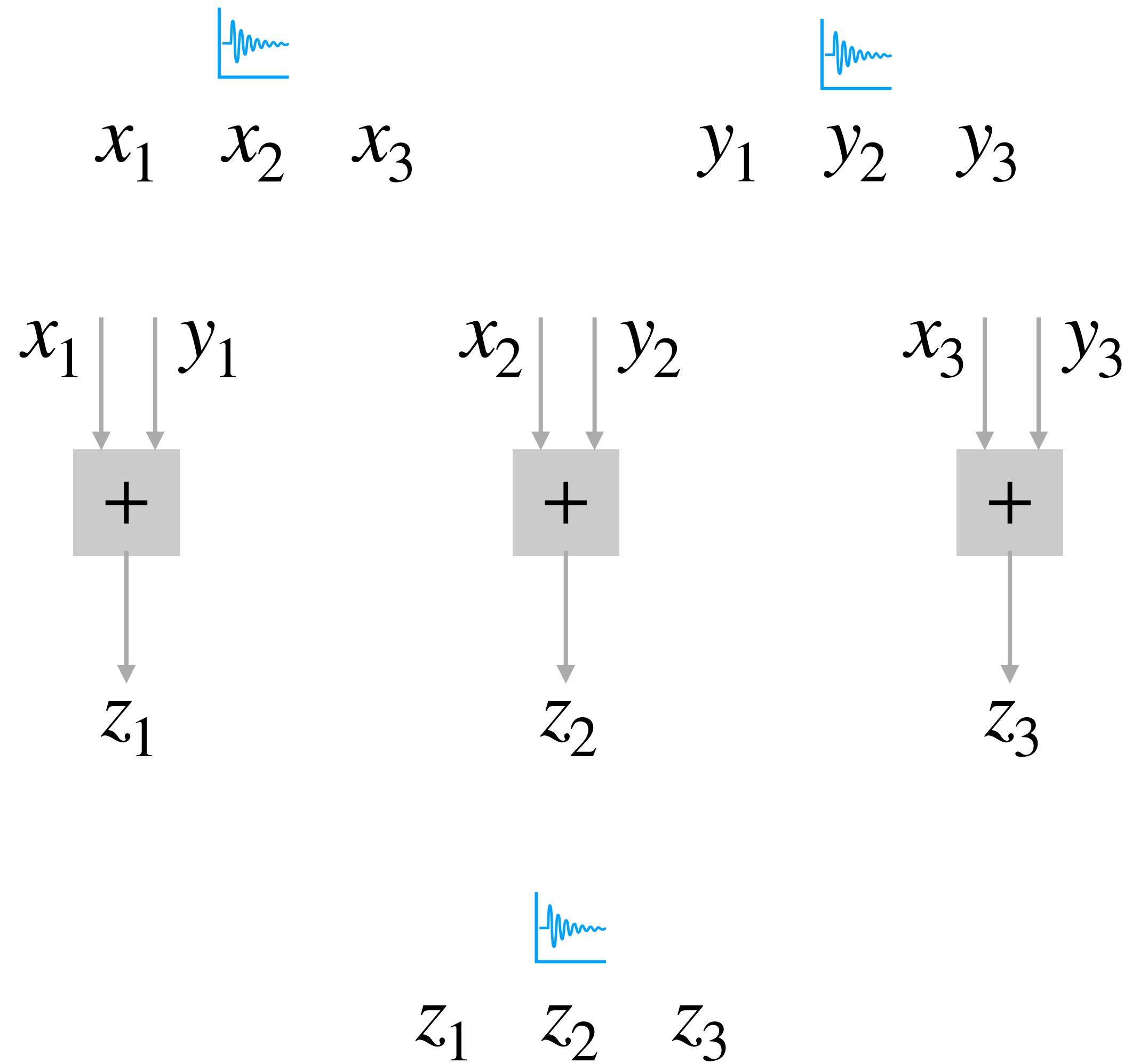
Alignment of secrets in packed sharing.

Alignment Of Secrets In Packed Sharing



Alignment Of Secrets In Packed Sharing

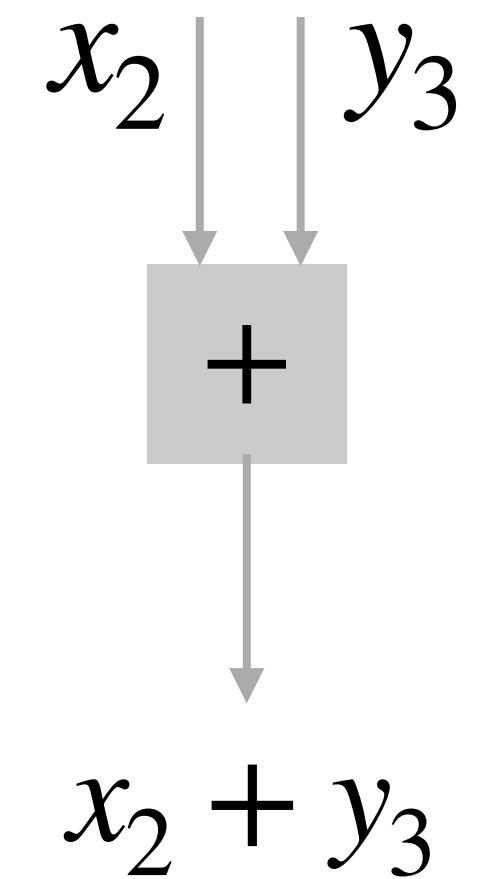
Computation is straightforward
when secrets are aligned.



Alignment Of Secrets In Packed Sharing

Computation is straightforward
when secrets are aligned.

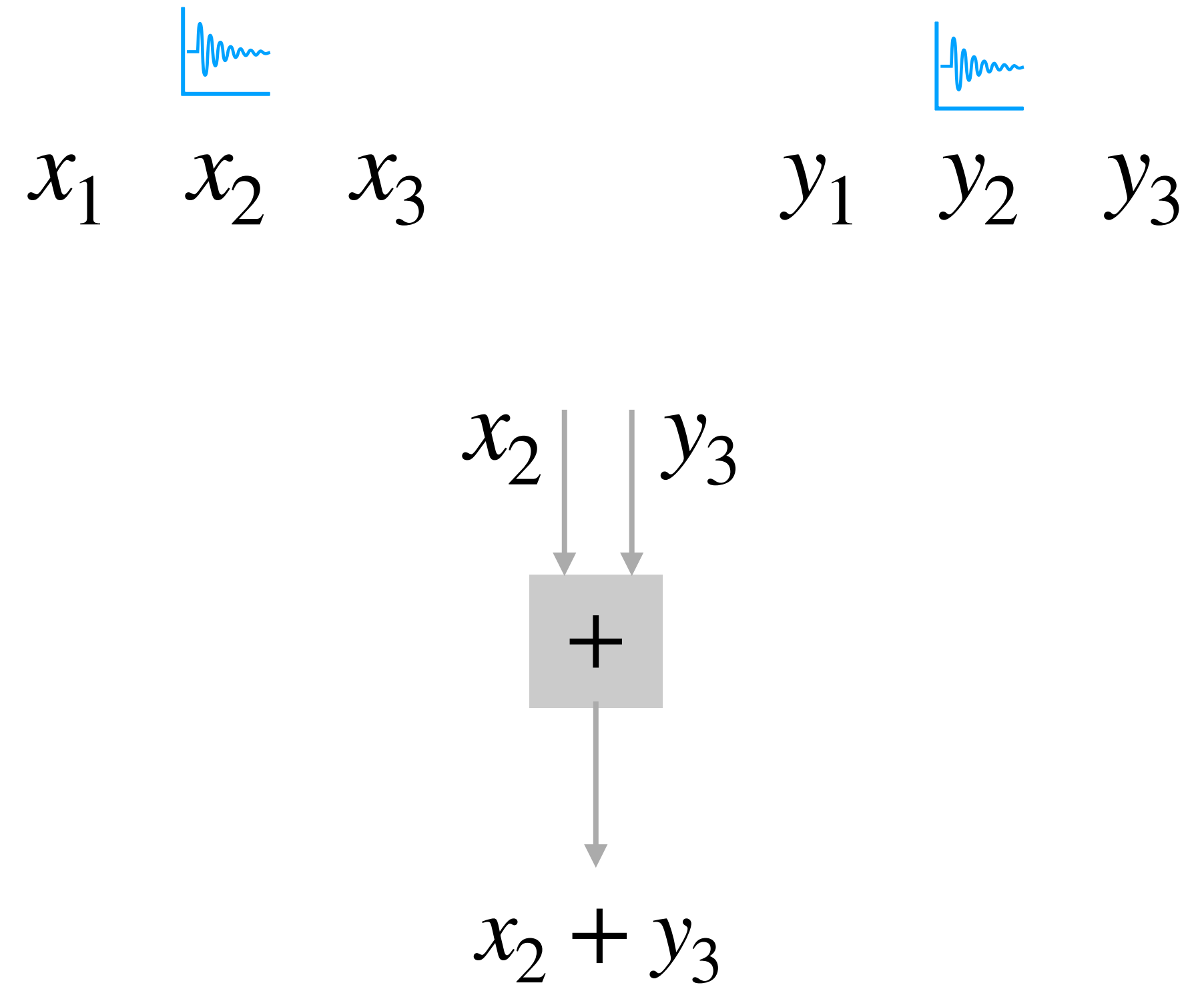
x_1 x_2 x_3 y_1 y_2 y_3



Alignment Of Secrets In Packed Sharing

Computation is straightforward when secrets are aligned.

Packed sharing requires secrets to be re-aligned.



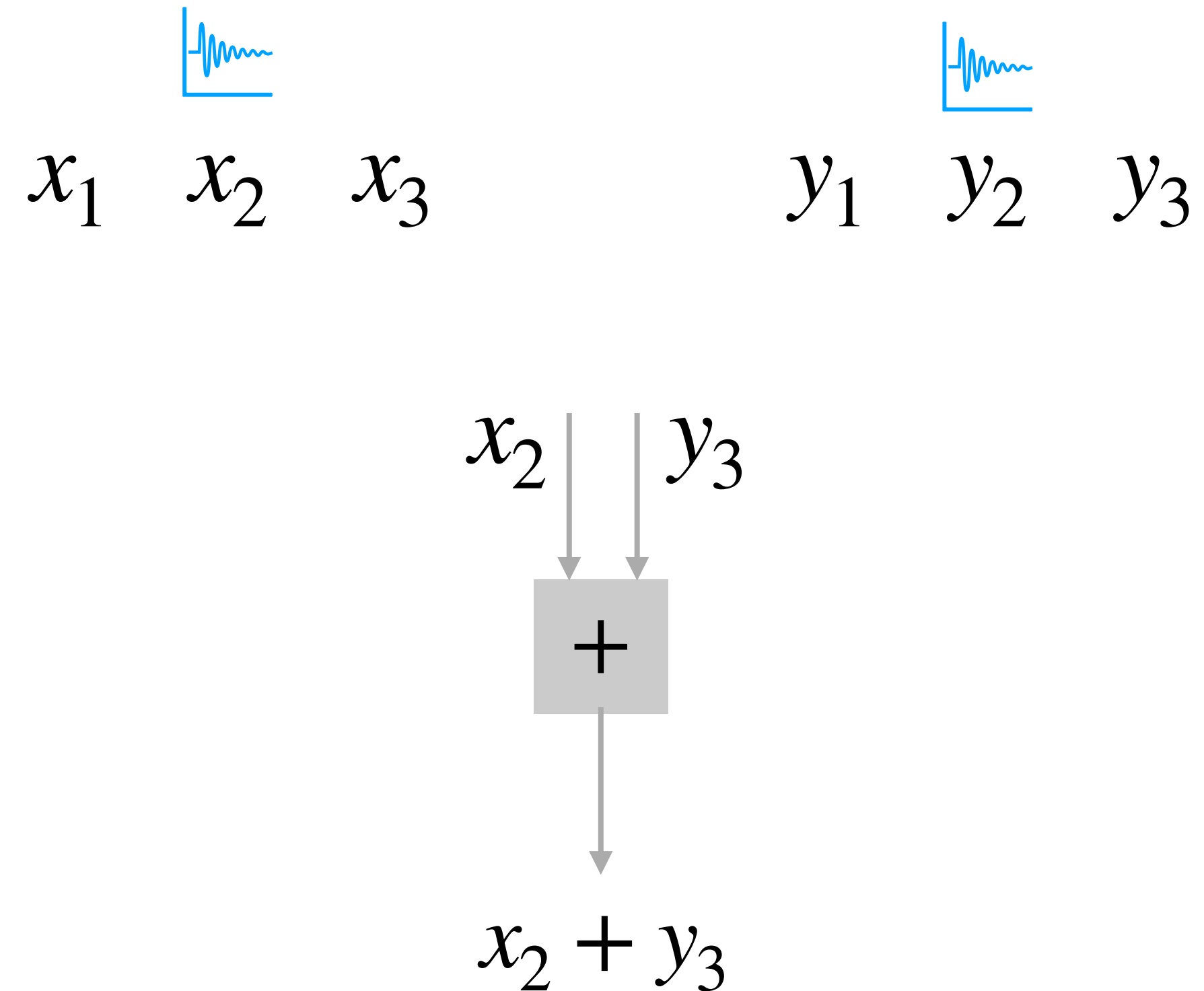
Alignment Of Secrets In Packed Sharing

Computation is straightforward when secrets are aligned.

Packed sharing requires secrets to be re-aligned.

Bottleneck is number of **distinct** re-alignments:

- Bound number of distinct re-alignments [DIK10, GIP15, GPS21].
- Restrict circuit type [BGJK21].
- Efficiently perform any re-alignment [GPS22].



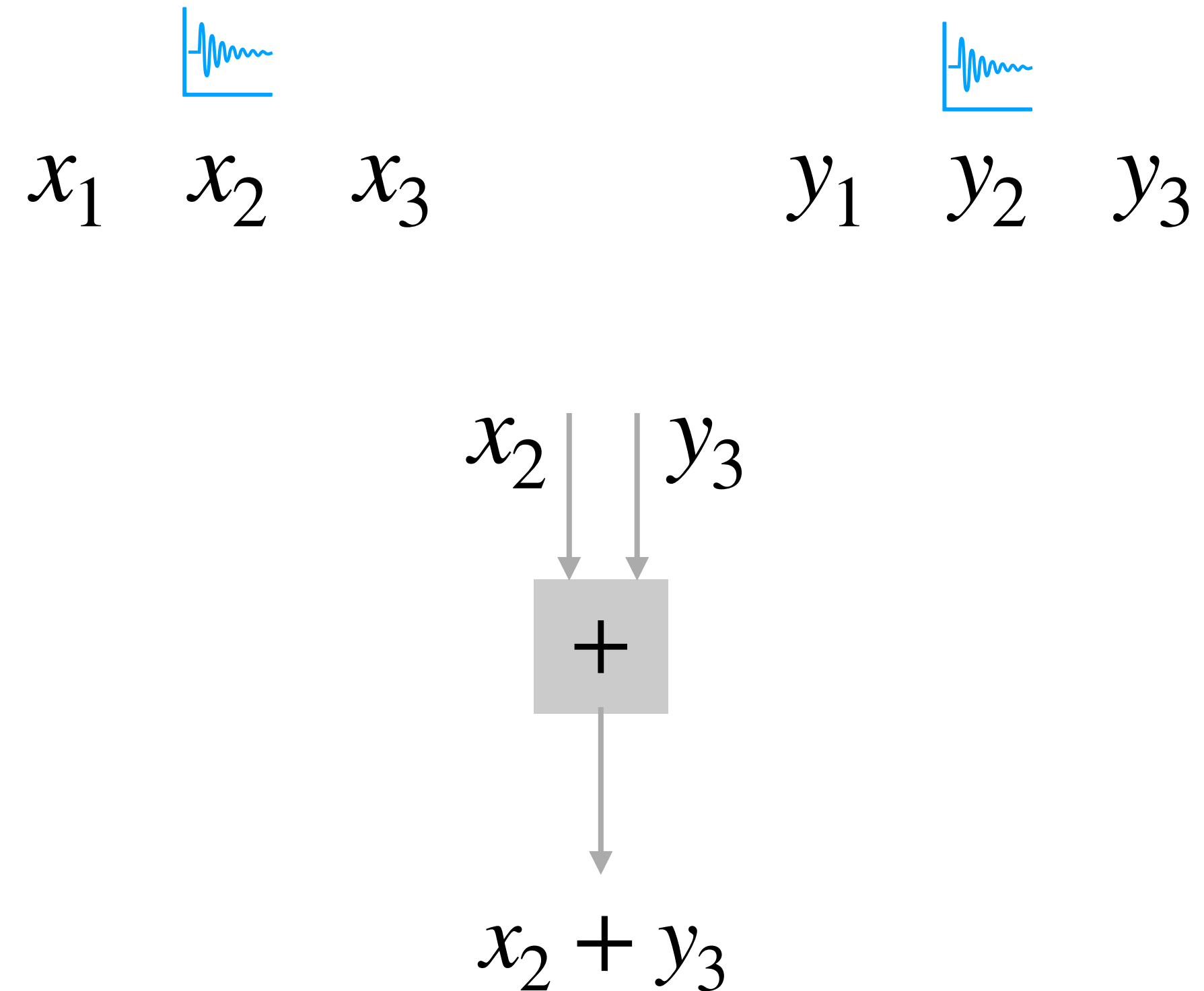
Alignment Of Secrets In Packed Sharing

Computation is straightforward when secrets are aligned.

Packed sharing requires secrets to be re-aligned.

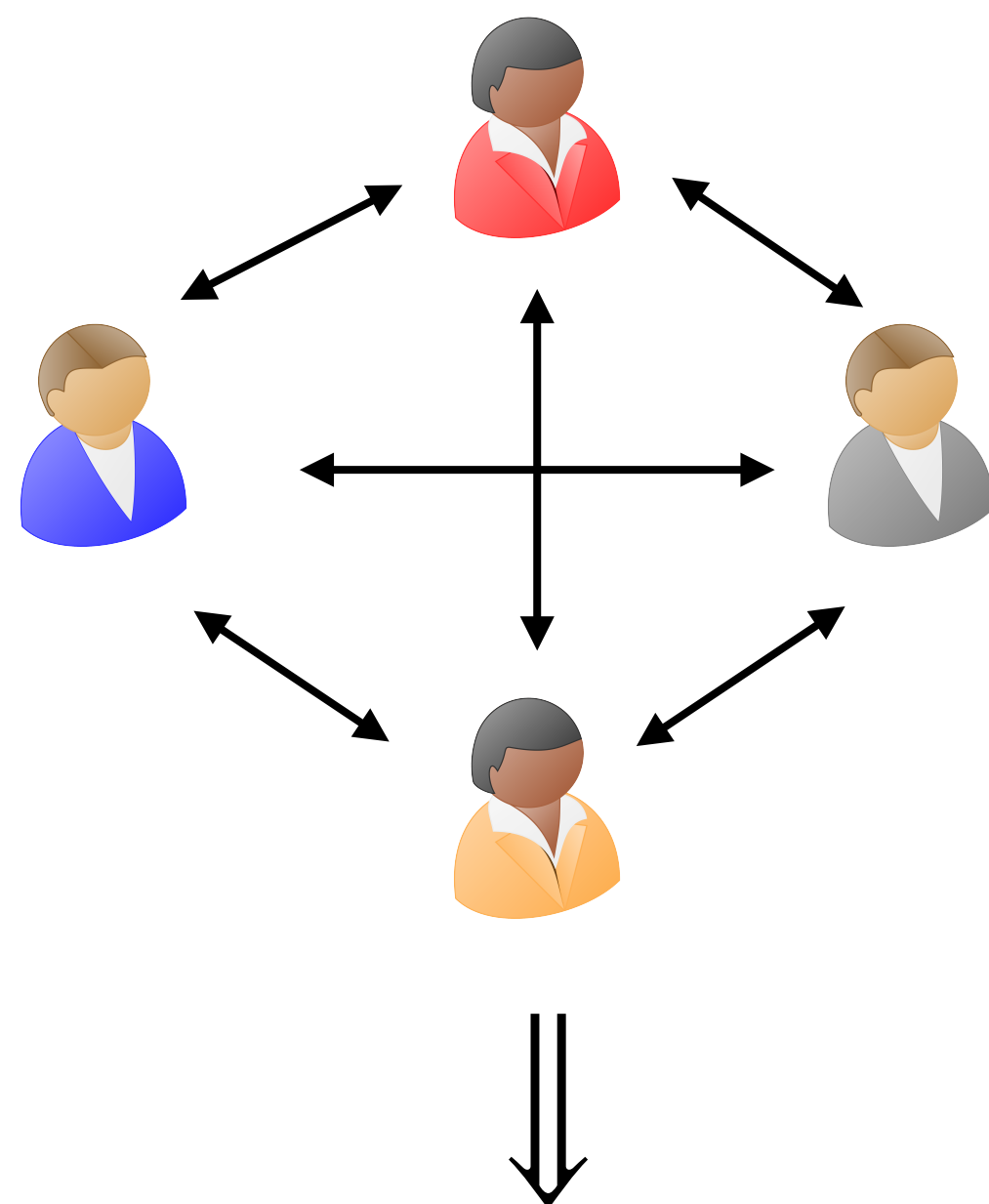
Bottleneck is number of **distinct** re-alignments:

- Bound number of distinct re-alignments [DIK10, GIP15, GPS21].
- Restrict circuit type [BGJK21].
- Efficiently perform any re-alignment [GPS22].



Computing The Garbled Circuit

Pre-processing Phase

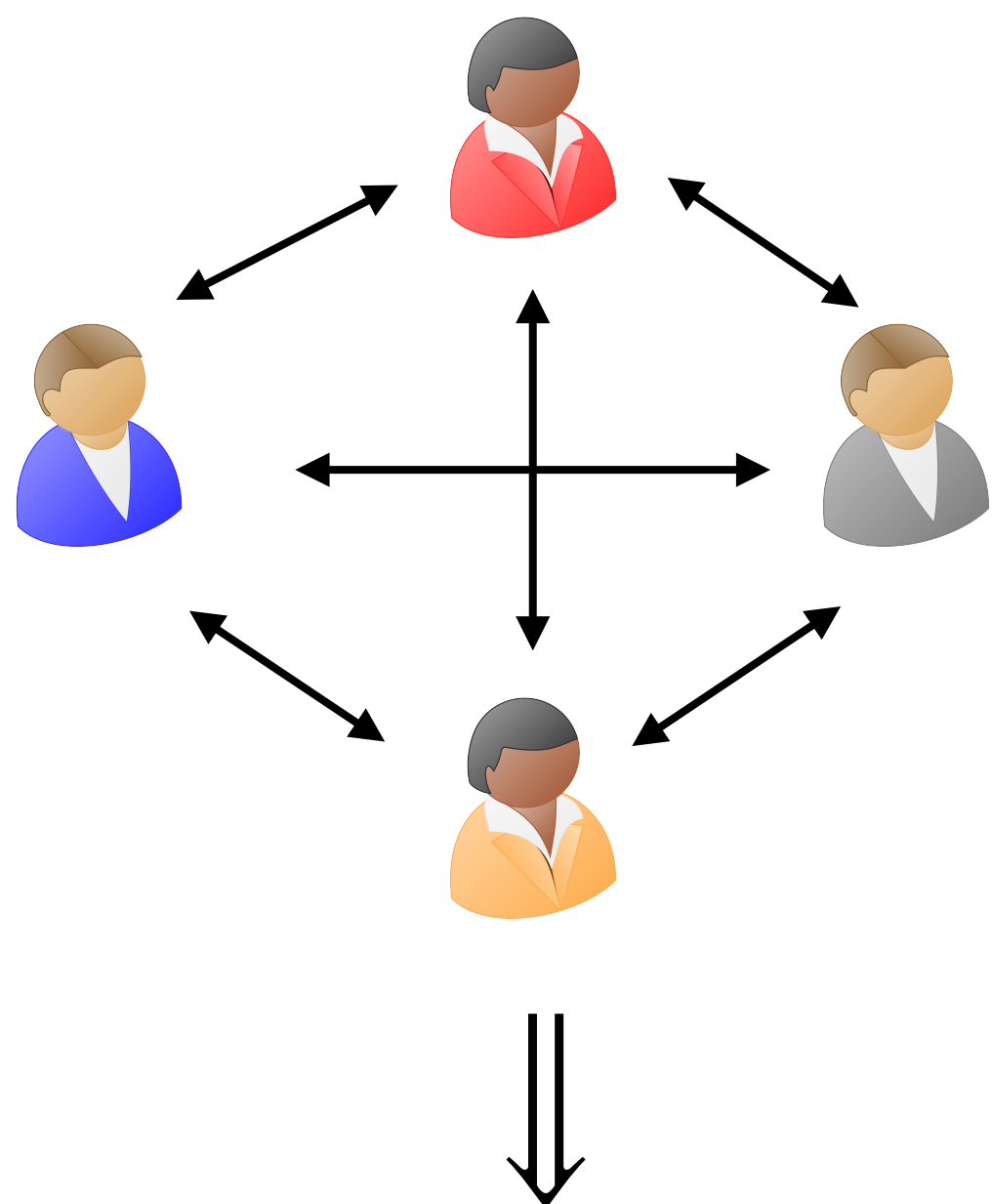


$[k]$ - Wire labels (LPN Keys)

$[\epsilon]$ - LPN Errors

$[b]$ - Wire label colors

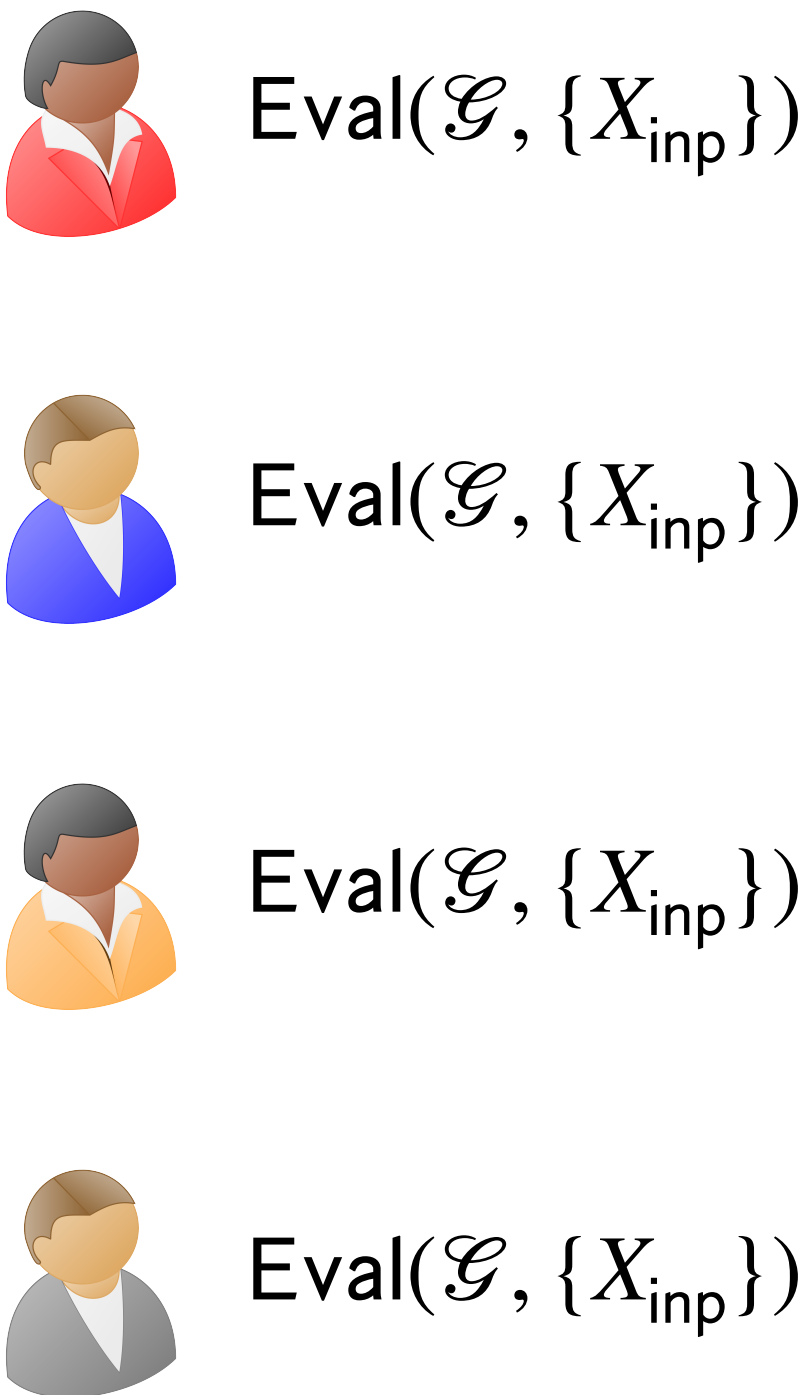
Garbling Phase



$\mathcal{G}$ - Garbled circuit

$\{X_{\text{inp}}\}$ - Wire labels for inputs

Evaluation Phase



$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

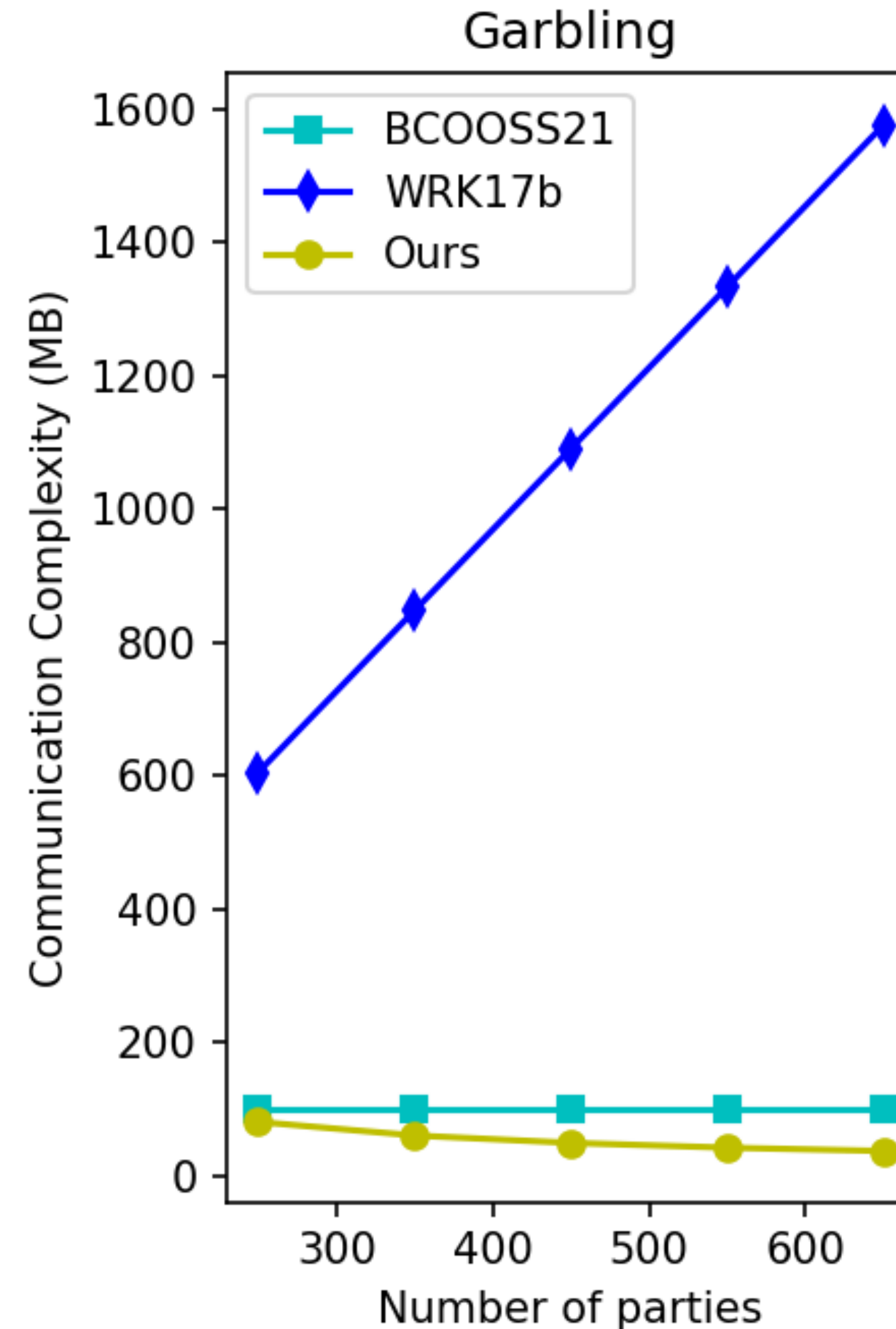
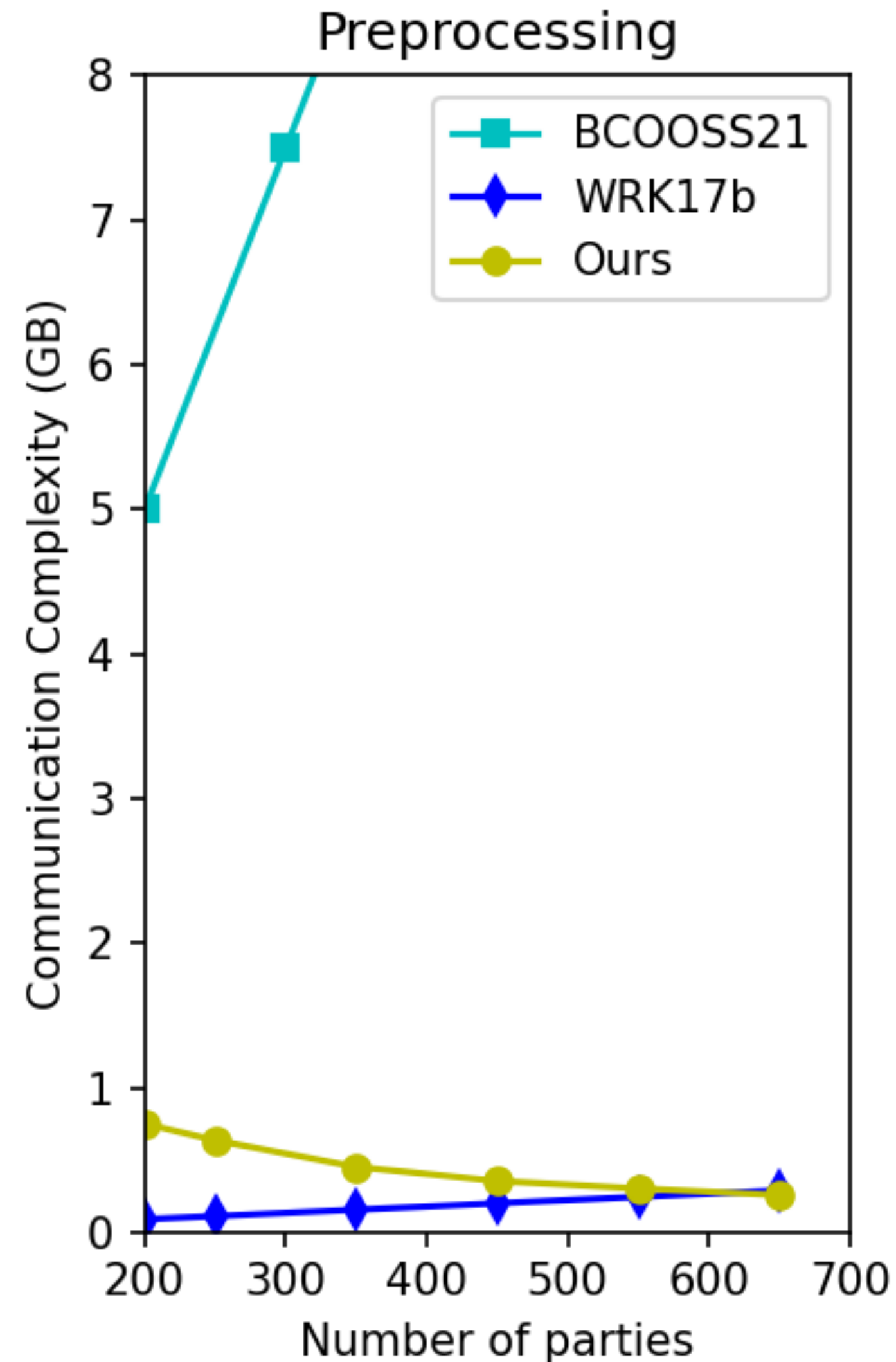
$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

$\text{Eval}(\mathcal{G}, \{X_{\text{inp}}\})$

Agenda

- Structure Of Garbled Circuit
- Multiparty Garbling Protocol
- Efficiency Analysis

Communication Cost Estimates For Evaluating AES-128



Maliciously
secure protocol

Per party costs

$$t \approx \frac{n}{4}$$

$$\ell \approx \frac{n}{4}$$

Bottlenecks To Concrete Efficiency

Semi-honest Protocol

- AES-128
- 8 threads per party
- $n = 250$
- $t = 50$
- $\ell = 50$

Pre-processing Phase

Total

Computation

1.94 min

Communication

240 MB

Bottlenecks To Concrete Efficiency

Semi-honest Protocol

- AES-128
- 8 threads per party
- $n = 250$
- $t = 50$
- $\ell = 50$

		Computation	Communication
Pre-processing Phase	Total	1.94 min	240 MB
	Error Generation	1.73 min	196 MB

Bottlenecks To Concrete Efficiency

Semi-honest Protocol

- AES-128
- 8 threads per party
- $n = 250$
- $t = 50$
- $\ell = 50$

Pre-processing Phase

	Computation	Communication
Total	1.94 min	240 MB
Error Generation	1.73 min	196 MB

Low rate of binary error correcting codes.

Bottlenecks To Concrete Efficiency

Semi-honest Protocol

- AES-128
- 8 threads per party
- $n = 250$
- $t = 50$
- $\ell = 50$

	Computation	Communication
Pre-processing Phase		
Total	1.94 min	240 MB
Error Generation	1.73 min	196 MB
Garbling Phase		
Total	1.64 min	43 MB

Bottlenecks To Concrete Efficiency

Semi-honest Protocol

- AES-128
- 8 threads per party
- $n = 250$
- $t = 50$
- $\ell = 50$

	Computation	Communication
Pre-processing Phase		
Total	1.94 min	240 MB
Error Generation	1.73 min	196 MB
Garbling Phase		
Total	1.64 min	43 MB
Re-aligning Secrets	27.66 sec	28 MB

Future Directions

- Improving concrete efficiency
 - Concretely efficient protocol for error generation.
 - Incorporating existing GC optimizations.
- Constructing $O(|C|)$ communication multiparty garbling from weaker assumptions.

Thank you



ia.cr/2023/099